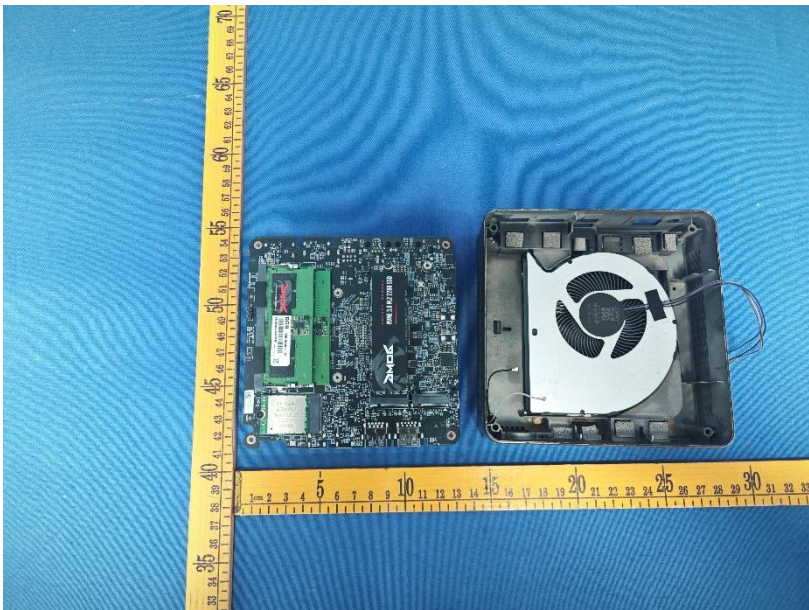
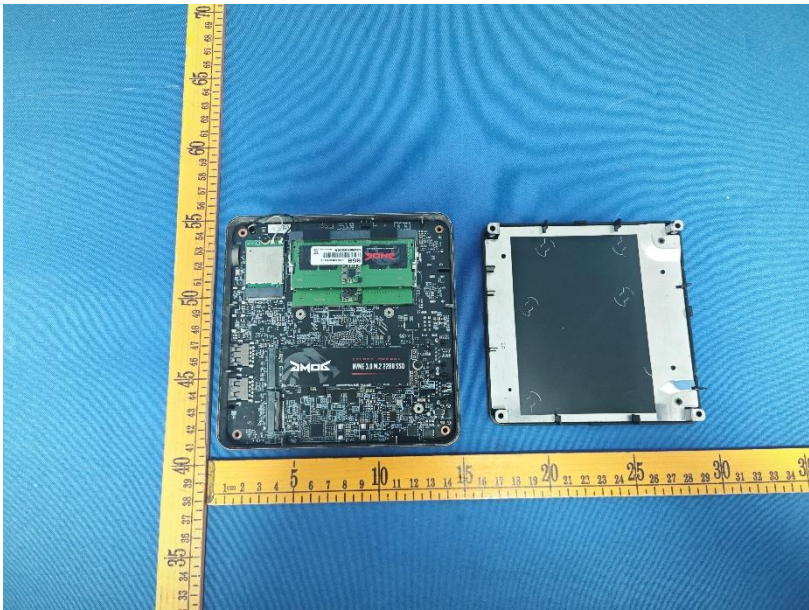
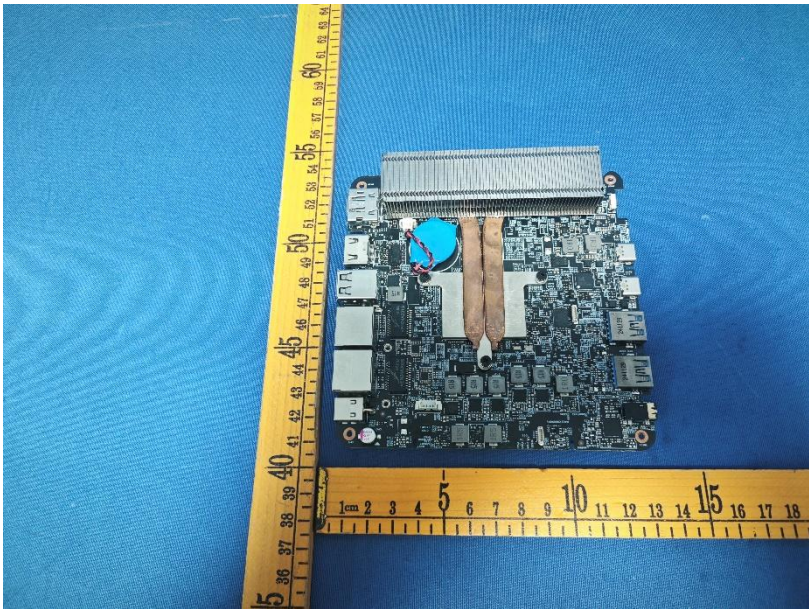
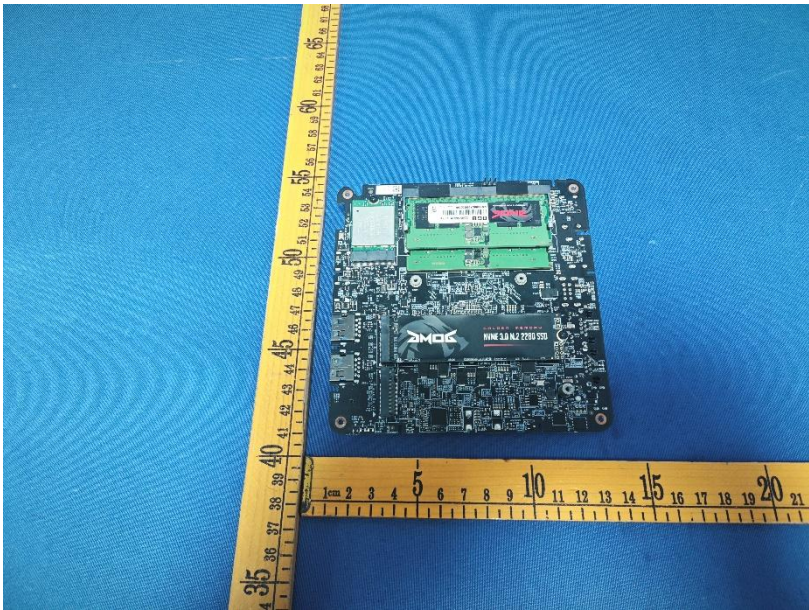
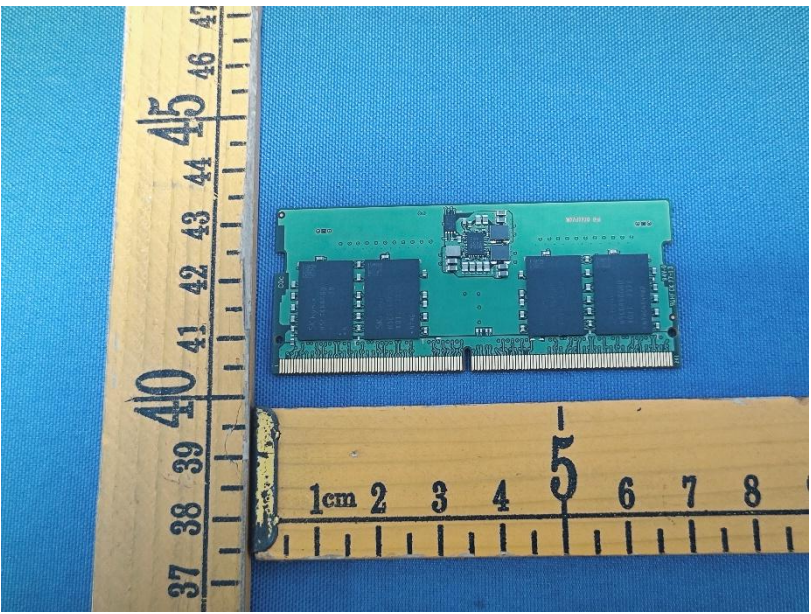
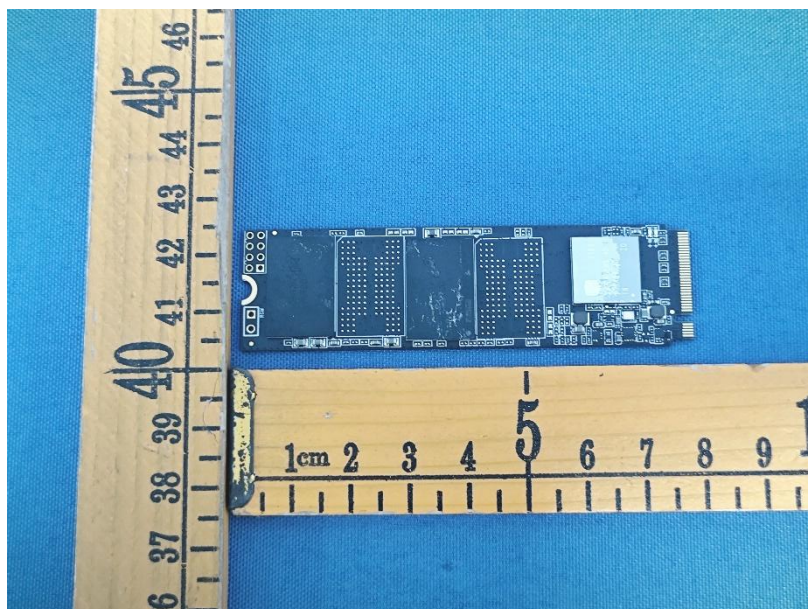
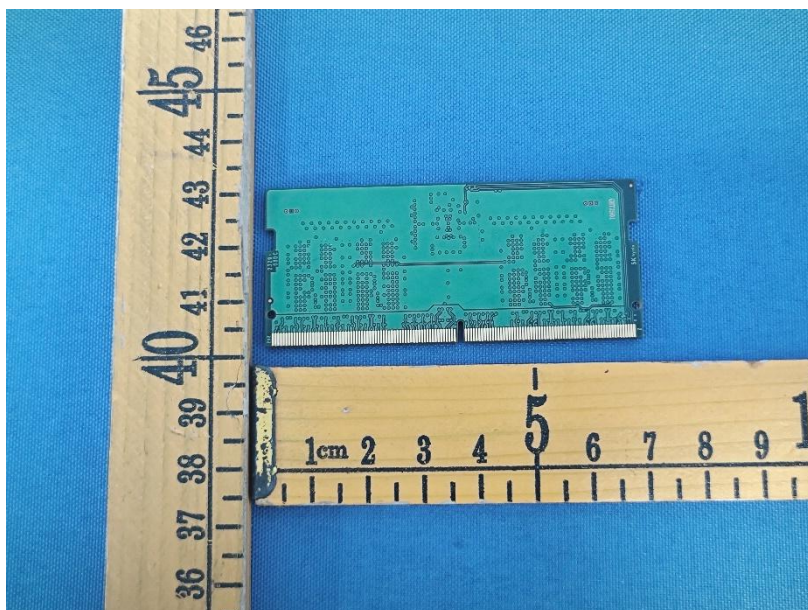
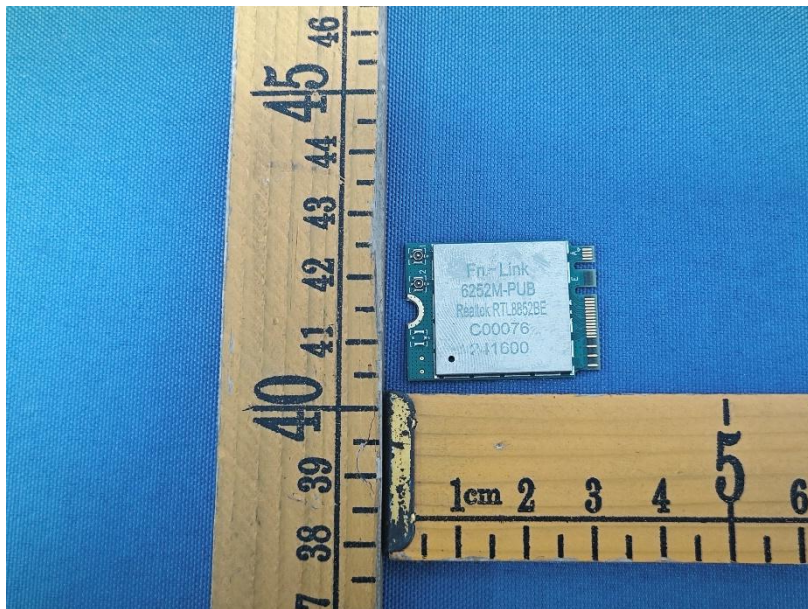


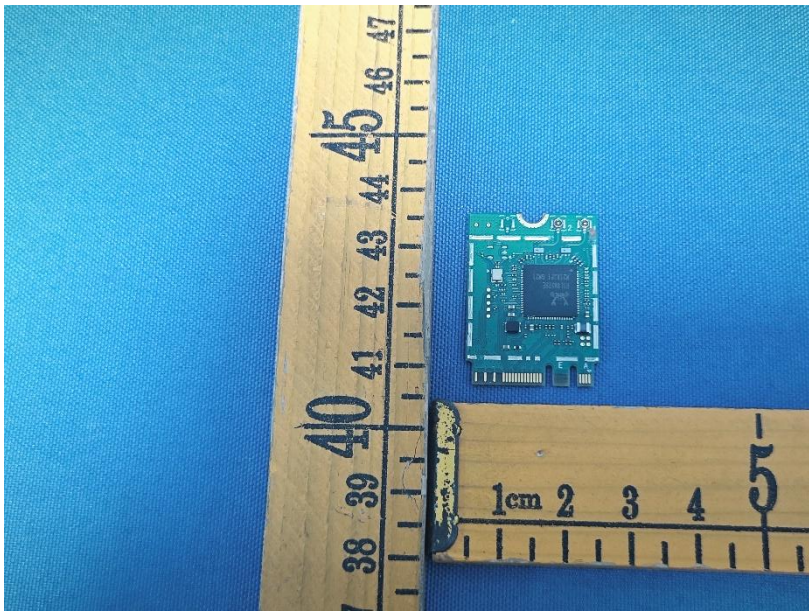
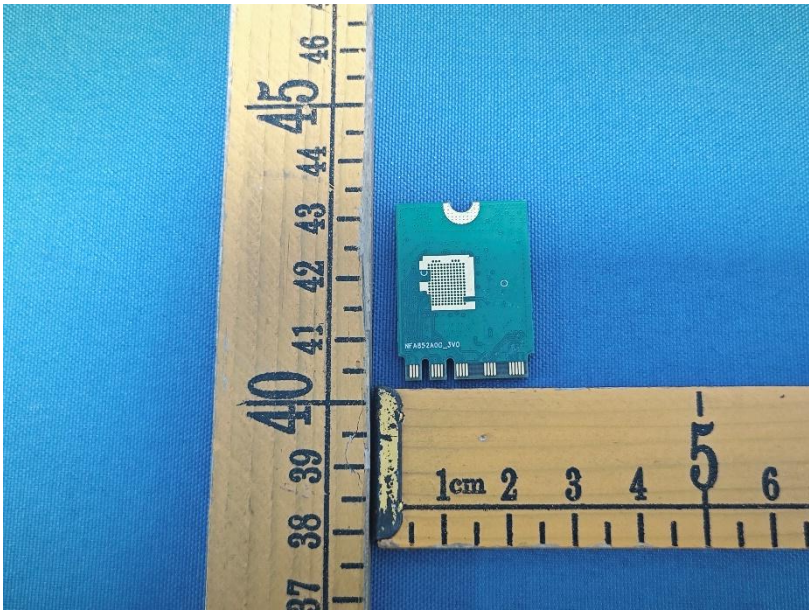


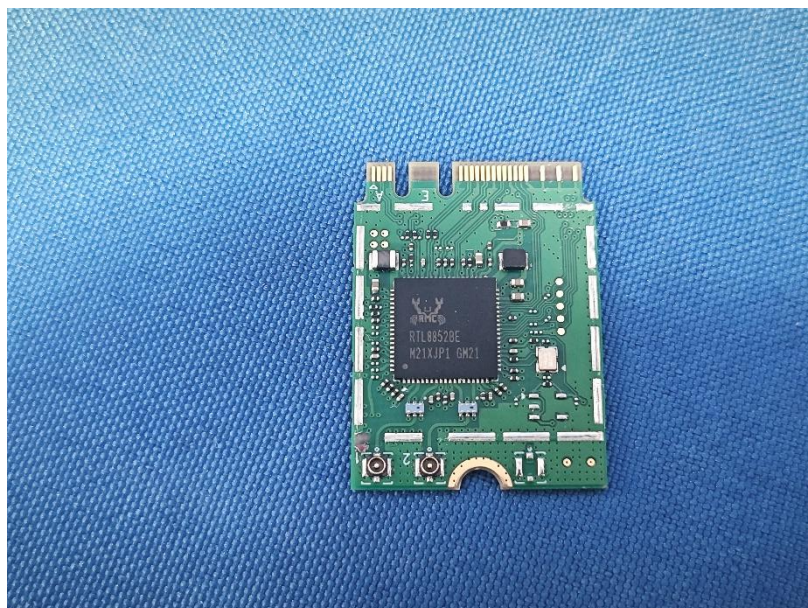


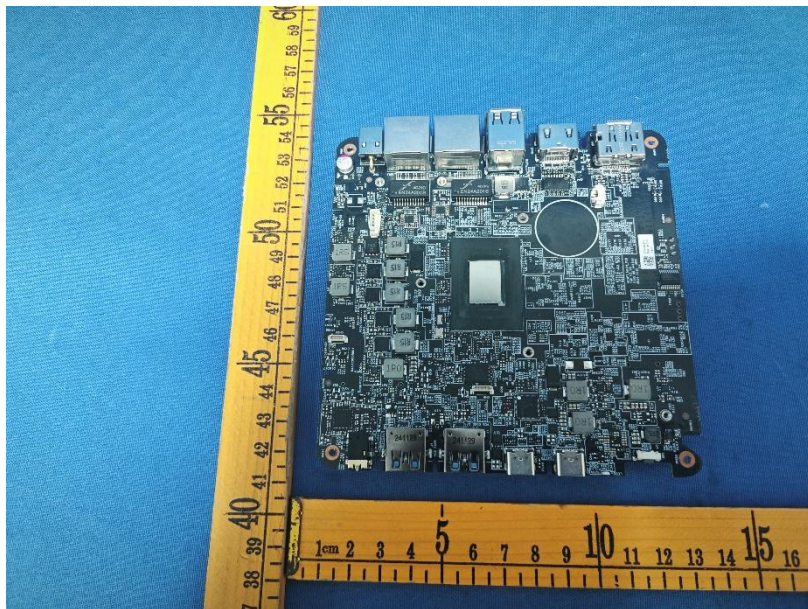
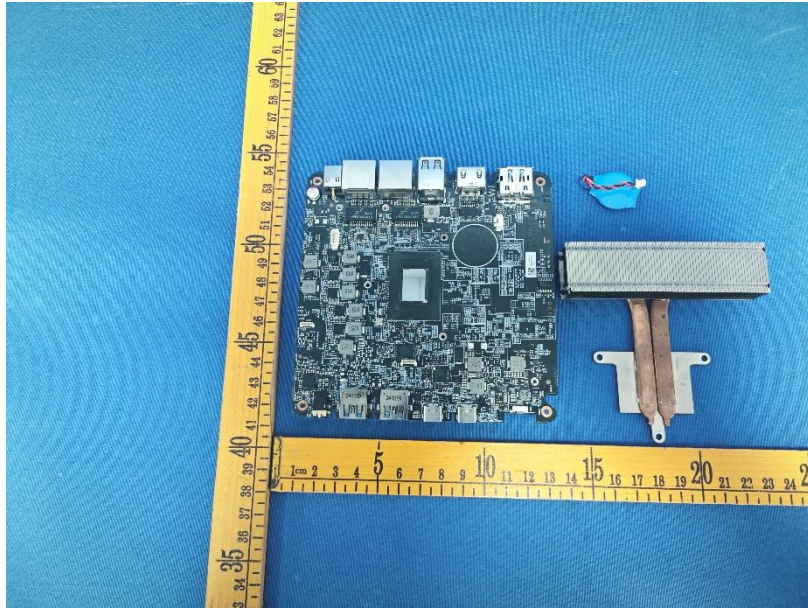


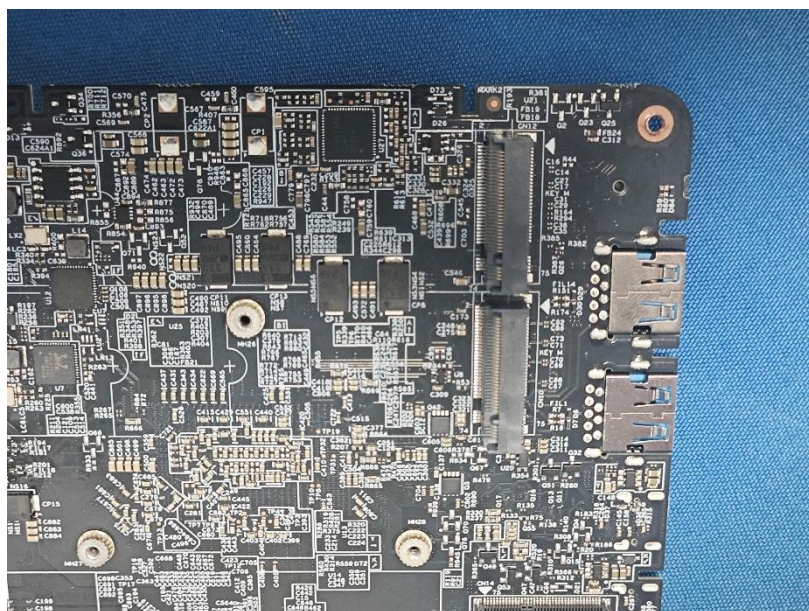
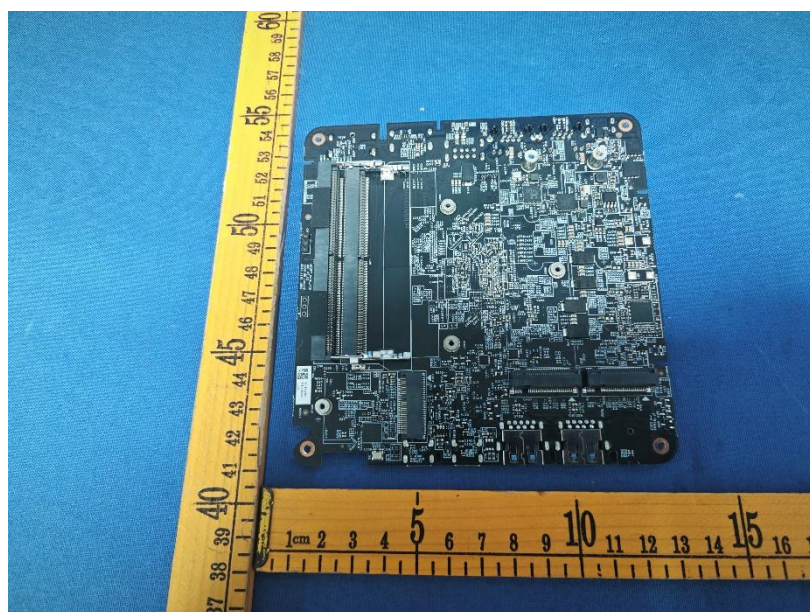


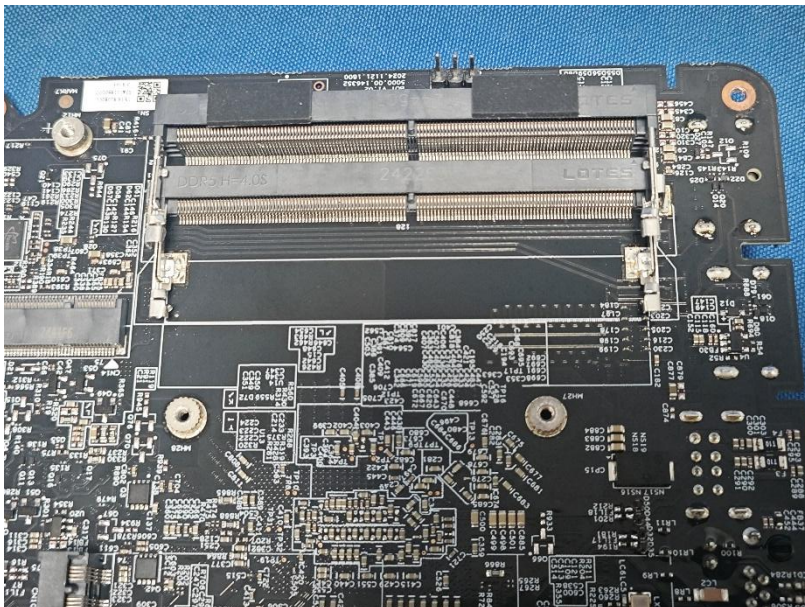
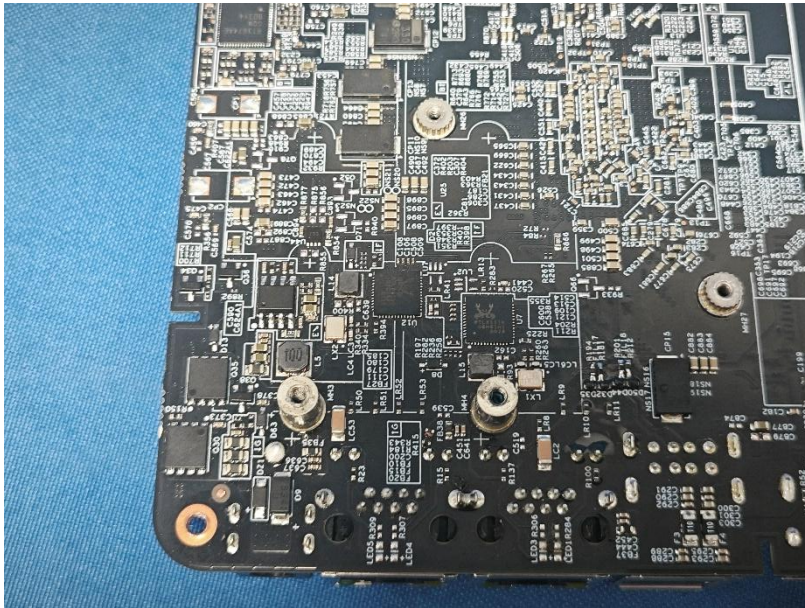


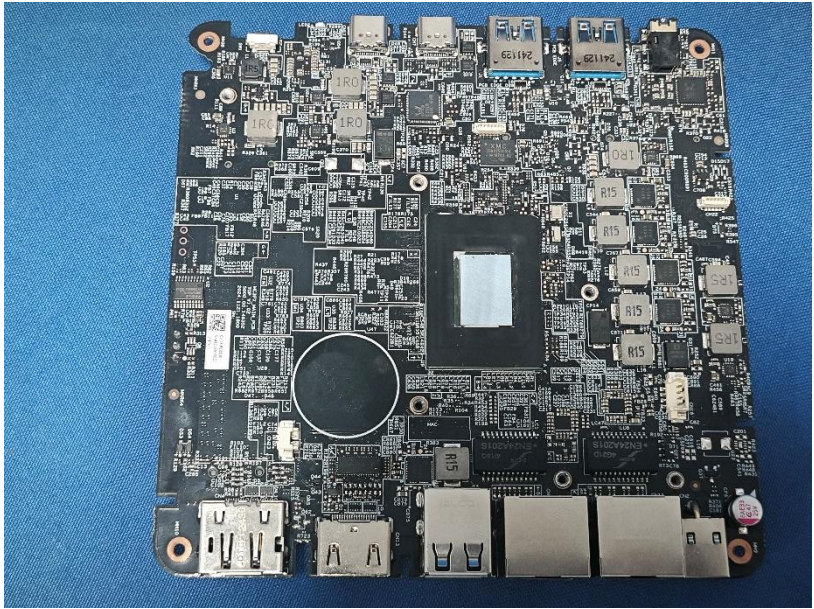
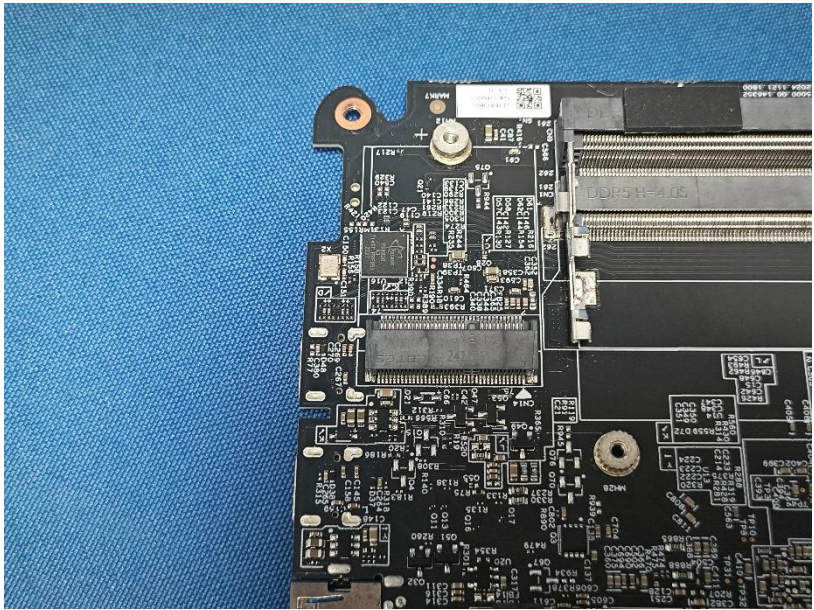


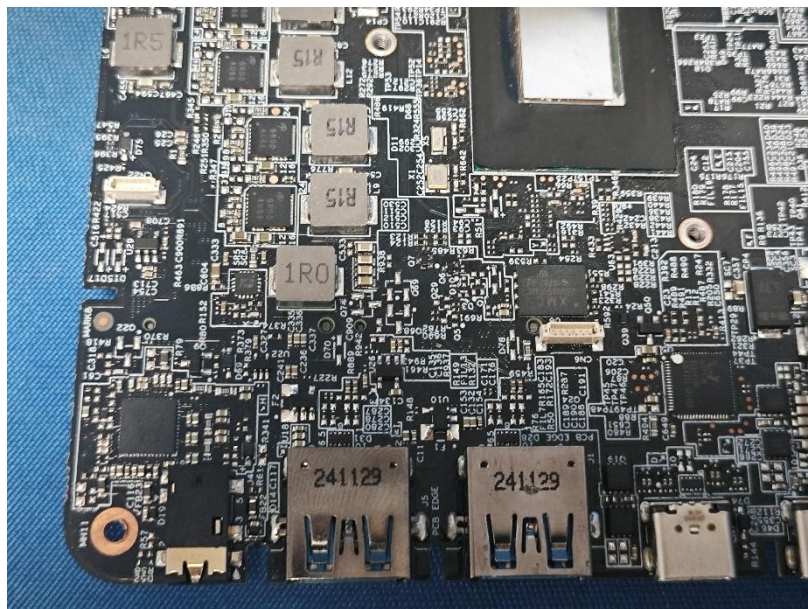
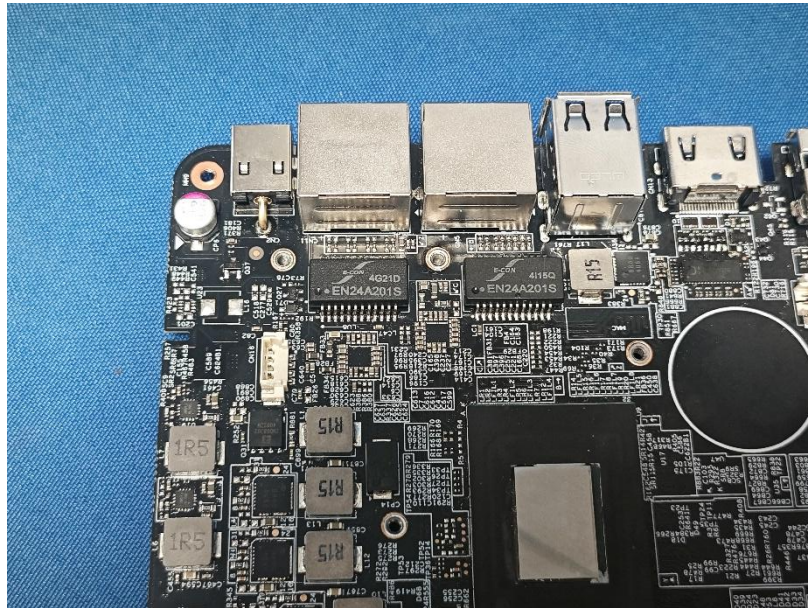


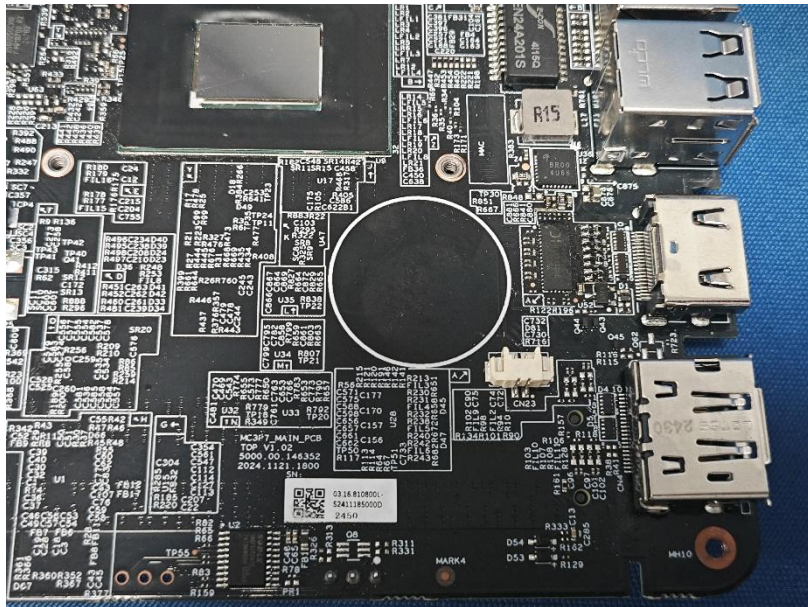
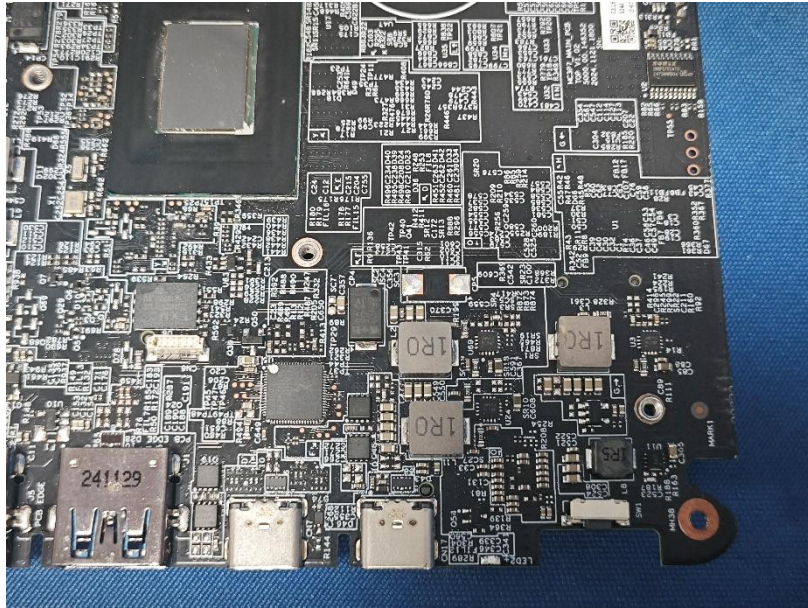




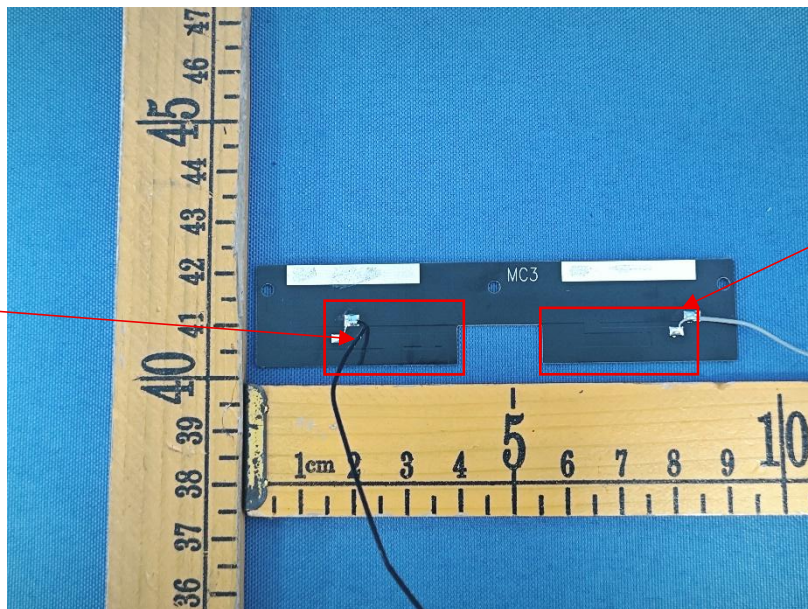
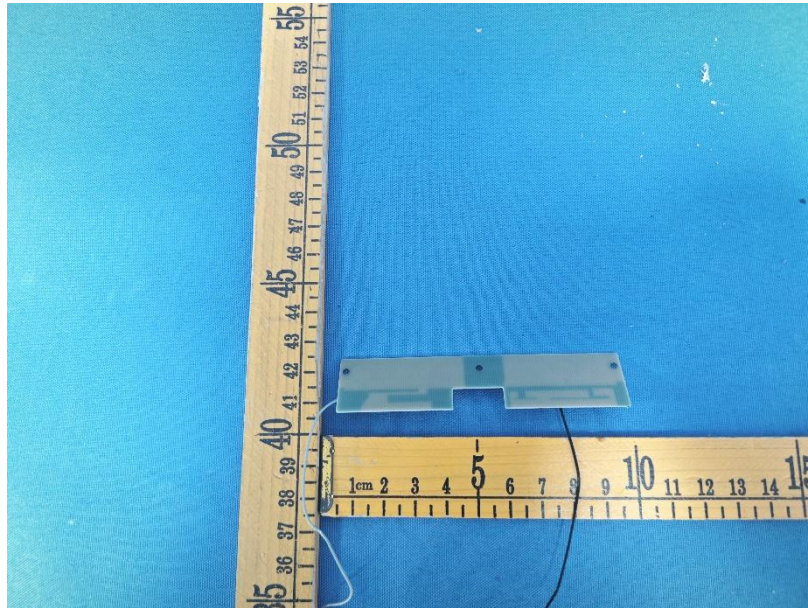












AUX(Ant2 BT/WALN)

Main(Ant1 WALN)