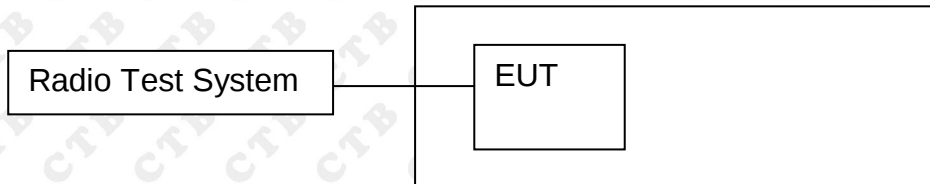


8DPSK/LCH	Agilent Spectrum Analyzer - Swept SA Marker 1 Δ 1.000000000 MHz Ref Offset 6.95 dB Ref 20.00 dBm Center 2.402500 GHz #Res BW 30 kHz Span 2.000 MHz Sweep 2.133 ms (1001 pts) 1Δ2 1.000 MHz (Δ) -0.422 dB 2.401 646 GHz -4.705 dBm	
8DPSK /MCH	Agilent Spectrum Analyzer - Swept SA Marker 1 Δ 1.002000000 MHz Ref Offset 6.96 dB Ref 20.00 dBm Center 2.441500 GHz #Res BW 30 kHz Span 2.000 MHz Sweep 2.133 ms (1001 pts) 1Δ2 1.002 MHz (Δ) 1.401 dB 2.441 002 GHz -5.862 dBm	
8DPSK /HCH	Agilent Spectrum Analyzer - Swept SA Marker 1 Δ 1.000000000 MHz Ref Offset 6.98 dB Ref 20.00 dBm Center 2.479500 GHz #Res BW 30 kHz Span 2.000 MHz Sweep 2.133 ms (1001 pts) 1Δ2 1.000 MHz (Δ) -2.672 dB 2.479 016 GHz -4.281 dBm	

12. HOPPING CHANNEL NUMBER

12.1 Block Diagram Of Test Setup



12.2 Limit

Frequency hopping systems in the 2400-2483.5 MHz band shall use at least 15 channels.

12.3 Test procedure

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set the spectrum analyzer: RBW = 100kHz. VBW = 300kHz. Sweep = auto; Detector Function = Peak. Trace = Max hold.
3. Allow the trace to stabilize. It may prove necessary to break the span up to sections. in order to clearly show all of the hopping frequencies. The limit is specified in one of the subparagraphs of this Section.
4. Set the spectrum analyzer: Start Frequency = 2.4GHz, Stop Frequency = 2.4835GHz. Sweep=auto;

12.4 Test Result

ANT1

Mode	Channel.	Number of Hopping Channel	Verdict
GFSK	Hop	79	PASS
$\pi/4$ DQPSK	Hop	79	PASS
8DPSK	Hop	79	PASS

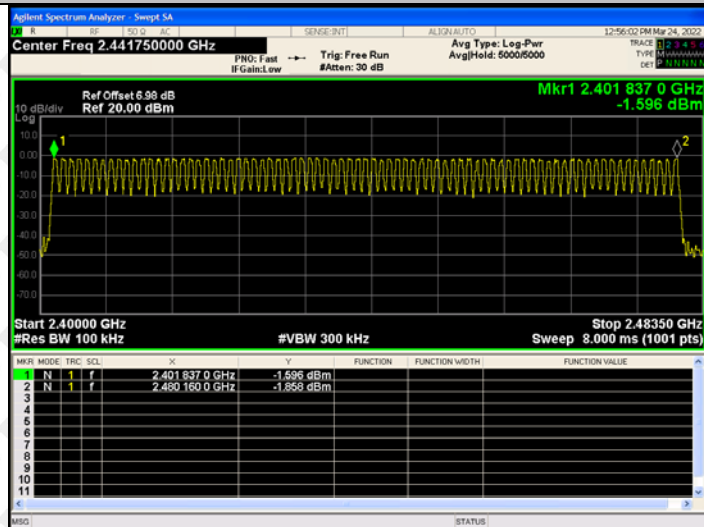
ANT2

Mode	Channel.	Number of Hopping Channel	Verdict
GFSK	Hop	79	PASS
$\pi/4$ DQPSK	Hop	79	PASS
8DPSK	Hop	79	PASS

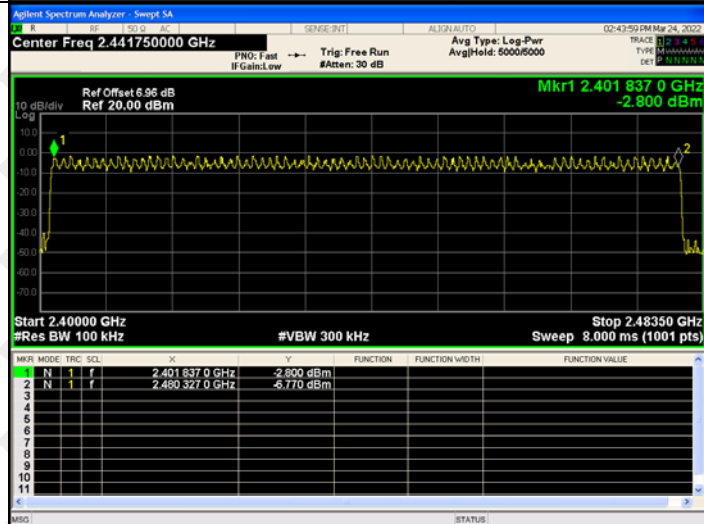
Test Graph ANT1

Graphs

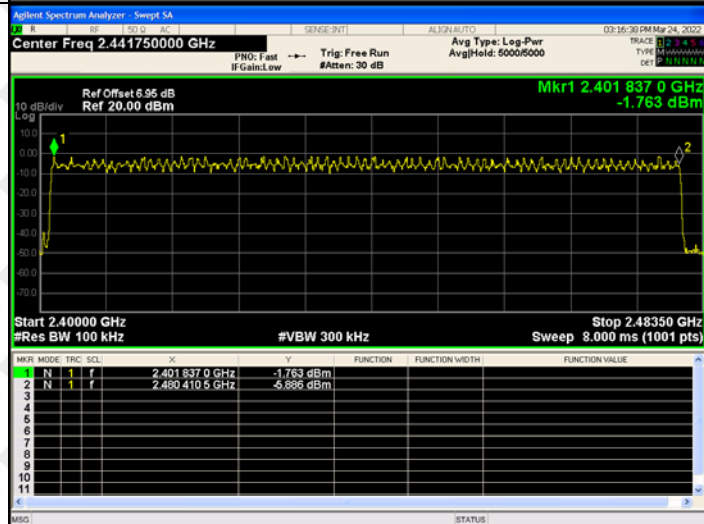
GFSK/Hop



$\pi/4$ DQPSK/Hop



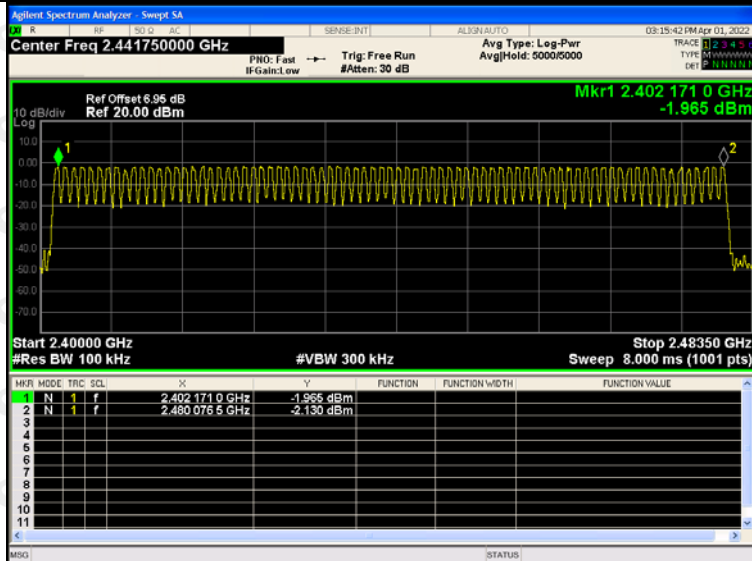
8DPSK/Hop



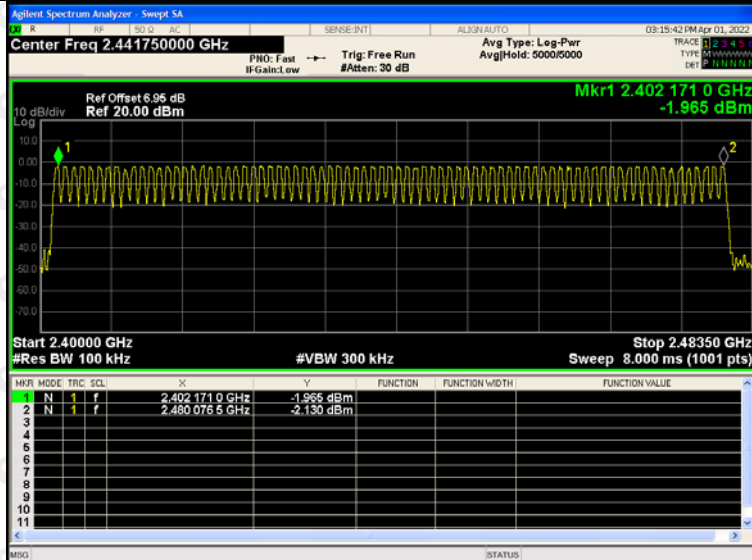
ANT2

Graphs

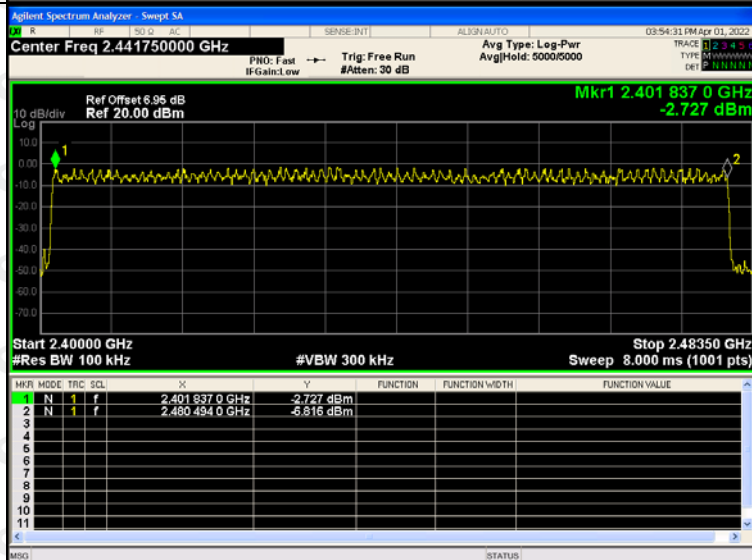
GFSK/Hop



$\pi/4$ DQPSK/Hop

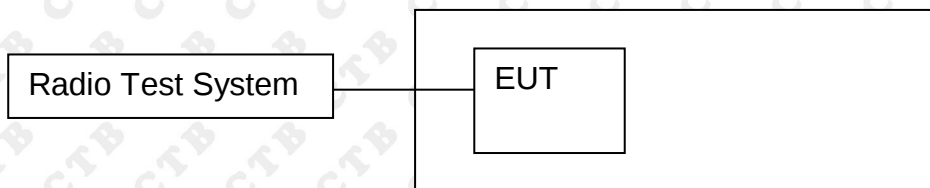


8DPSK/Hop



13. DWELL TIME

13.1 Block Diagram Of Test Setup



13.2 Limit

Frequency hopping systems in the 2400-2483.5 MHz band shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

13.3 Test procedure

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set spectrum analyzer span = 0. Centred on a hopping channel;
3. Set RBW = 1MHz and VBW = 3MHz. Sweep = as necessary to capture the entire dwell time per hopping channel. Set the EUT for DH5, DH3 and DH1 packet transmitting.
4. Use the marker-delta function to determine the dwell time. If this value varies with different modes of operation (e.g., data rate, modulation format, etc.), repeat this test for each variation. The limit is specified in one of the subparagraphs of this Section. Submit this plot(s).

13.4 Test Result

ANT1

Mode	Packet	Channel	Pulse Time (ms)	Total Dwell Time (ms)	Limit (ms)	Verdict
GFSK	DH1	LCH	0.376	120.32	400	PASS
	DH1	MCH	0.376	120.32	400	PASS
	DH1	HCH	0.376	120.32	400	PASS
	DH3	LCH	1.638	262.08	400	PASS
	DH3	MCH	1.638	262.08	400	PASS
	DH3	HCH	1.638	262.08	400	PASS
	DH5	LCH	2.888	308.053	400	PASS
	DH5	MCH	2.888	308.053	400	PASS
	DH5	HCH	2.888	308.053	400	PASS

ANT2

Mode	Packet	Channel	Pulse Time (ms)	Total Dwell Time (ms)	Limit (ms)	Verdict
GFSK	DH1	LCH	0.376	120.32	400	PASS
	DH1	MCH	0.376	120.32	400	PASS
	DH1	HCH	0.376	120.32	400	PASS
	DH3	LCH	1.638	262.08	400	PASS
	DH3	MCH	1.638	262.08	400	PASS
	DH3	HCH	1.638	262.08	400	PASS
	DH5	LCH	2.888	308.053	400	PASS
	DH5	MCH	2.888	308.053	400	PASS
	DH5	HCH	2.888	308.053	400	PASS

Remark: DH5 Packet permit maximum 1600 / 79 / 6 hops per second in each channel (5 time slots RX, 1 time slot TX).

DH3 Packet permit maximum 1600 / 79 / 4 hops per second in each channel (3 time slots RX, 1 time slot TX).

DH1 Packet permit maximum 1600 / 79 / 2 hops per second in each channel (1 time slot RX, 1 time slot TX). So, the Dwell Time can be calculated as follows:

DH5: $1600/79/6*0.4*79*(MkrDelta)/1000$

DH3: $1600/79/4*0.4*79*(MkrDelta)/1000$

DH1: $1600/79/2*0.4*79*(MkrDelta)/1000$

Remark: Mkr Delta is once pulse time.

Graphs

Agilent Spectrum Analyzer - Sweep 5A

10 MHz 100 dBm

Center Freq 2.402000000 GHz

Trig Delay: 500.0 μ s

Trig: Video

Avg Type: Log-Pwr

PNO: Fast

IF Gain: 4.0

Ref Offset: 6.95 dB

Ref: 20.00 dBm

Span 0 Hz

Res BW 1.0 MHz

#VBW 3.0 MHz

Sweep 1.000 ms

(10001 pts)

12:40:10 PM Mar 24, 2012

Trace: 3

Type: WWWWWWWW

Det: SRRN

10 dB/div

0.00

10.0

20.0

30.0

40.0

50.0

60.0

70.0

102

376.0 μ s

4.95 dB

X₂

Table:

MNR	MODE	TRC	SCL	X	Y	FUNCTION	WIDTH	FUNCTION VALUE
1	Δ 2	1	t	(Δ)	376.0 μ s	(Δ)	4.95 dB	
3	F	1	t		499.8 μ s	-10.42 dBm		
4								
5								
6								
7								
8								
9								
10								
11								

Agilent Spectrum Analyzer - Sweep SA

12:52:16.58 Mar 24, 2022

Center Freq 2.441000000 GHz

Trace 1: Freq 3.000 MHz

TYPE: WAVEFORM

DET: ENVELOPE

SENSE: RFL

ALGN: AUTO

Avg Type: Log-Pwr

PNO: Fast

IF Gain: Low

Trig Delay: 500.0 μ s

Trig: Video

#Atten: 30 dB

Ref Offset: 6.96 dB

Ref: 20.00 dBm

Δ Mkr1 376.0 μ s

4.34 dB

Δ 2

10.00 dB

Center 2.441000000 GHz

Res BW 1.0 MHz

#VBW 3.0 MHz

Sweep 1.000 ms

Span 0 Hz

(10001 pts)

MKR	MODE	TRIG	SQL	X	Y	FUNCTION	WIDTH	FUNCTION VALUE
1	Δ 2	1	t	(Δ)	376.0 μ s	(Δ)	4.34 dB	
2	F	1	t		499.8 μ s		-10.00 dBm	
3								
4								
5								
6								
7								
8								
9								
10								
11								

Agilent Spectrum Analyzer - Sweep 5A

Center Freq 2.480000000 GHz

Trig Delay: 500.0 μs

Trig: Video

Attenu: 30 dB

Arg Type: Log-Pwr

Ref Offset 6.98 dB

Ref 20.00 dBm

ΔMkr1 376.0 μs

5.33 dB

Center 2.480000000 GHz

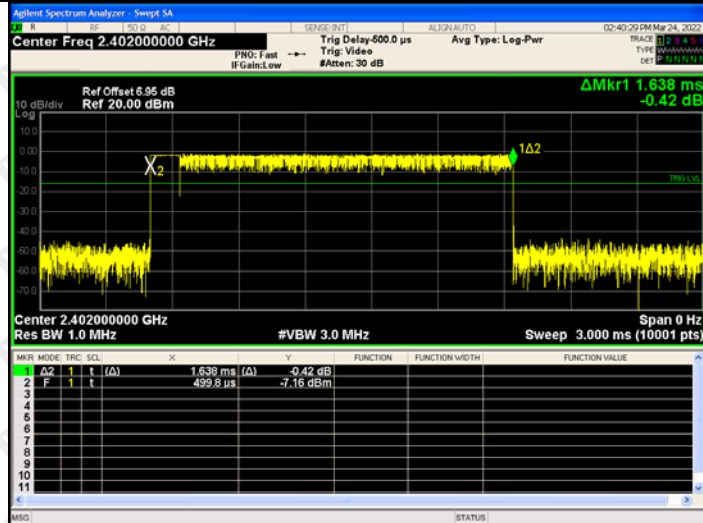
Res BW 1.0 MHz

#VBW 3.0 MHz

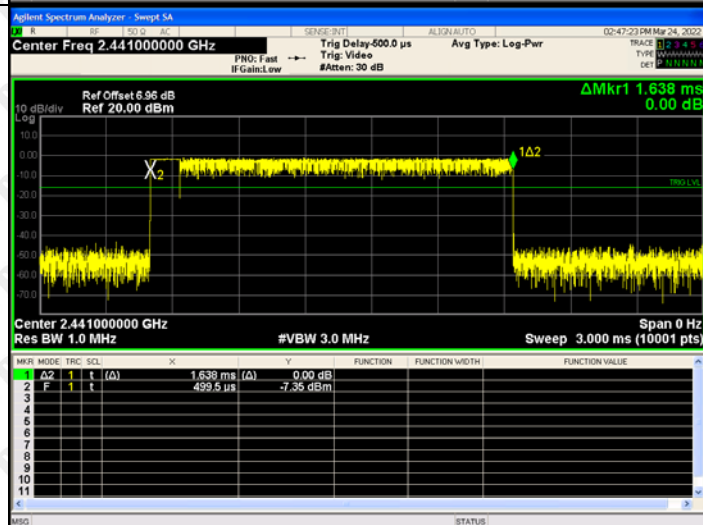
Sweep 1.000 ms (10001 pts)

MKR	MODE	TRIG	SCL	X	Y	FUNCTION	WIDTH	FUNCTION VALUE
1	Δ2	1	t	(Δ)	376.0 μs (Δ)	5.33 dB		
2	F	1	t		499.8 μs	-10.76 dBm		
3								
4								
5								
6								
7								
8								
9								
10								
11								

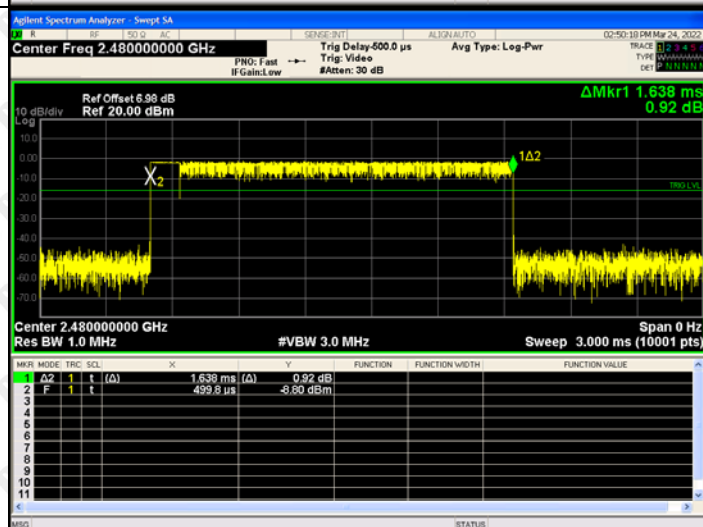
GFSK_DH3/LCH



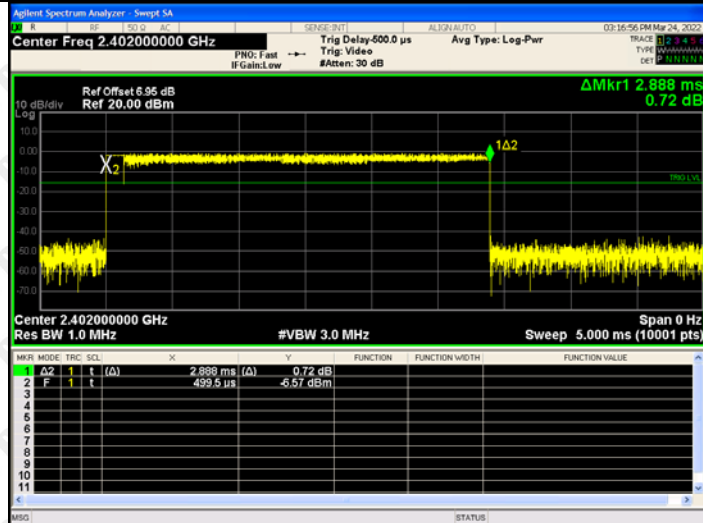
GFSK_DH3/MCH



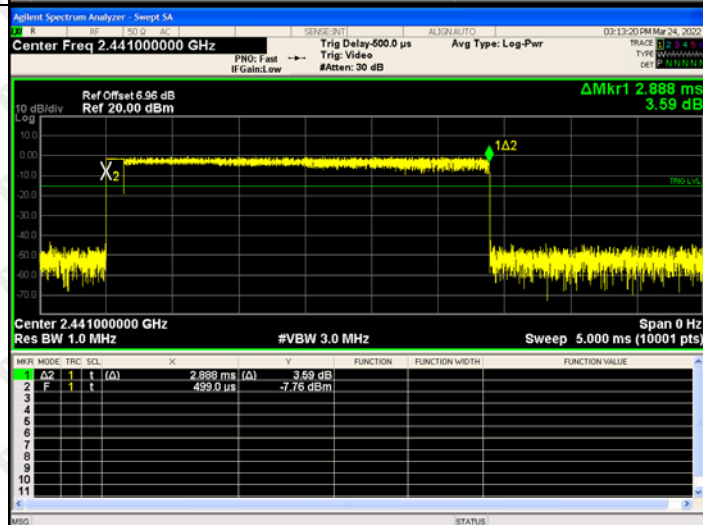
GFSK_DH3/HCH



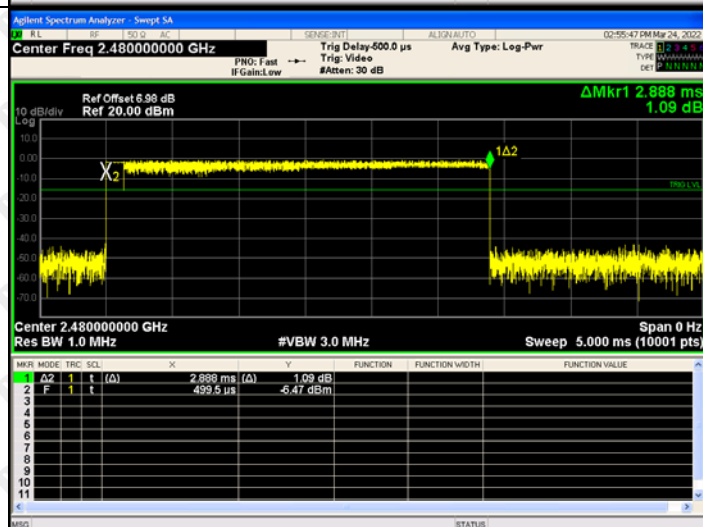
GFSK_DH5/LCH



GFSK_DH5/MCH



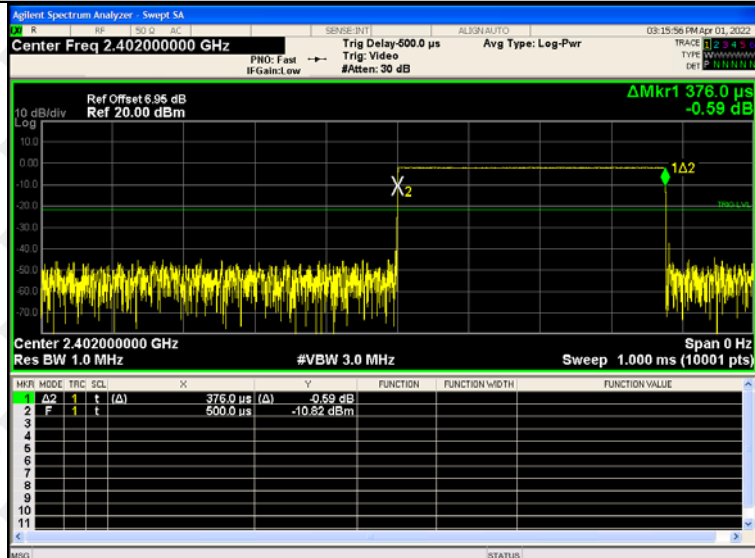
GFSK_DH5/HCH



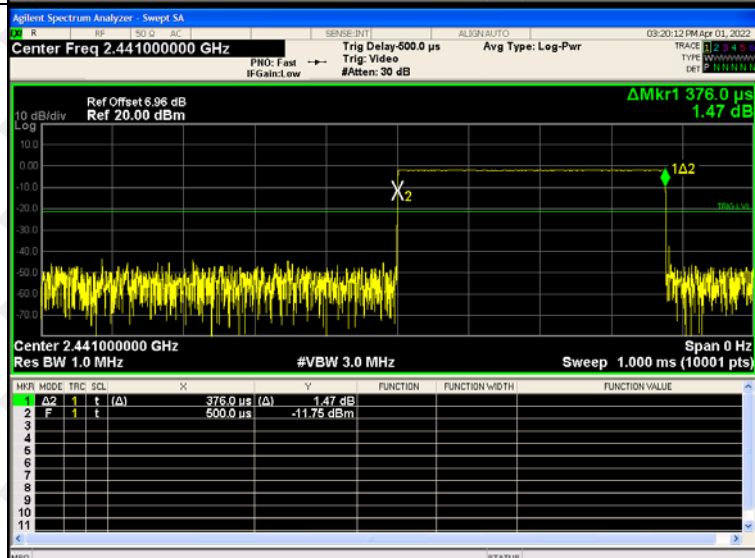
ANT 2

Graphs

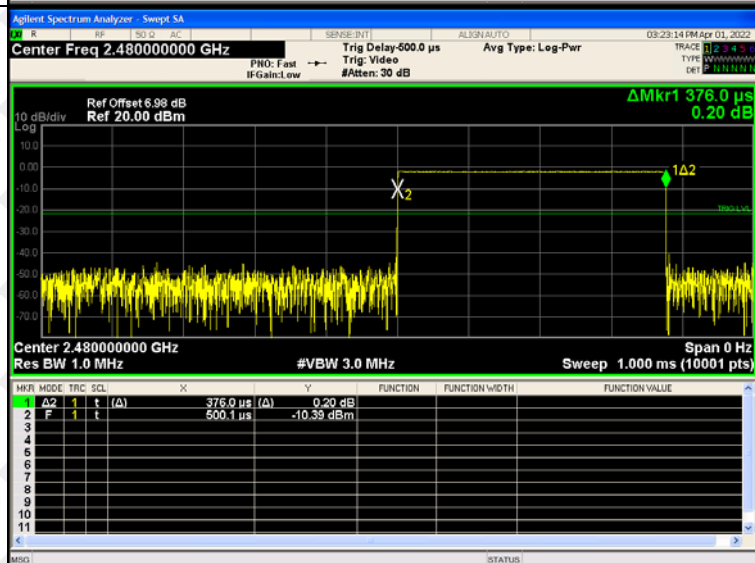
GFSK_DH1/LCH



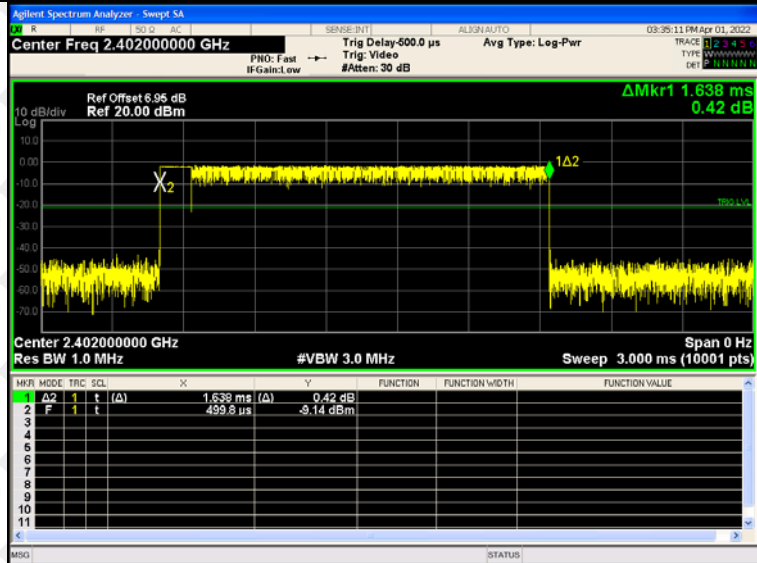
GFSK_DH1/MCH



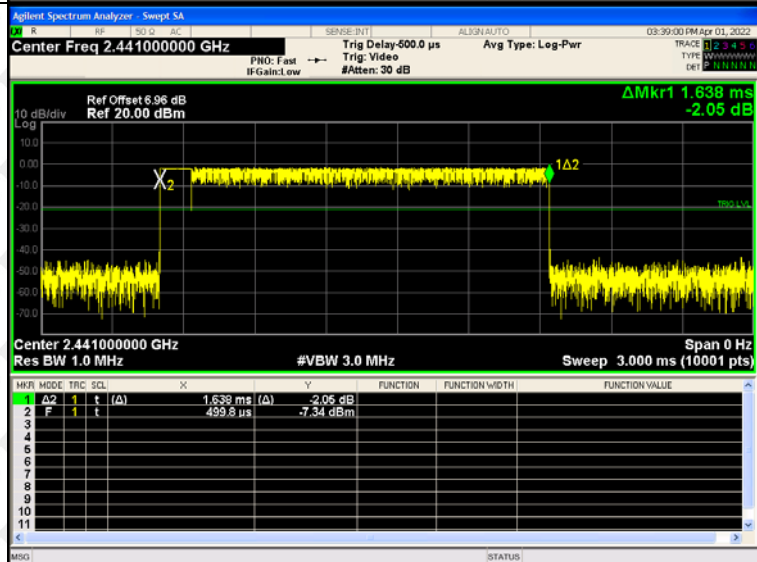
GFSK_DH1/HCH



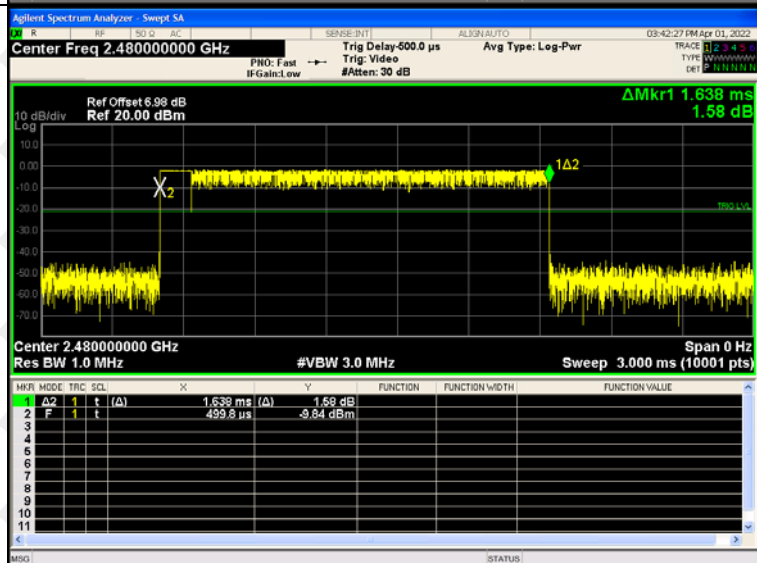
GFSK_DH3/LCH



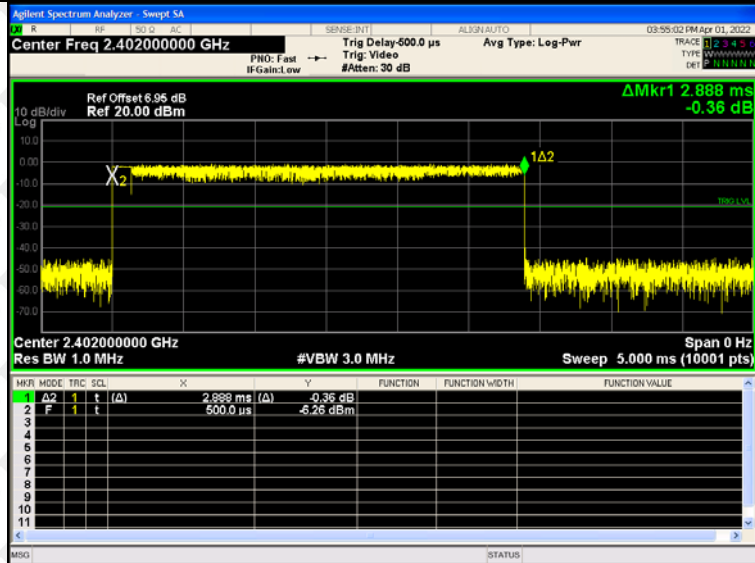
GFSK_DH3/MCH



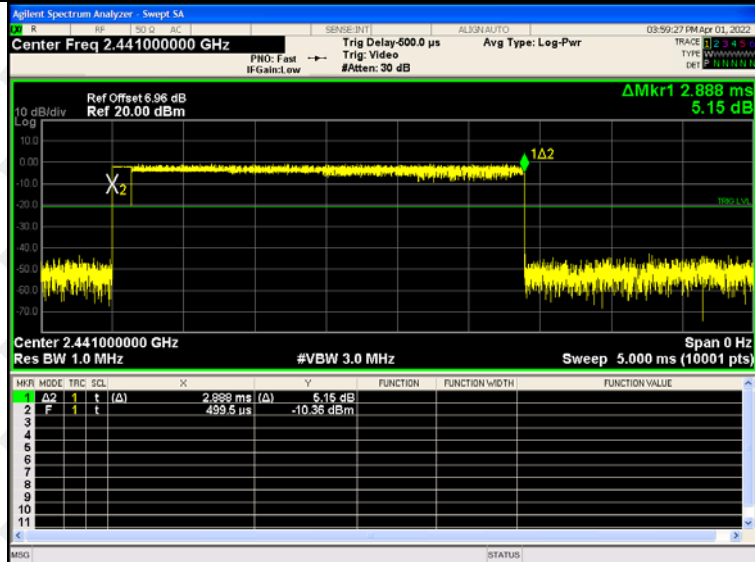
GFSK_DH3/HCH



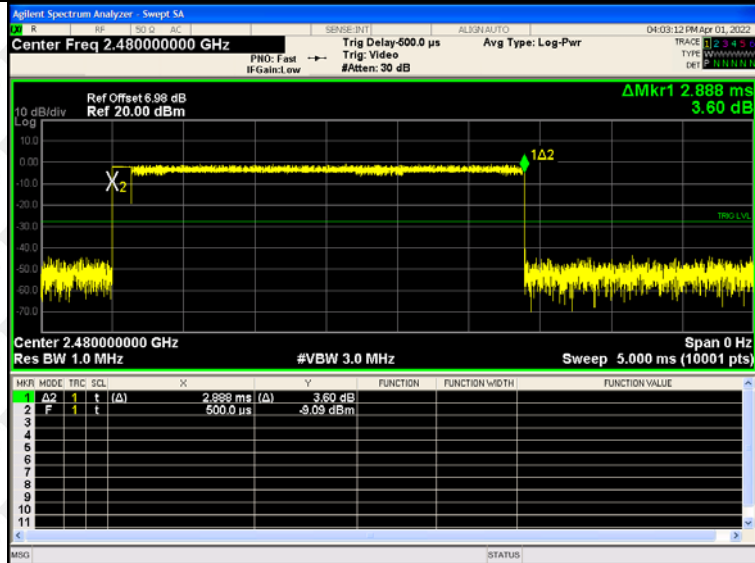
GFSK_DH5/LCH



GFSK_DH5/MCH



GFSK_DH5/HCH



14. PSEUDORANDOM FREQUENCY

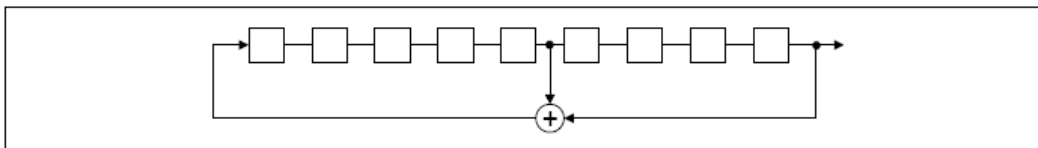
14.1 Limit

Frequency hopping systems shall have hopping channel carrier frequencies separated by a minimum of 25 kHz or the 20 dB bandwidth of the hopping channel, whichever is greater. Alternatively, Frequency hopping systems operating in the 2400-2483.5 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20 dB bandwidth of the hopping channel, whichever is greater, provided the systems operate with an output power no greater than 125 mW. The system shall hop to channel frequencies that are selected at the system hopping rate from a Pseudorandom ordered list of hopping frequencies. Each frequency must be used equally on the average by each transmitter. The system receivers shall have input bandwidths that match the hopping channel bandwidths of their corresponding transmitters and shall shift frequencies in synchronization with the transmitted signals.

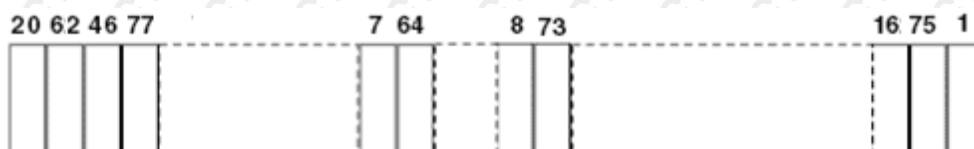
14.2 Test procedure

The pseudorandom sequence may be generated in a nine-stage shift register whose 5th and 9th stage outputs are added in a modulo-two addition stage. And the result is fed back to the input of the first stage. The sequence begins with the first ONE of 9 consecutive ONES; i.e. the shift register is initialized with nine ones.

- Number of shift register stages: 9
- Length of pseudo-random sequence: $2^9 - 1 = 511$ bits
- Longest sequence of zeros: 8 (non-inverted signal)



An example of Pseudorandom Frequency Hopping Sequence as follow:



Each frequency used equally on the average by each transmitter. The system receivers have input bandwidths that match the hopping channel bandwidths of their Corresponding transmitters and shift frequencies in synchronization with the transmitted signals.

14.3 Test Result

The device does not have the ability to be coordinated with other FHSS systems in an effort to avoid the simultaneous occupancy of individual hopping frequencies by multiple transmitters.

15. ANTENNA REQUIREMENT

15.203 requirement:

An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

15.247(b) (4) requirement:

The conducted output power limit specified in paragraph (b) of this section is based on the use of antennas with directional gains that do not exceed 6 dBi. Except as shown in paragraph (c) of this section, if transmitting antennas of directional gain greater than 6 dBi are used, the conducted output power from the intentional radiator shall be reduced below the stated values in paragraphs (b)(1), (b)(2), and (b)(3) of this section, as appropriate, by the amount in dB that the directional gain of the antenna exceeds 6 dBi.

EUT Antenna:

The antenna is PCB antenna. The best case gain of the antenna is 1dBi.

16. EUT PHOTOGRAPHS

EUT Photo 1

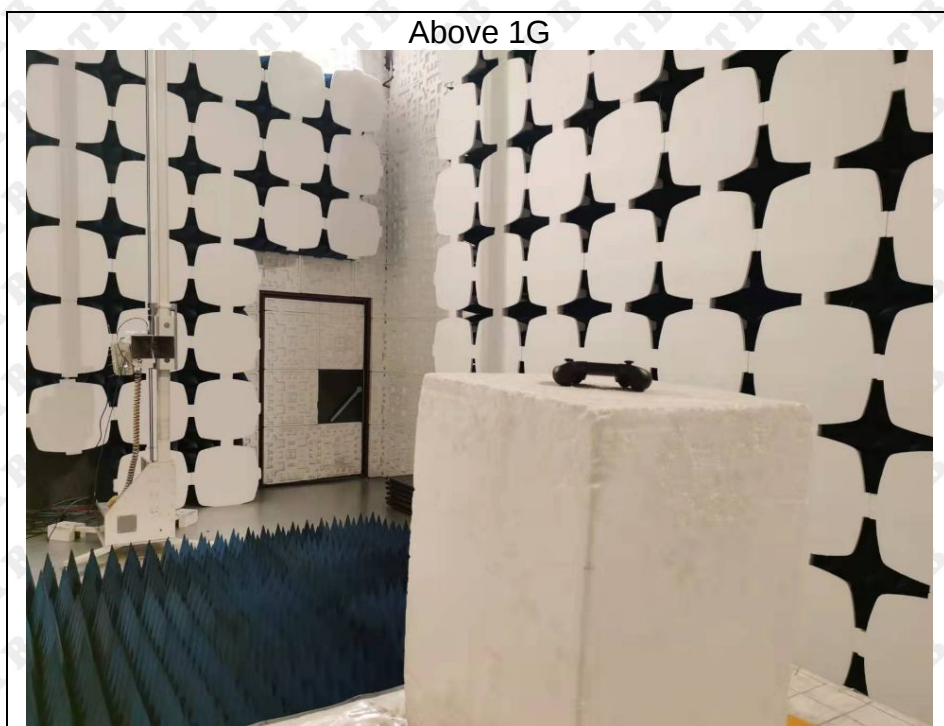
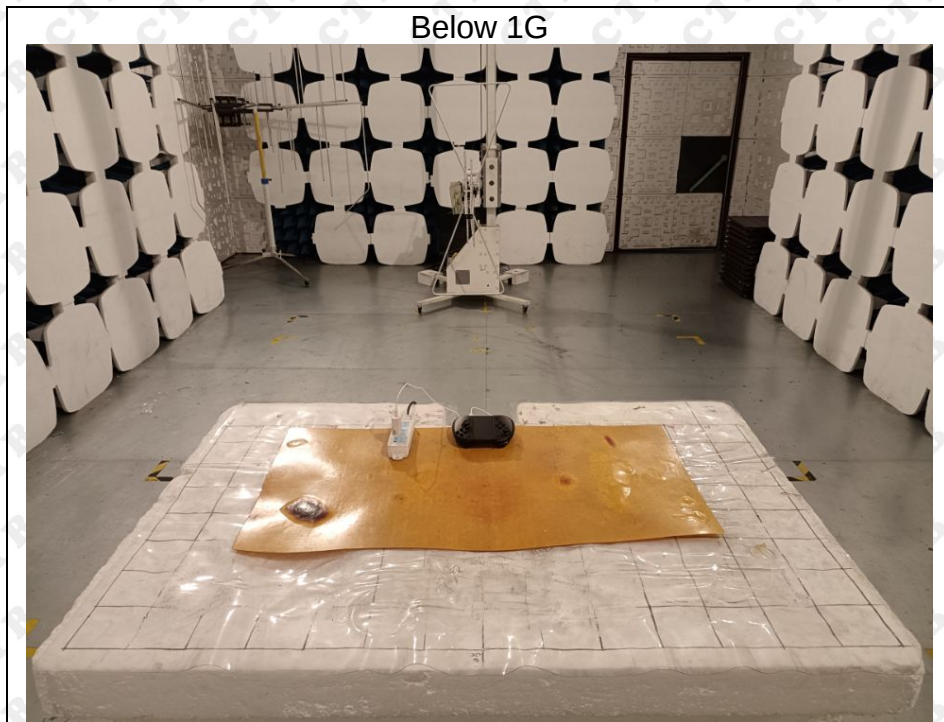


EUT Photo 2



17. EUT TEST SETUP PHOTOGRAPHS

Radiated Emission



Conducted emissions



***** END OF REPORT *****