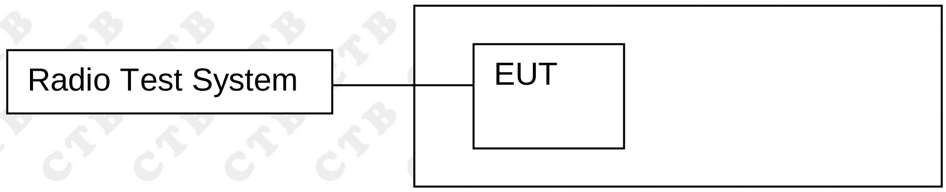
13. DWELL TIME

13.1 Block Diagram Of Test Setup



13.2 Limit

Frequency hopping systems in the 2400-2483.5 MHz band shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

13.3 Test procedure

1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
2. Set spectrum analyzer span = 0. Centred on a hopping channel;
3. Set RBW = 1MHz and VBW = 3MHz.Sweep = as necessary to capture the entire dwell time per hopping channel. Set the EUT for DH5, DH3 and DH1 packet transmitting.
4. Use the marker-delta function to determine the dwell time. If this value varies with different modes of operation (e.g.. data rate. modulation format. etc.). repeat this test for each variation. The limit is specified in one of the subparagraphs of this Section. Submit this plot(s).

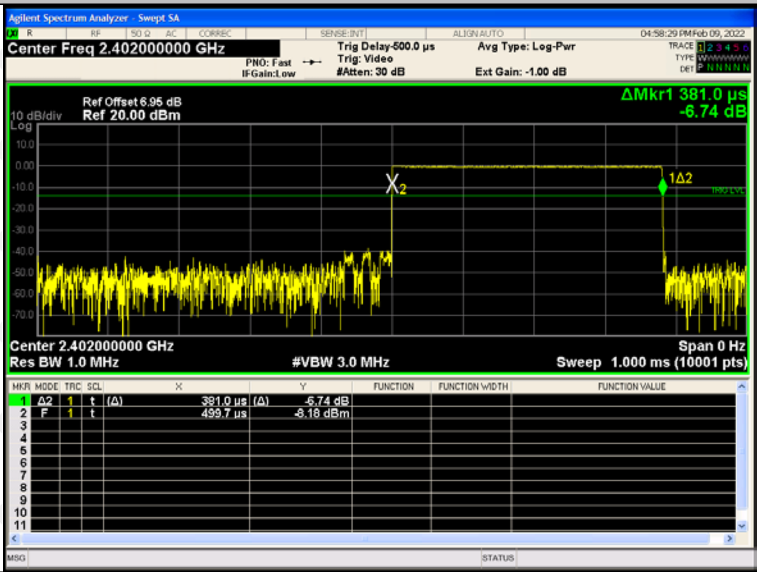
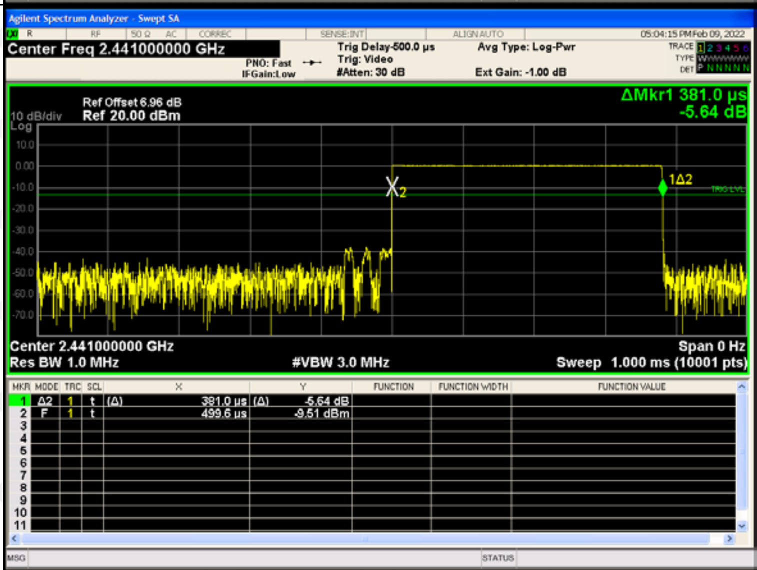
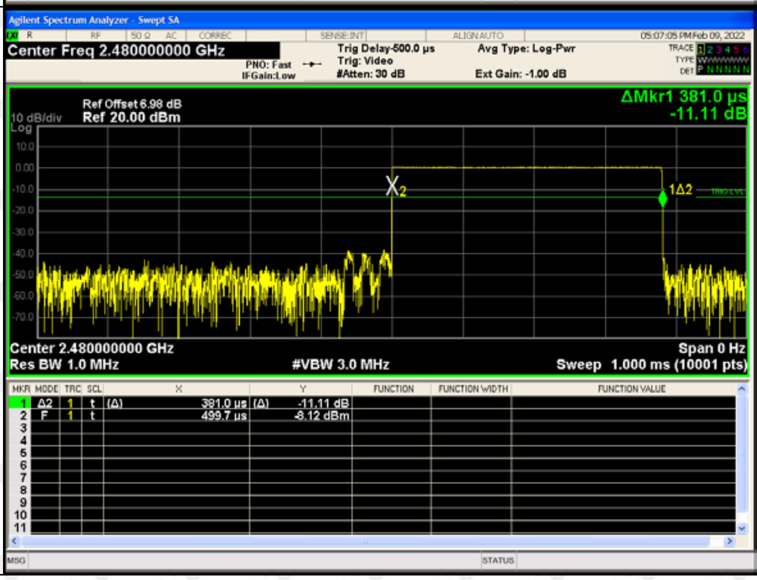
13.4 Test Result

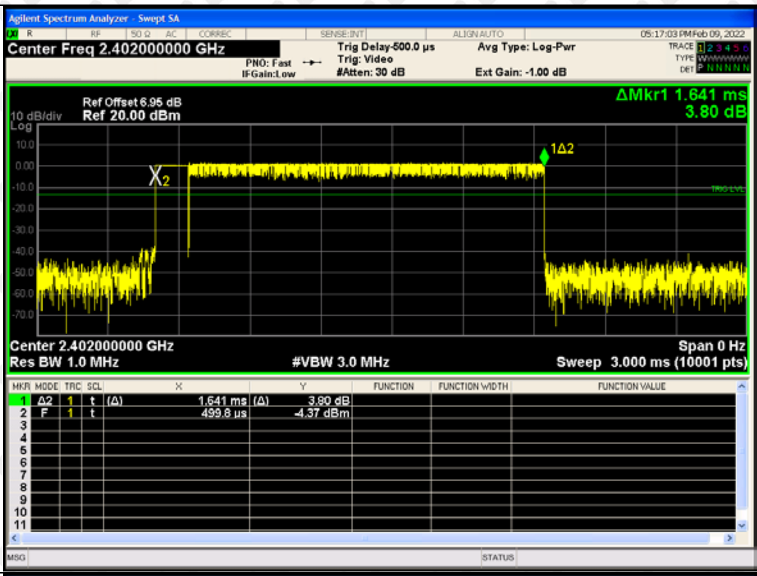
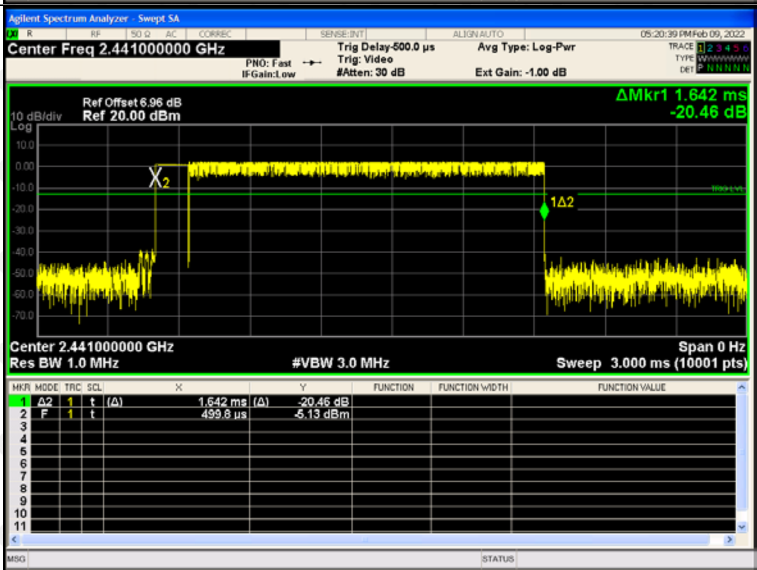
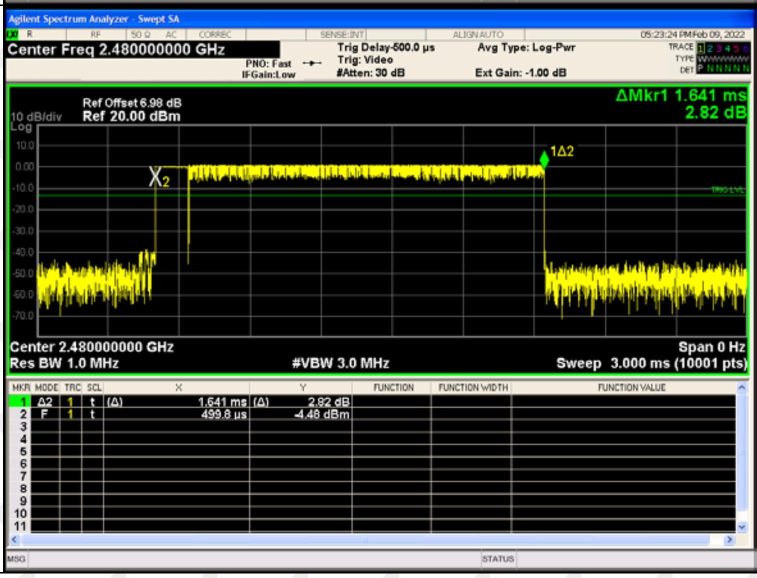
Mode	Packet	Channel	Pulse Time (ms)	Total Dwell Time (ms)	Limit (ms)	Verdict
GFSK	DH1	LCH	0.381	121.92	400	PASS
	DH1	MCH	0.381	121.92	400	PASS
	DH1	HCH	0.381	121.92	400	PASS
	DH3	LCH	1.641	262.56	400	PASS
	DH3	MCH	1.642	262.72	400	PASS
	DH3	HCH	1.641	262.56	400	PASS
	DH5	LCH	2.891	308.373	400	PASS
	DH5	MCH	2.891	308.373	400	PASS
	DH5	HCH	2.891	308.373	400	PASS

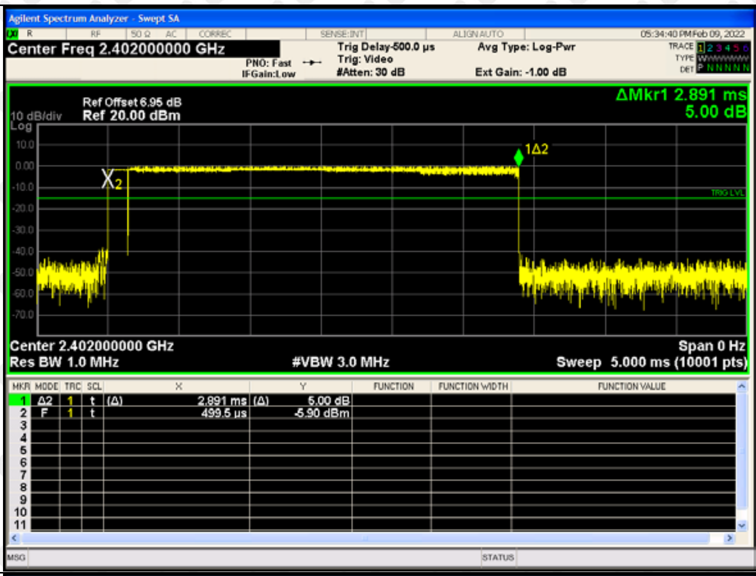
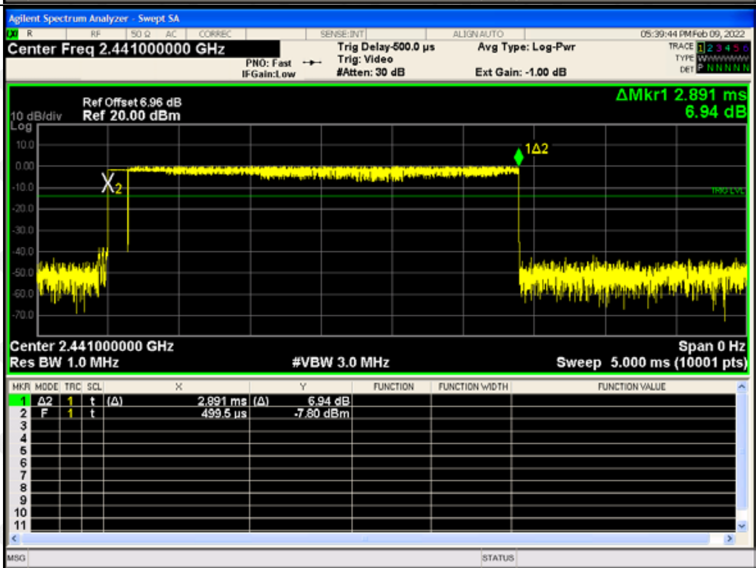
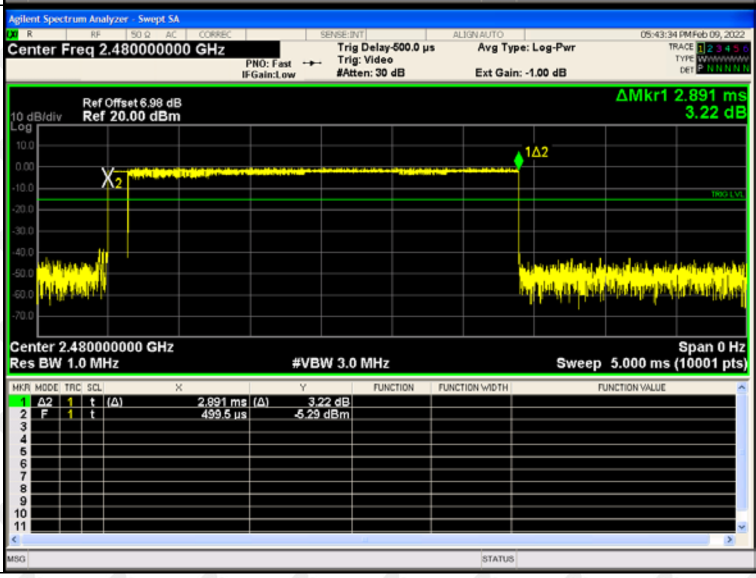
Remark: DH5 Packet permit maximum 1600 / 79 / 6 hops per second in each channel (5 time slots RX, 1 time slot TX).
DH3 Packet permit maximum 1600 / 79 / 4 hops per second in each channel (3 time slots RX, 1 time slot TX).
DH1 Packet permit maximum 1600 / 79 /2 hops per second in each channel (1 time slot RX, 1 time slot TX). So, the Dwell Time can be calculated as follows:

DH5:1600/79/6*0.4*79*(MkrDelta)/1000
DH3:1600/79/4*0.4*79*(MkrDelta)/1000
DH1:1600/79/2*0.4*79*(MkrDelta)/1000
Remark: Mkr Delta is once pulse time.

Test Graph

<table>
<tr><th colspan="3">Graphs</th></tr>
<tr><td>GFSK_DH1/LCH</td><td></td><td>
</td></tr>
<tr><td>GFSK_DH1/MCH</td><td></td><td>
</td></tr>
<tr><td>GFSK_DH1/HCH</td><td></td><td>
</td></tr>
</table>

<table>
<tr><td>GFSK_DH3/LCH</td><td></td><td>
</td></tr>
<tr><td>GFSK_DH3/MCH</td><td></td><td>
</td></tr>
<tr><td>GFSK_DH3/HCH</td><td></td><td>
</td></tr>
</table>

GFSK_DH5/LCH		
GFSK_DH5/MCH		
GFSK_DH5/HCH		

14. PSEUDORANDOM FREQUENCY

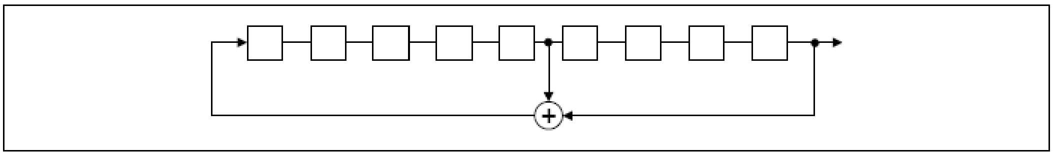
14.1 Limit

Frequency hopping systems shall have hopping channel carrier frequencies separated by a minimum of 25 kHz or the 20 dB bandwidth of the hopping channel, whichever is greater. Alternatively. Frequency hopping systems operating in the 2400-2483.5 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20 dB bandwidth of the hopping channel, whichever is greater, provided the systems operate with an output power no greater than 125 mW. The system shall hop to channel frequencies that are selected at the system hopping rate from a Pseudorandom ordered list of hopping frequencies. Each frequency must be used equally on the average by each transmitter. The system receivers shall have input bandwidths that match the hopping channel bandwidths of their corresponding transmitters and shall shift frequencies in synchronization with the transmitted signals.

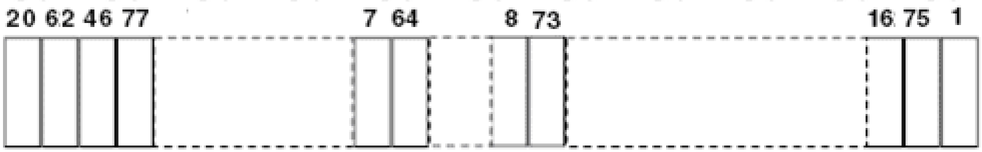
14.2 Test procedure

The pseudorandom sequence may be generated in a nine-stage shift register whose 5th and 9th stage outputs are added in a modulo-two addition stage. And the result is fed back to the input of the first stage. The sequence begins with the first ONE of 9 consecutive ONEs; i.e. the shift register is initialized with nine ones.

- Number of shift register stages: 9
- Length of pseudo-random sequence: 29 -1 = 511 bits
- Longest sequence of zeros: 8 (non-inverted signal)



An example of Pseudorandom Frequency Hopping Sequence as follow:



Each frequency used equally on the average by each transmitter.
The system receivers have input bandwidths that match the hopping channel bandwidths of their Corresponding transmitters and shift frequencies in synchronization with the transmitted signals.

14.3 Test Result

The device does not have the ability to be coordinated with other FHSS systems in an effort to avoid the simultaneous occupancy of individual hopping frequencies by multiple transmitters.

15. ANTENNA REQUIREMENT

15.203 requirement:
An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.
15.247(b) (4) requirement:
The conducted output power limit specified in paragraph (b) of this section is based on the use of antennas with directional gains that do not exceed 6 dBi. Except as shown in paragraph (c) of this section, if transmitting antennas of directional gain greater than 6 dBi are used, the conducted output power from the intentional radiator shall be reduced below the stated values in paragraphs (b)(1), (b)(2), and (b)(3) of this section, as appropriate, by the amount in dB that the directional gain of the antenna exceeds 6 dBi.

EUT Antenna:

The antenna is Internal antenna. The best case gain of the antenna is 1.0dBi.

16. EUT PHOTOGRAPHS

EUT Photo 1

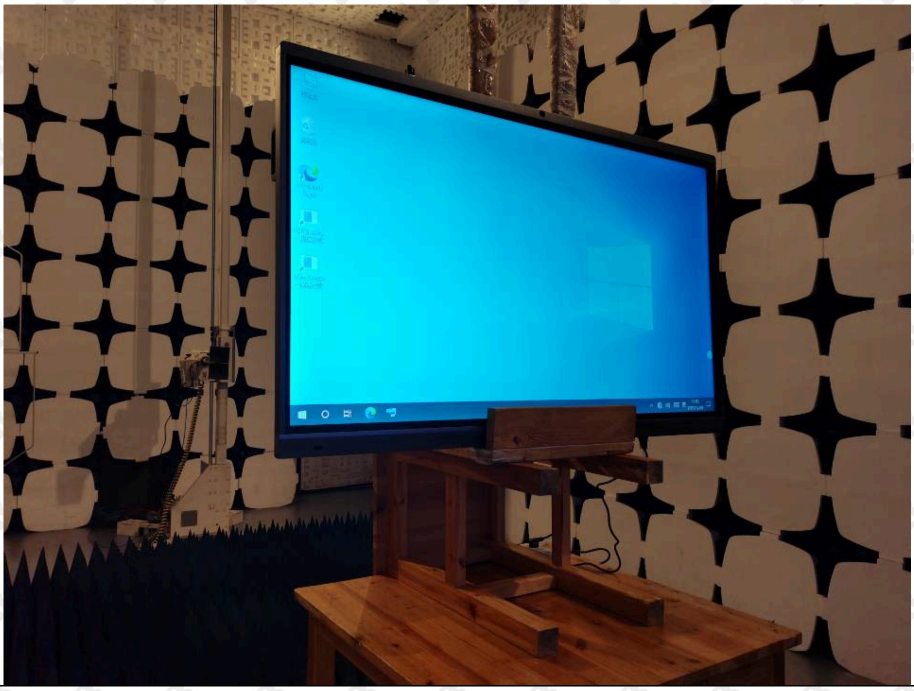
EUT Photo 2

17. EUT TEST SETUP PHOTOGRAPHS

Radiated Emission

Below 1G

Above 1G



Conducted emissions

※※※※※ END OF REPORT ※※※※※