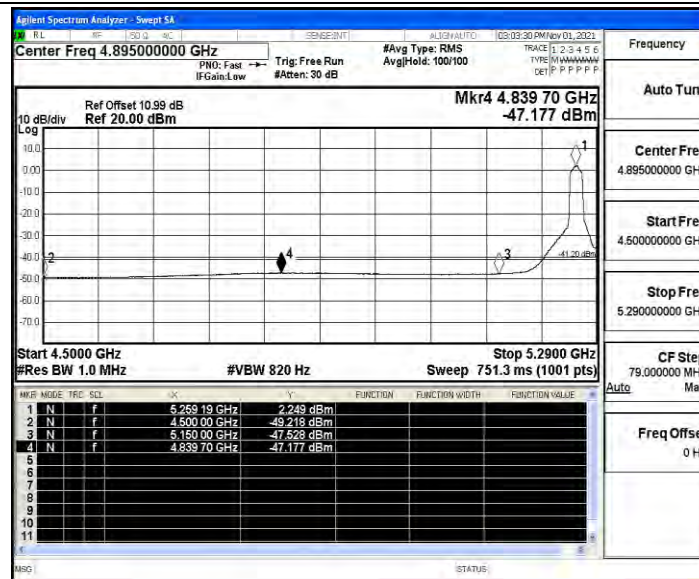
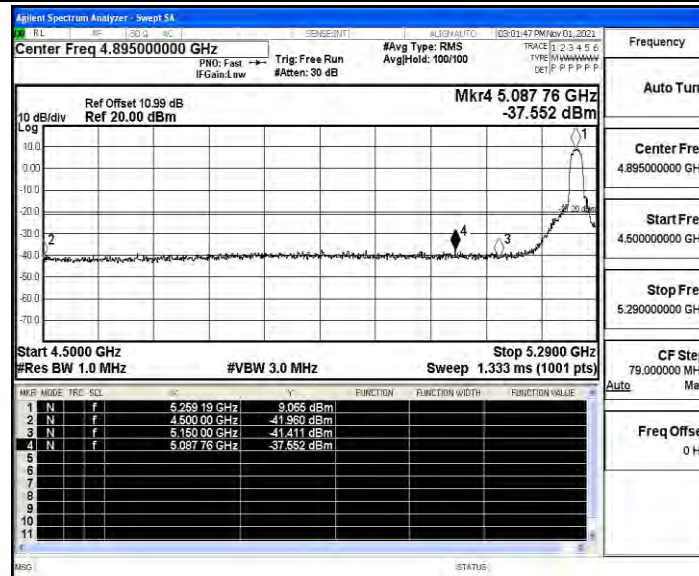
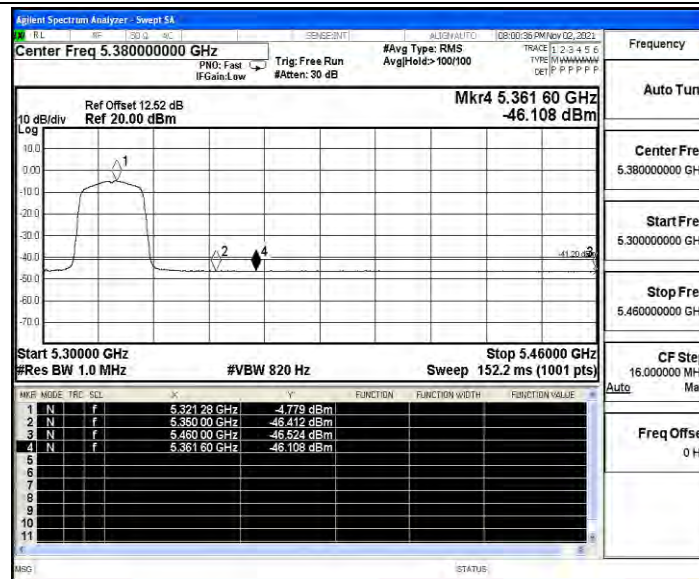




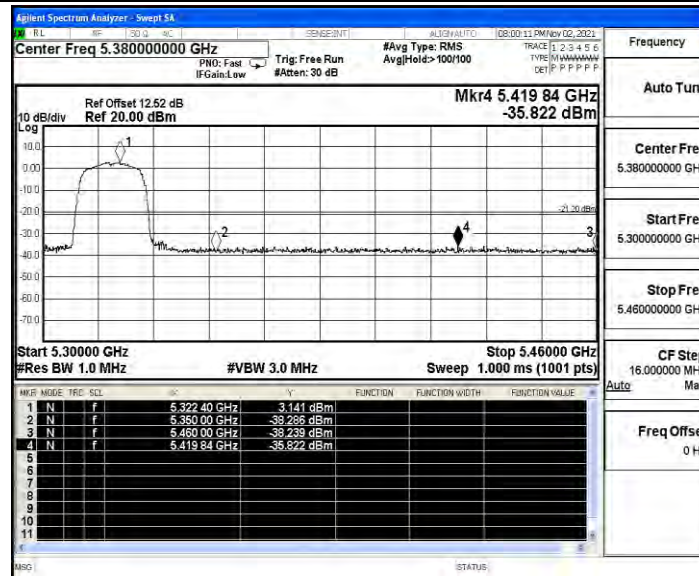
11A_Ant1_Low_5260_Peak



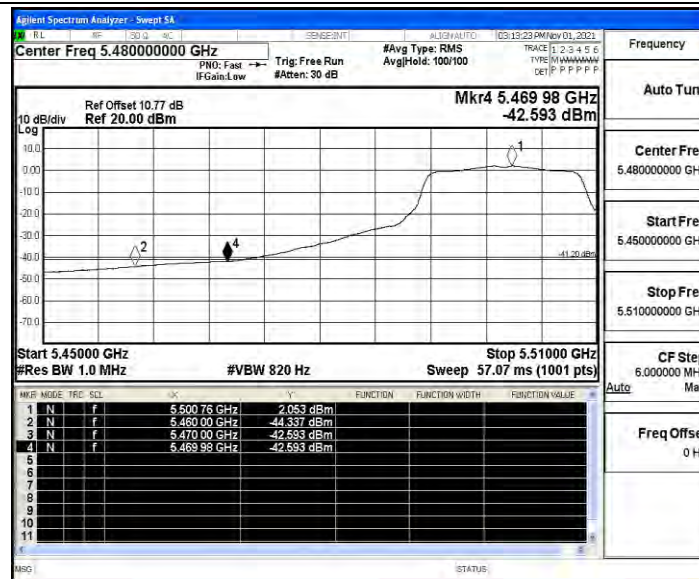
11A_Ant1_High_5320_AV



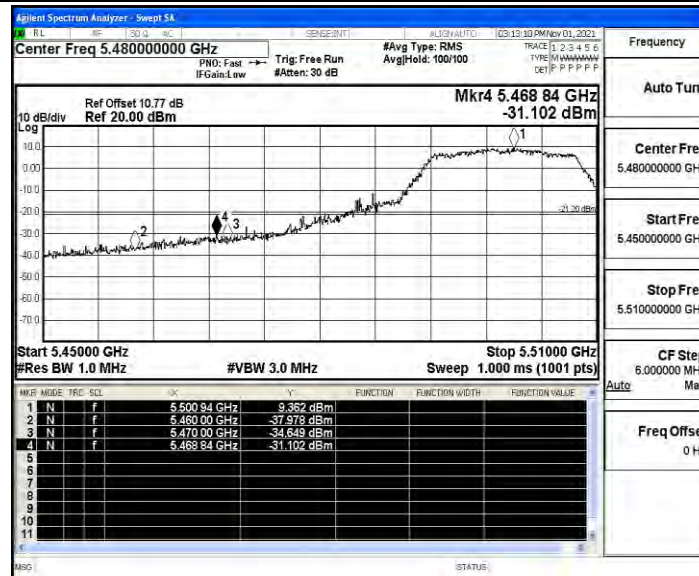
11A_Ant1_High_5320_Peak



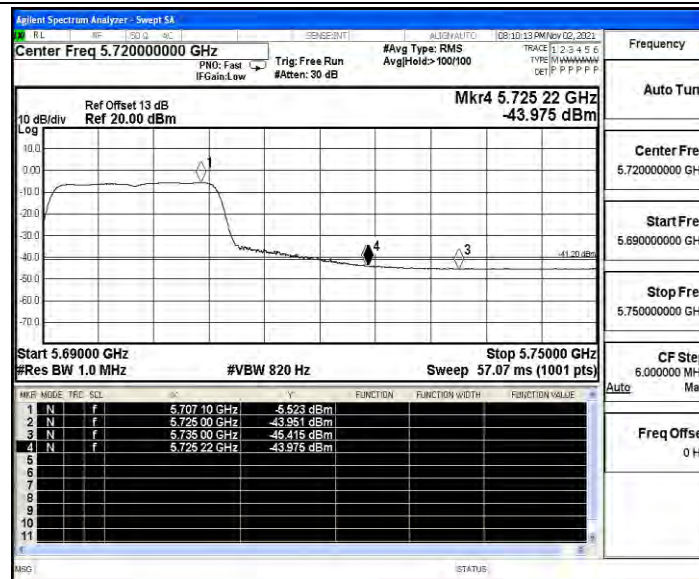
11A_Ant1_Low_5500_AV



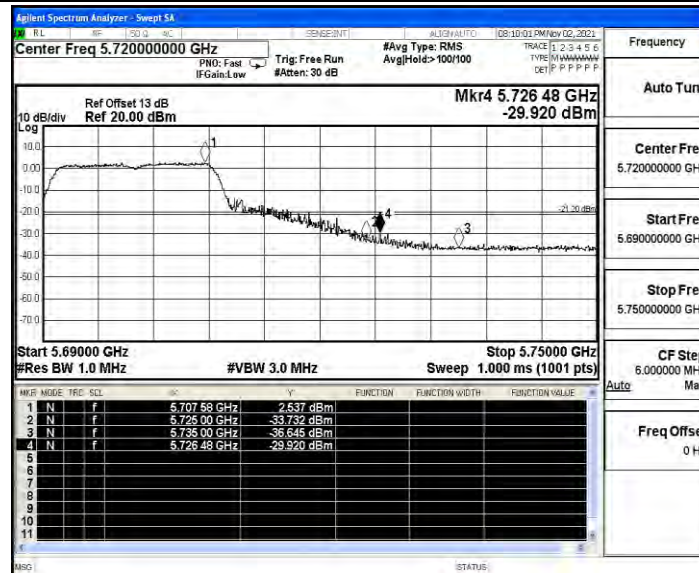
11A_Ant1_Low_5500_Peak



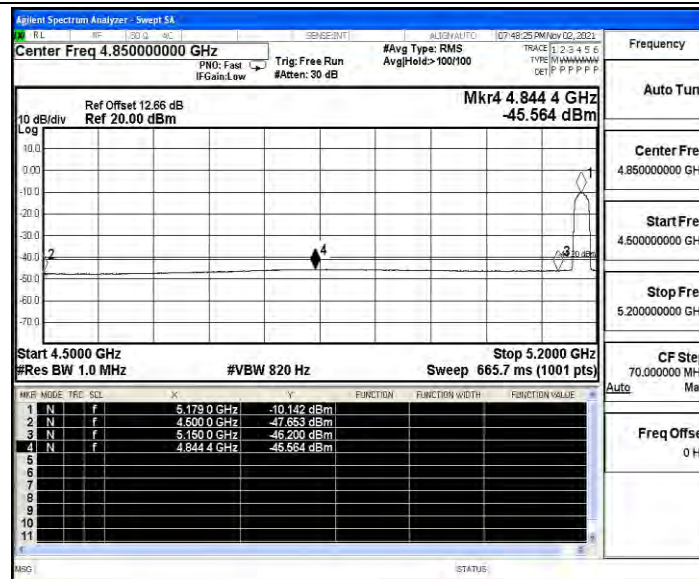
11A_Ant1_High_5700_AV



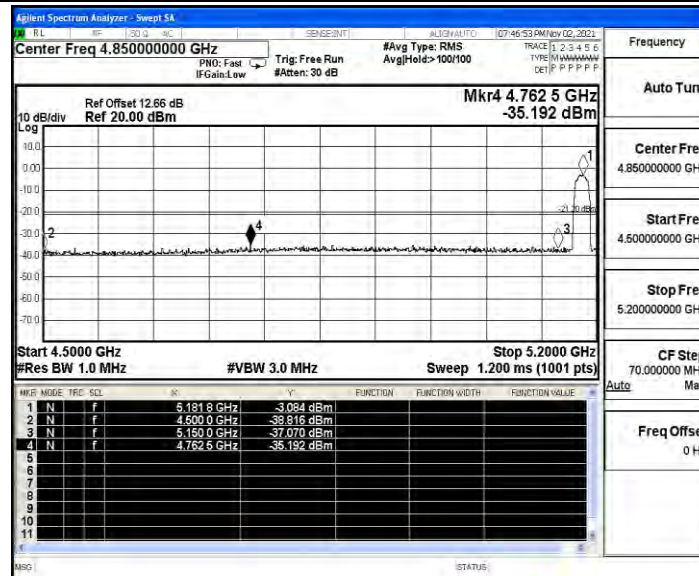
11A_Ant1_High_5700_Peak



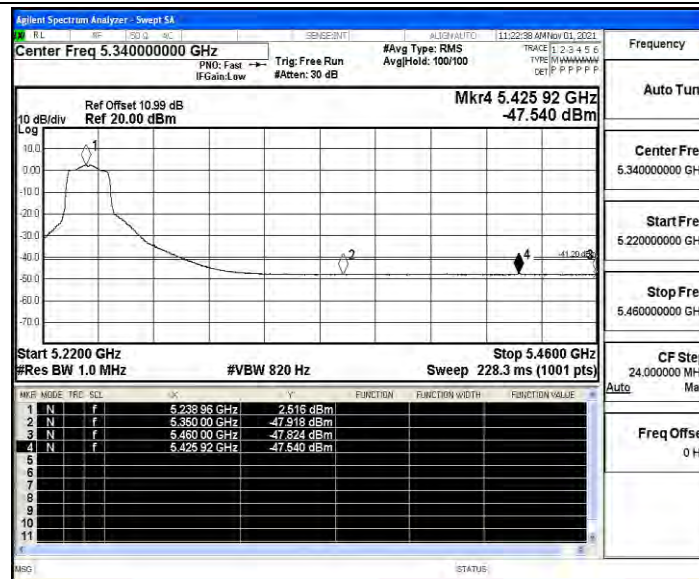
11N20SISO_Ant1_Low_5180_AV



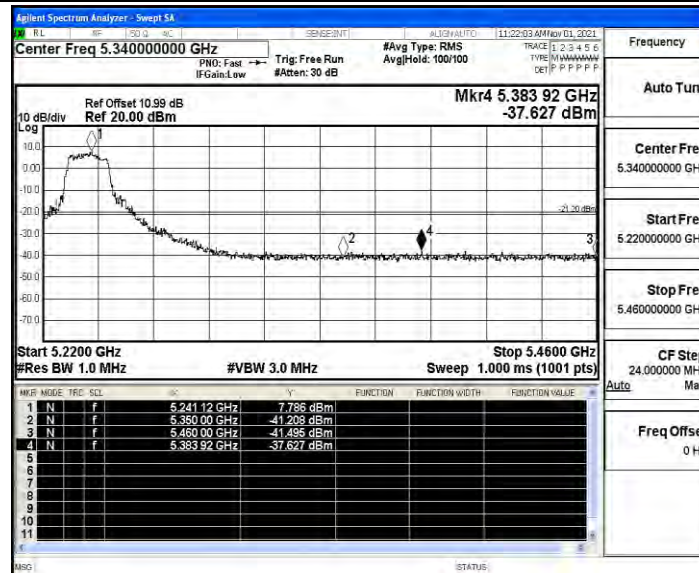
11N20SISO_Ant1_Low_5180_Peak



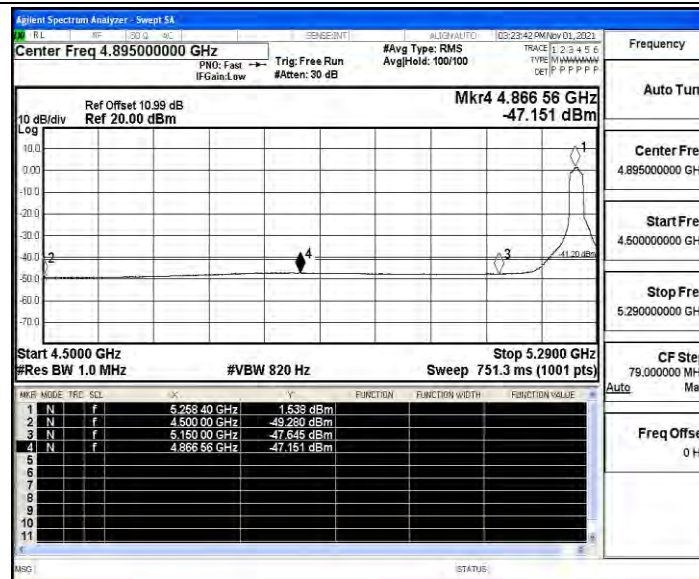
11N20SISO_Ant1_High_5240_AV



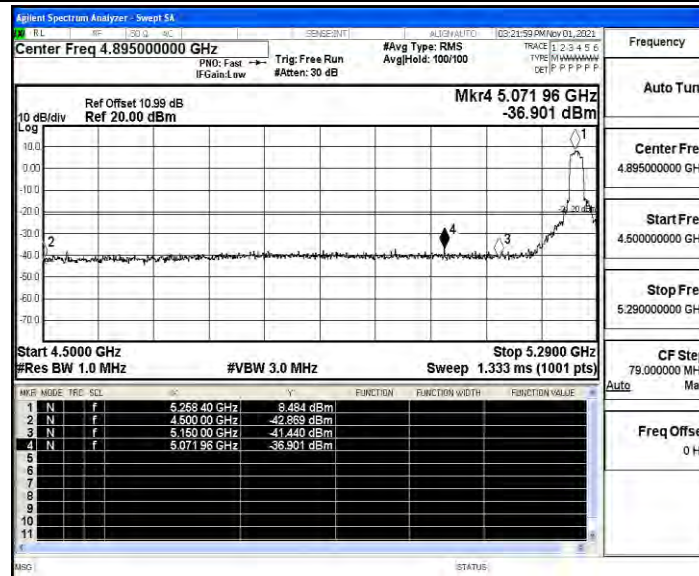
11N20SISO_Ant1_High_5240_Peak



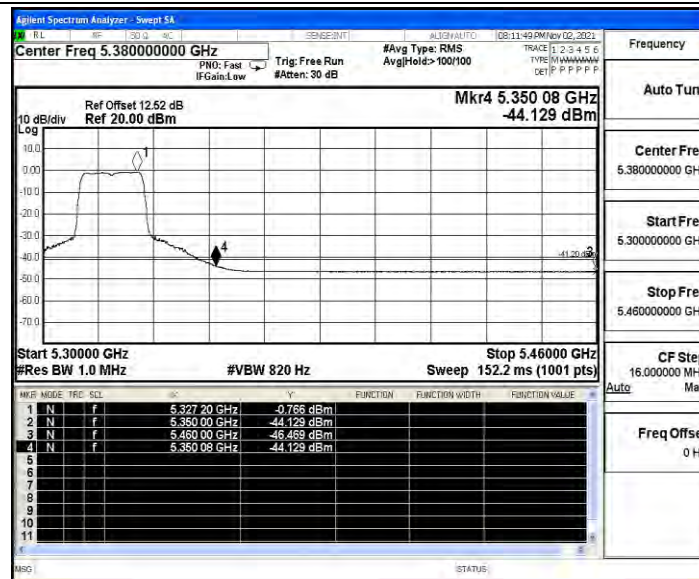
11N20SISO_Ant1_Low_5260_AV



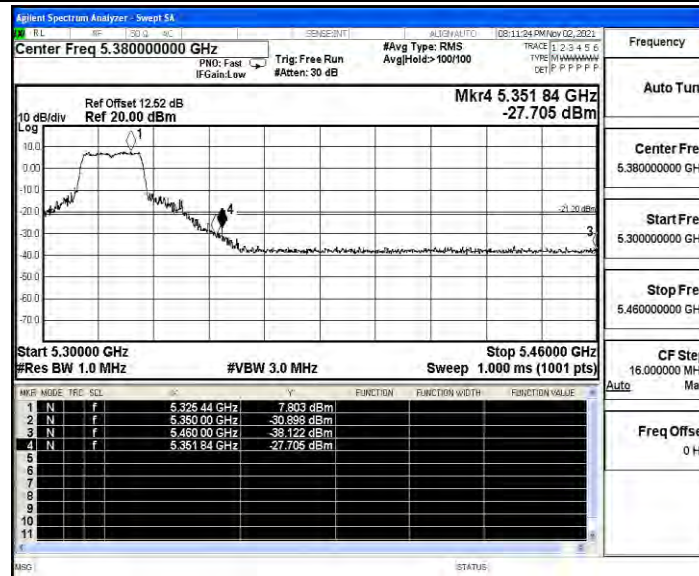
11N20SISO_Ant1_Low_5260_Peak



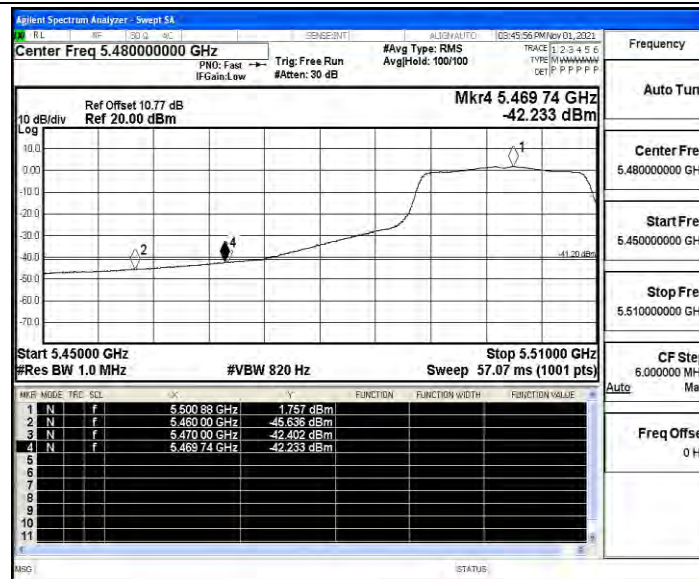
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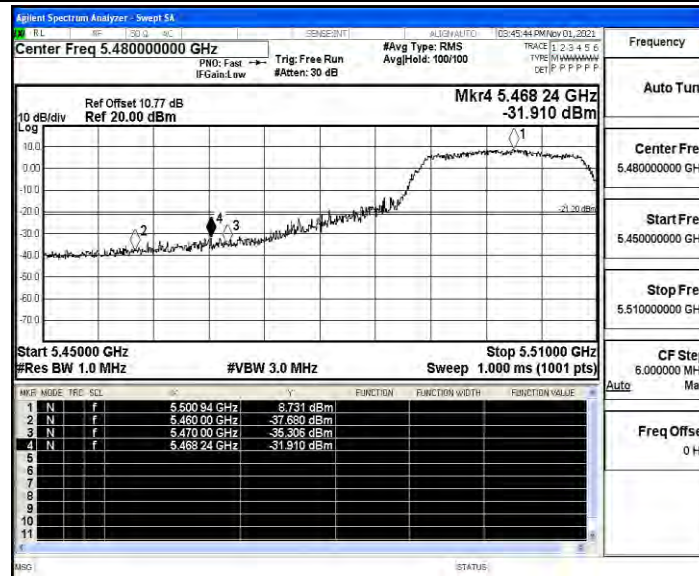
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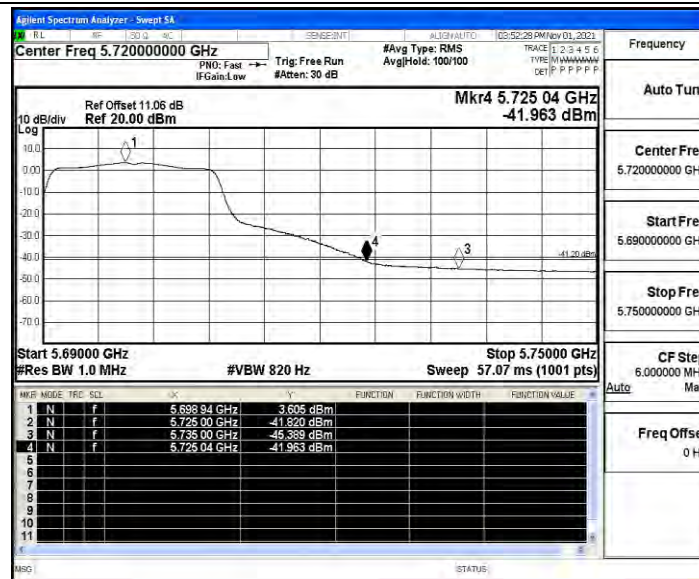
11N20SISO_Ant1_Low_5500_AV



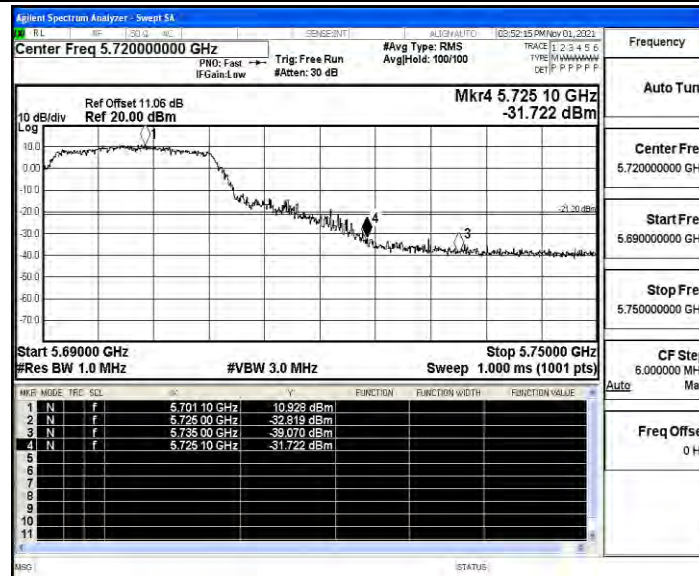
11N20SISO_Ant1_Low_5500_Peak



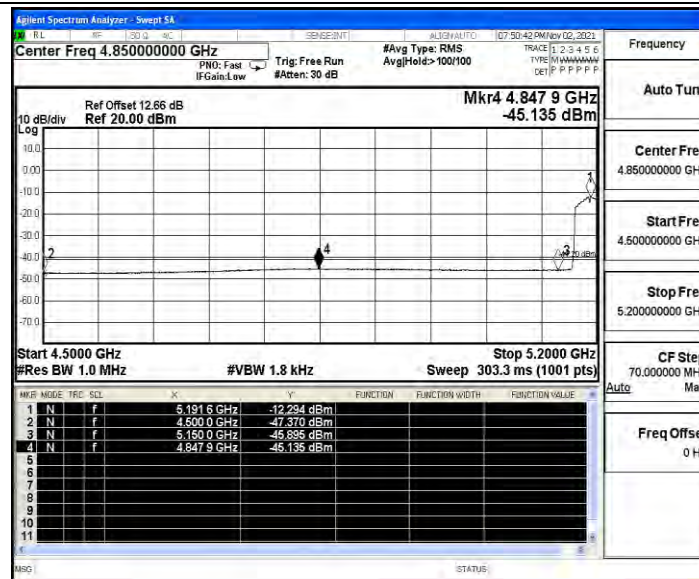
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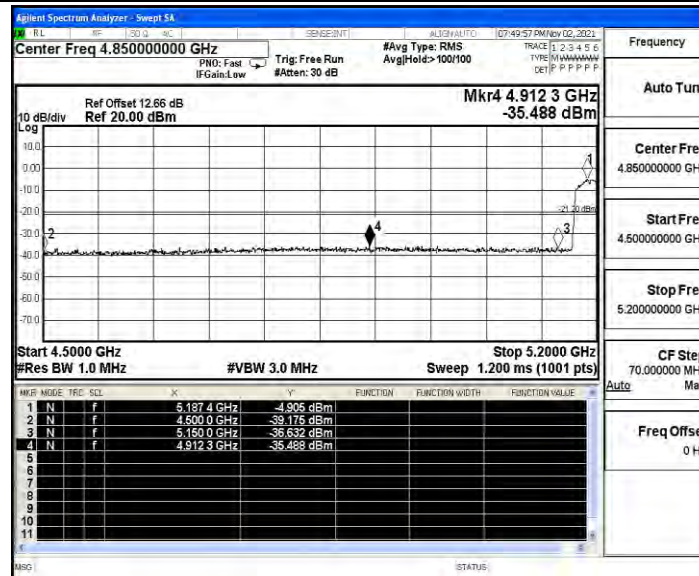
11N20SISO_Ant1_High_5700_Peak



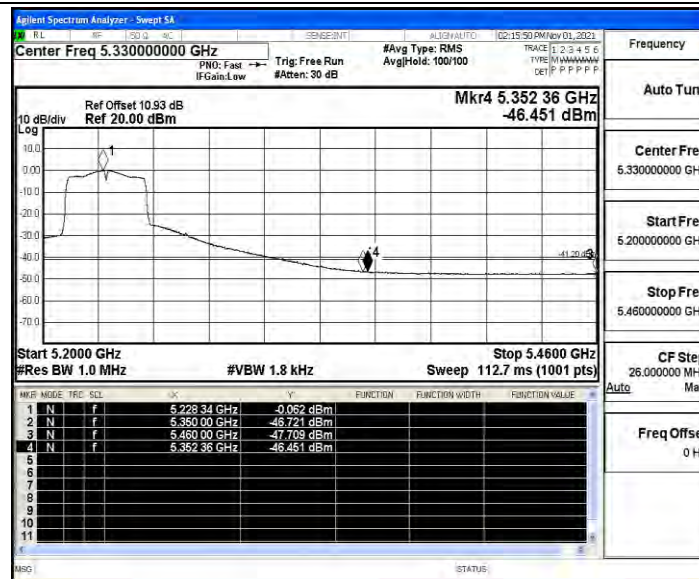
11N40SISO_Ant1_Low_5190_AV



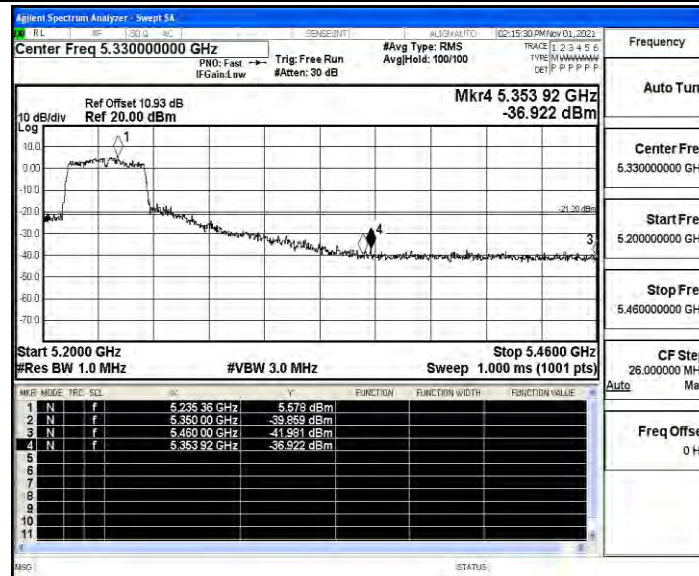
11N40SISO_Ant1_Low_5190_Peak



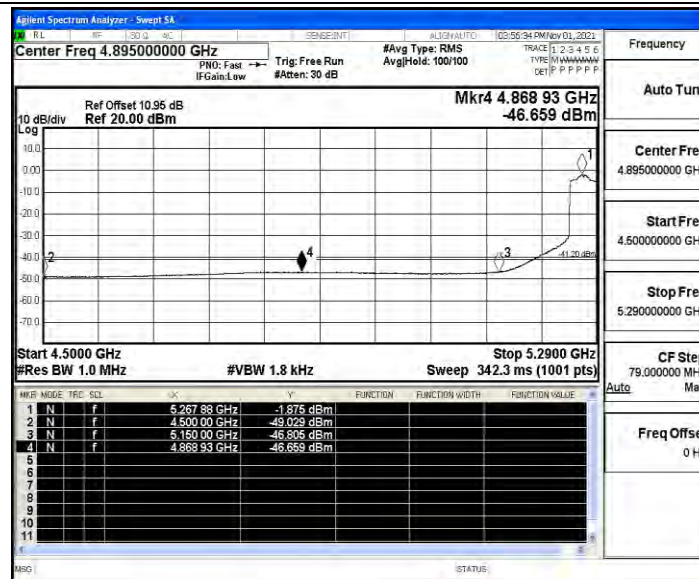
11N40SISO_Ant1_High_5230_AV



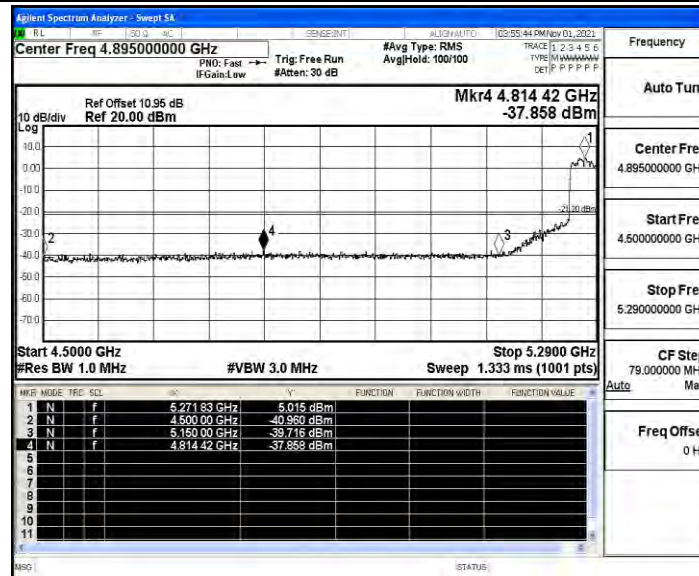
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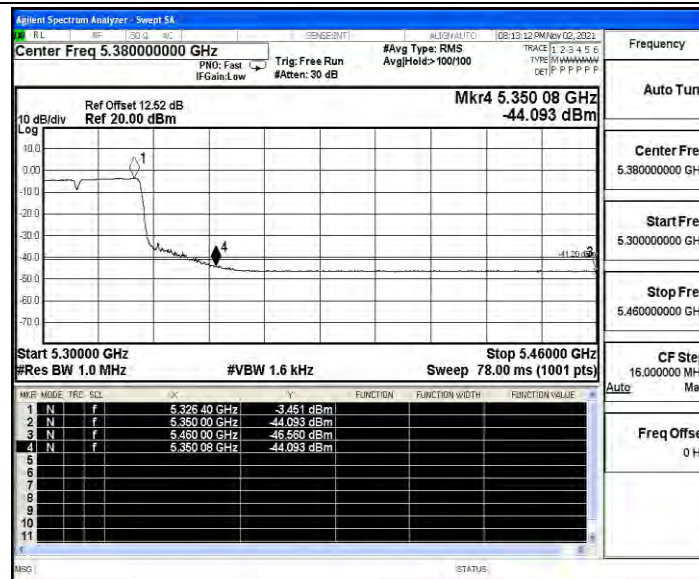
11N40SISO_Ant1_Low_5270_AV



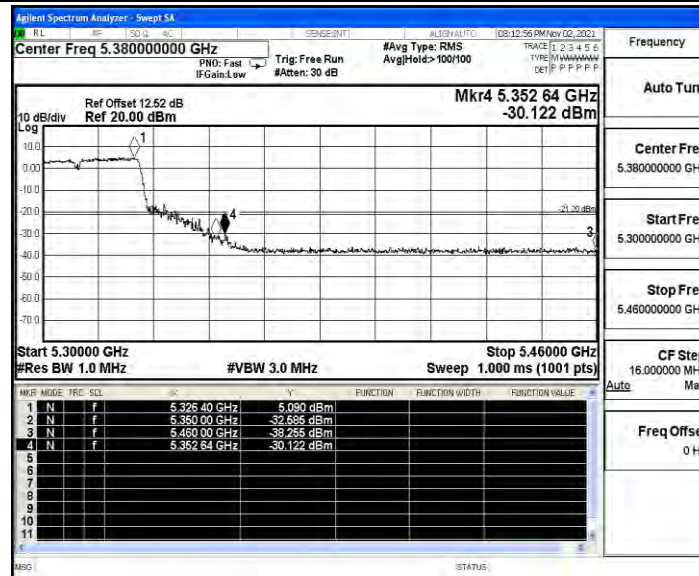
11N40SISO_Ant1_Low_5270_Peak



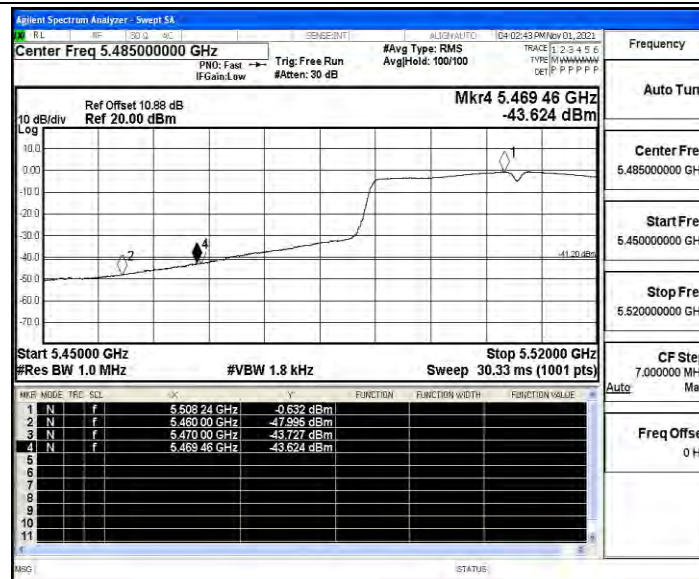
11N40SISO_Ant1_High_5310_AV



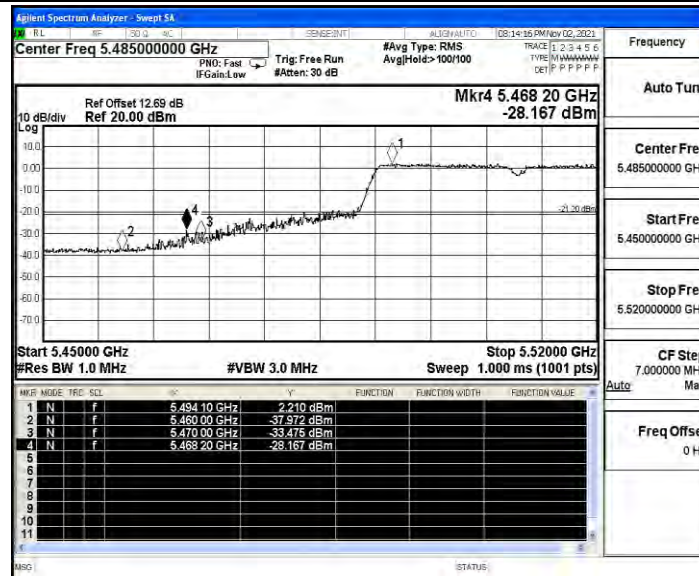
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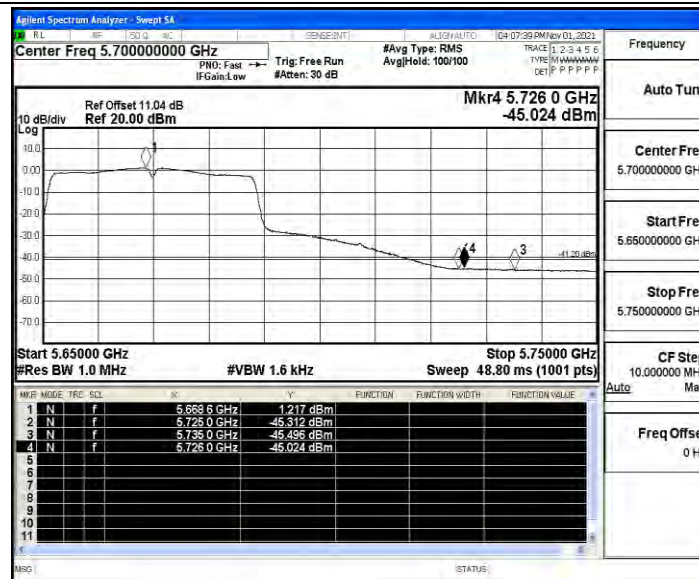
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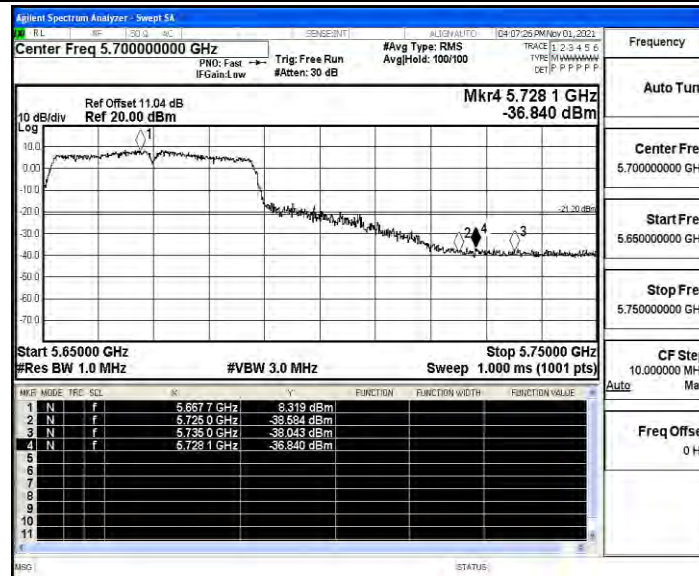
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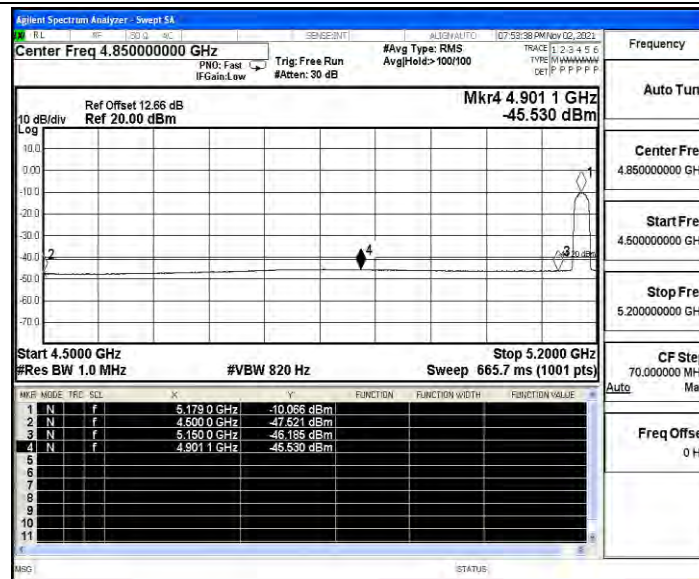
11N40SISO_Ant1_High_5670_AV



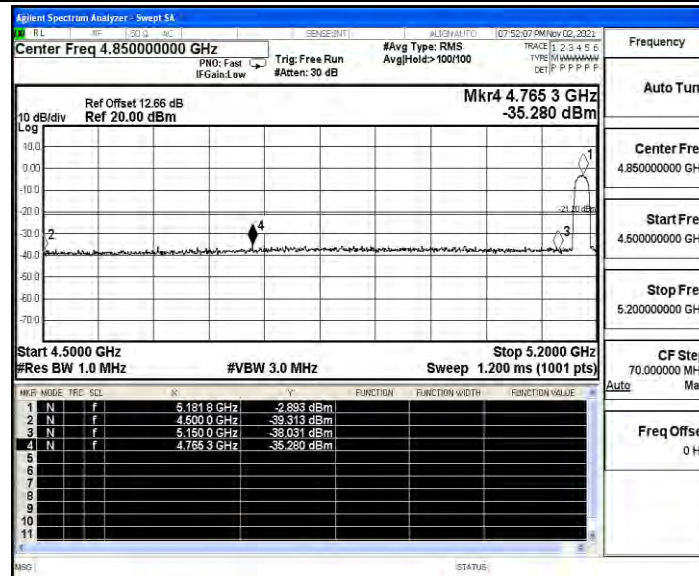
11N40SISO_Ant1_High_5670_Peak



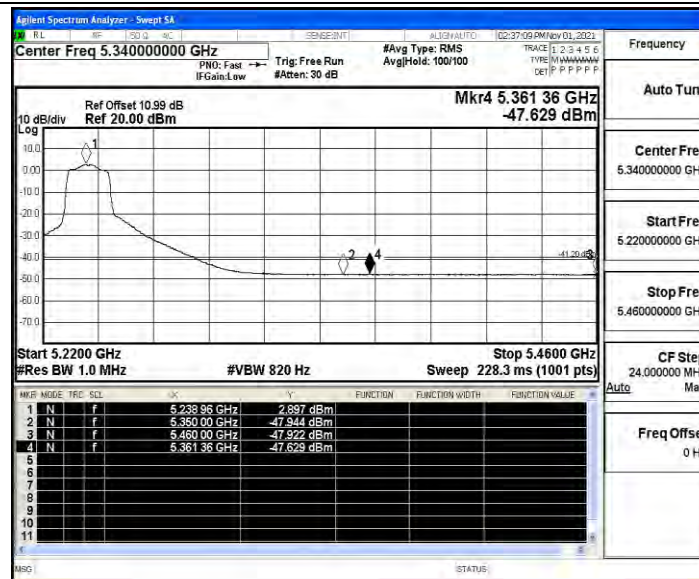
11AC20SISO_Ant1_Low_5180_AV



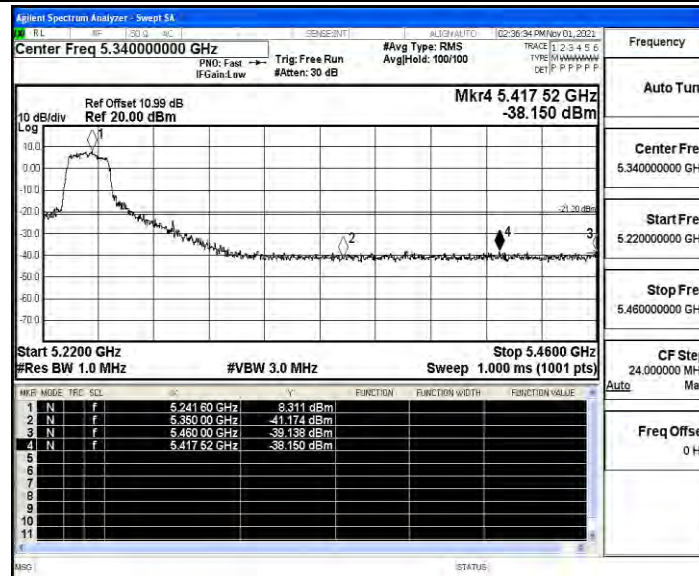
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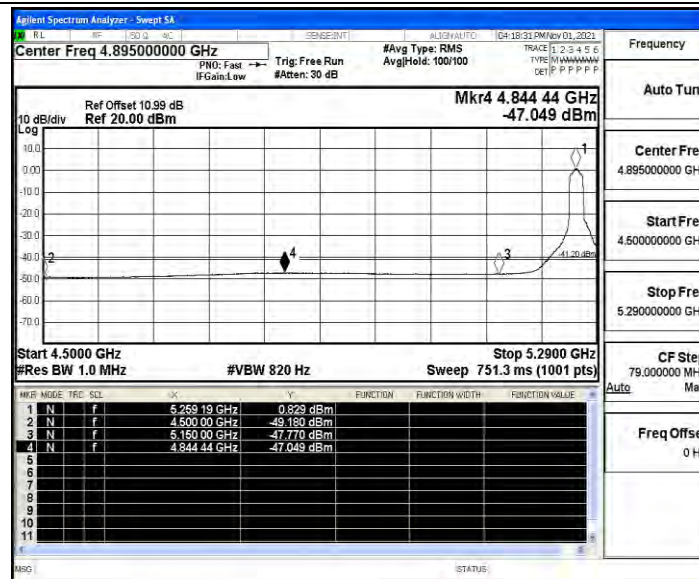
11AC20SISO_Ant1_High_5240_AV



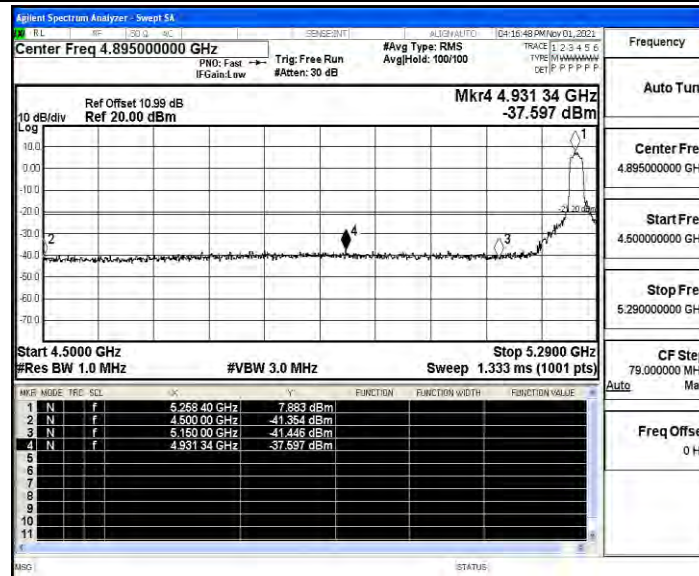
11AC20SISO_Ant1_High_5240_Peak



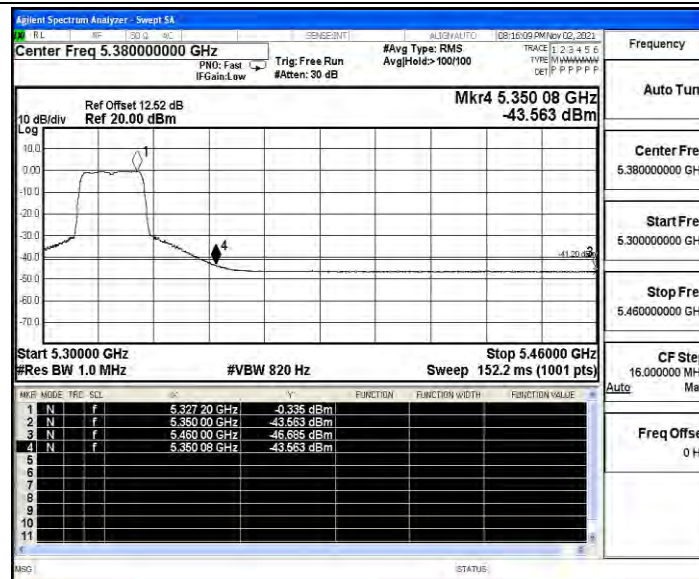
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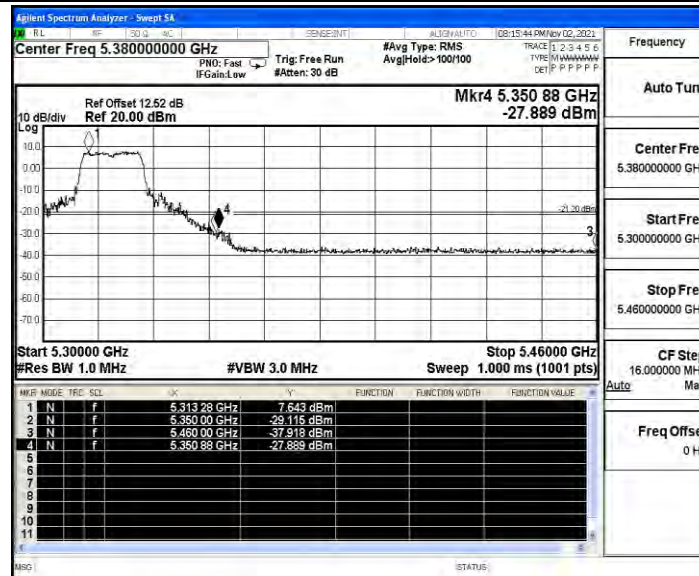
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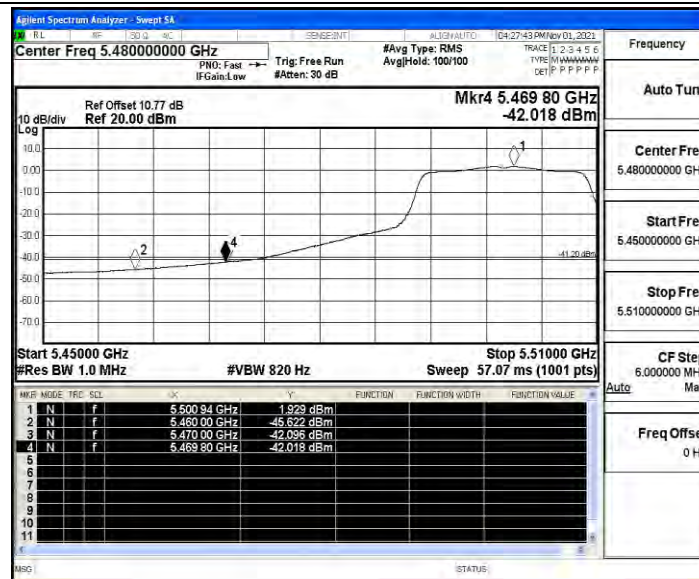
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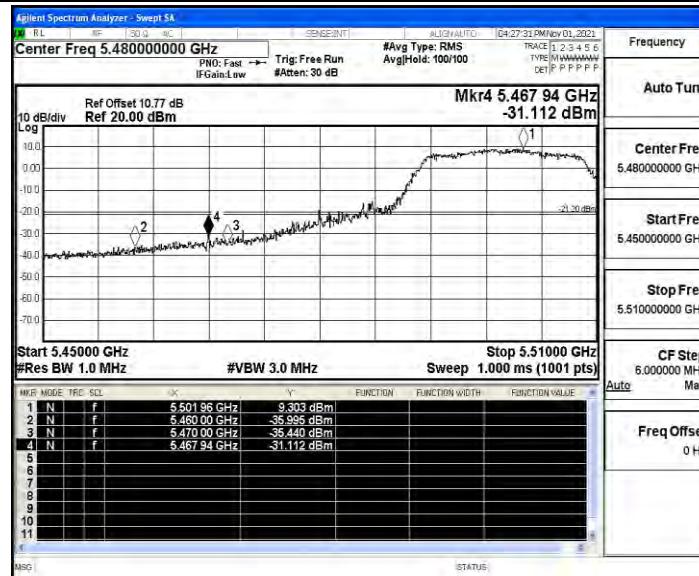
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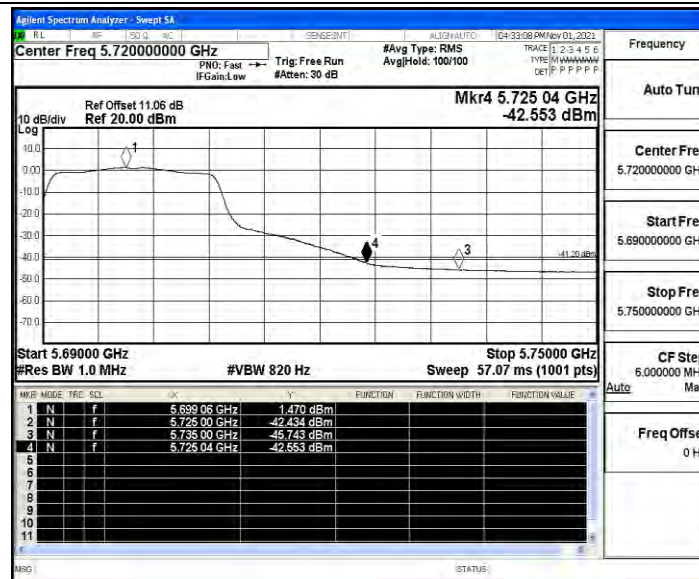
11AC20SISO_Ant1_Low_5500_AV



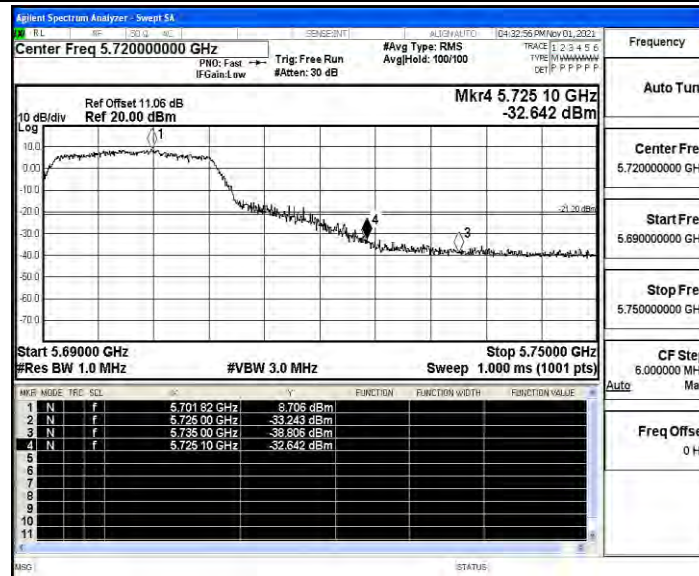
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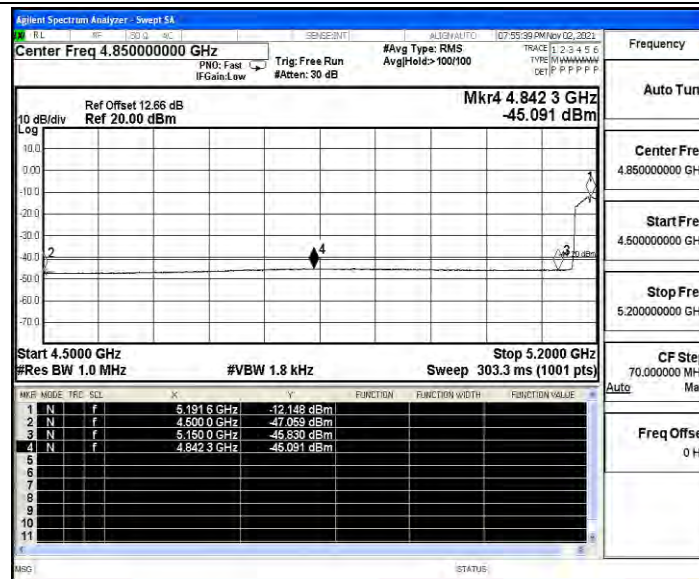
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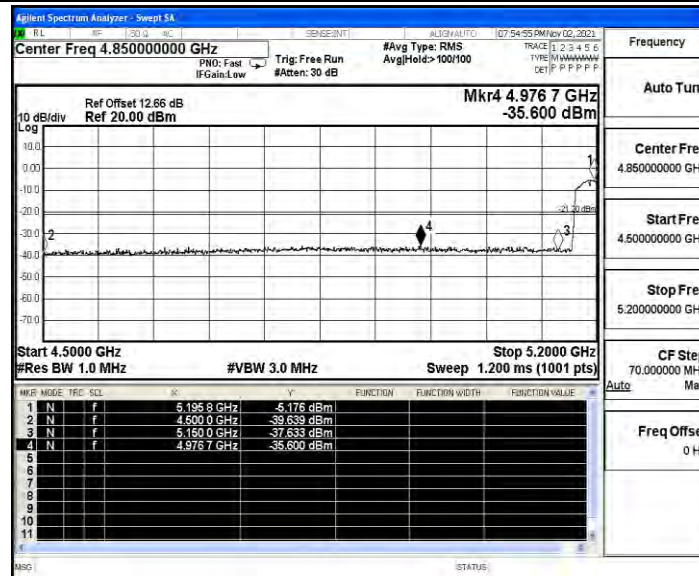
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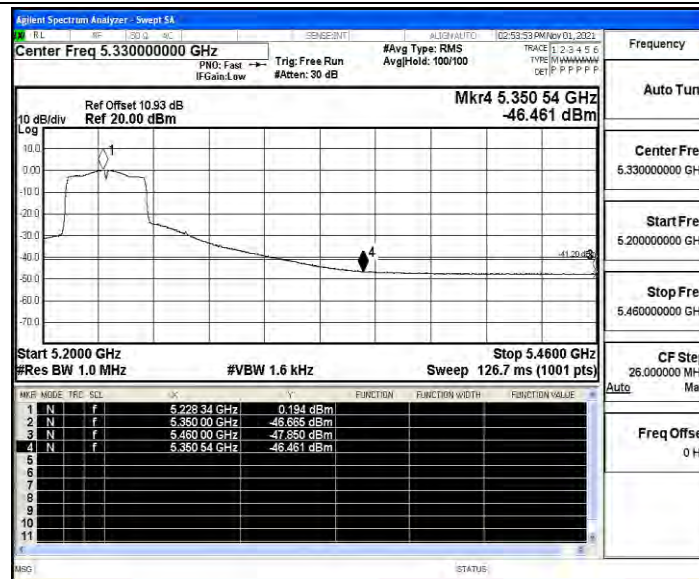
11AC40SISO_Ant1_Low_5190_AV



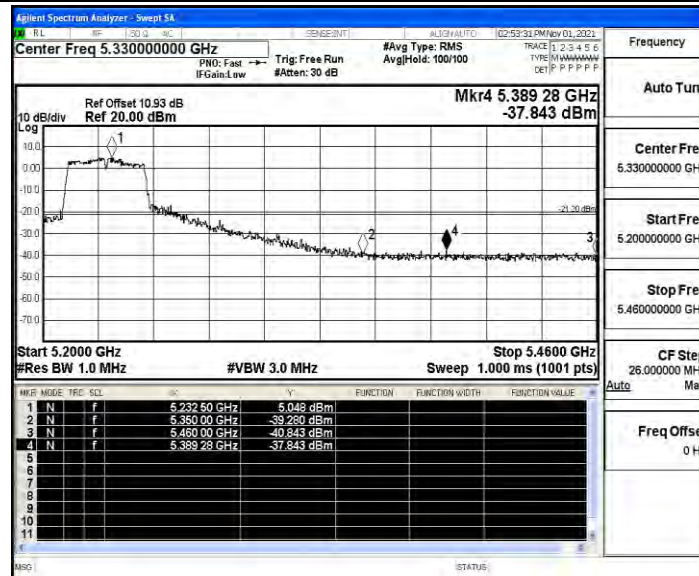
11AC40SISO_Ant1_Low_5190_Peak



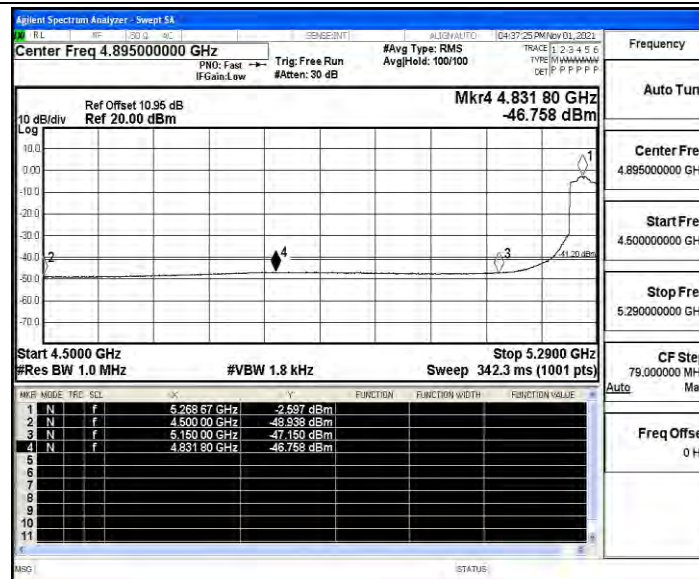
11AC40SISO_Ant1_High_5230_AV



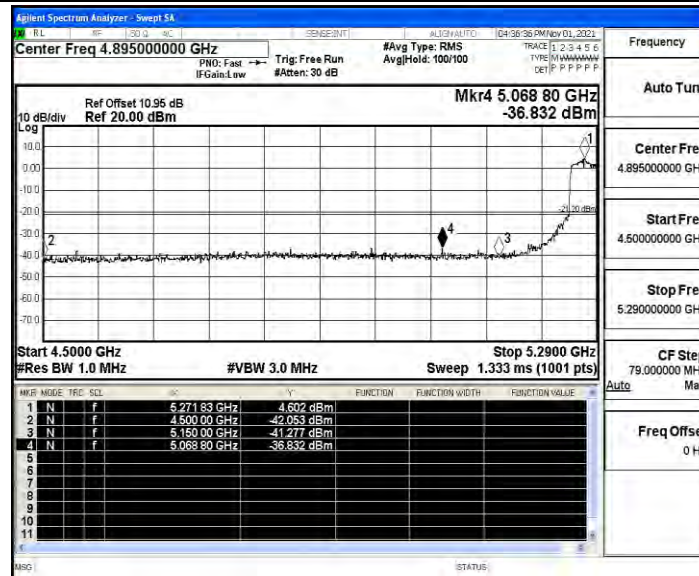
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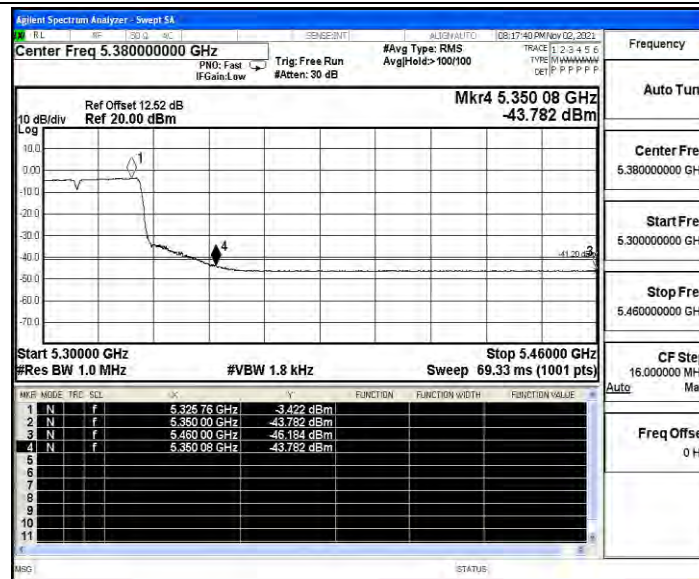
11AC40SISO_Ant1_Low_5270_AV



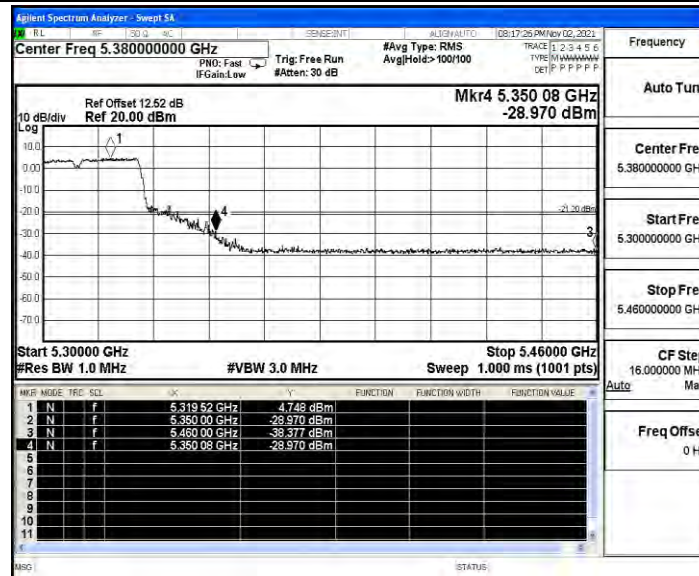
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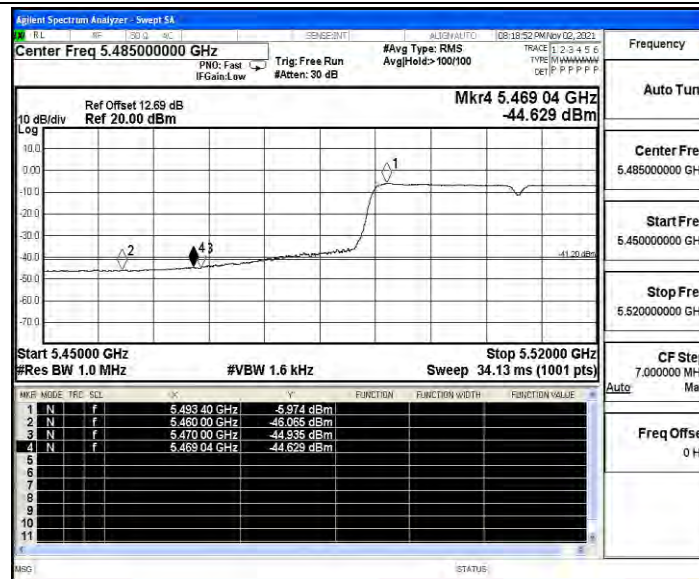
11AC40SISO_Ant1_High_5310_AV



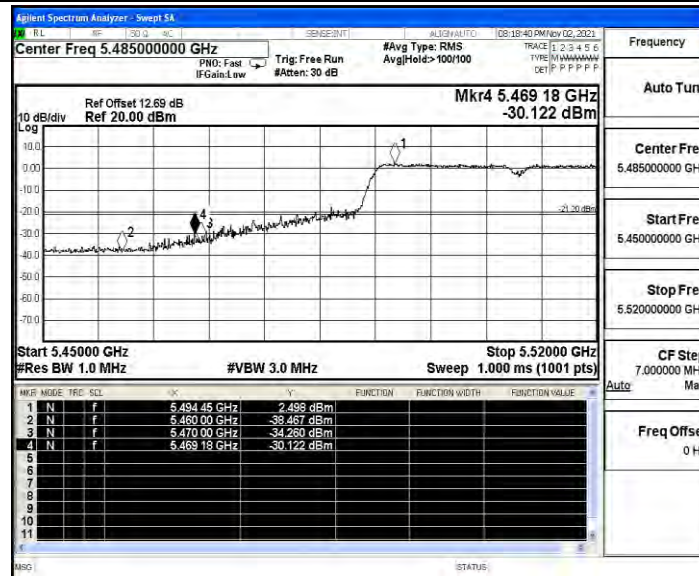
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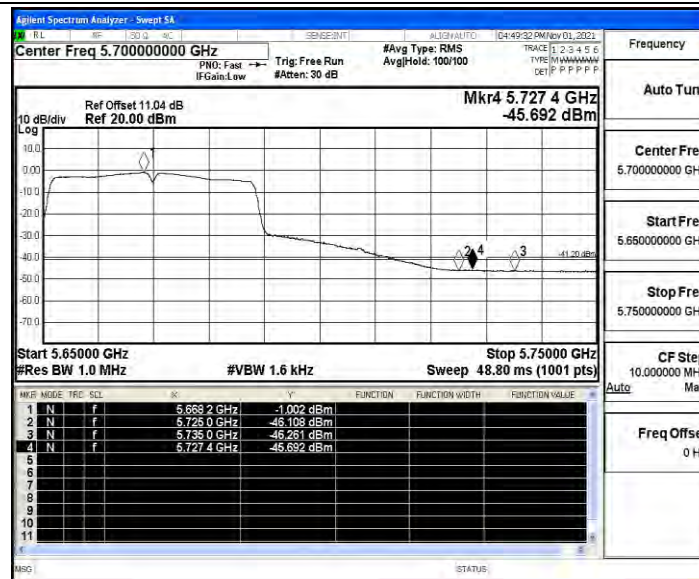
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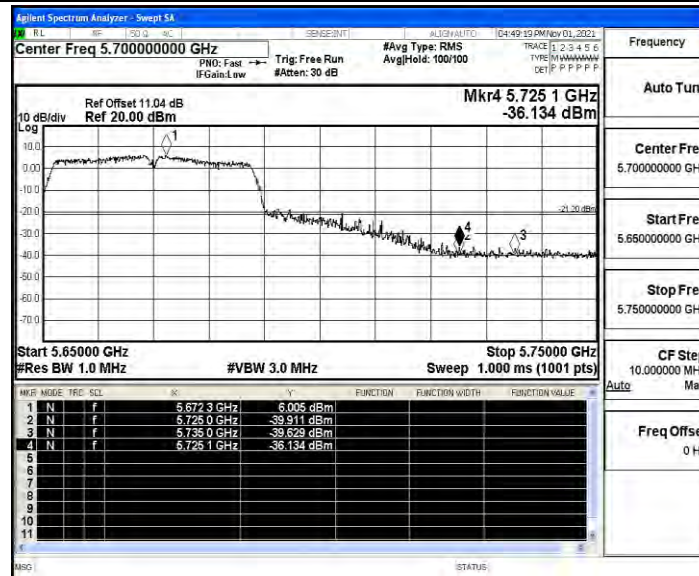
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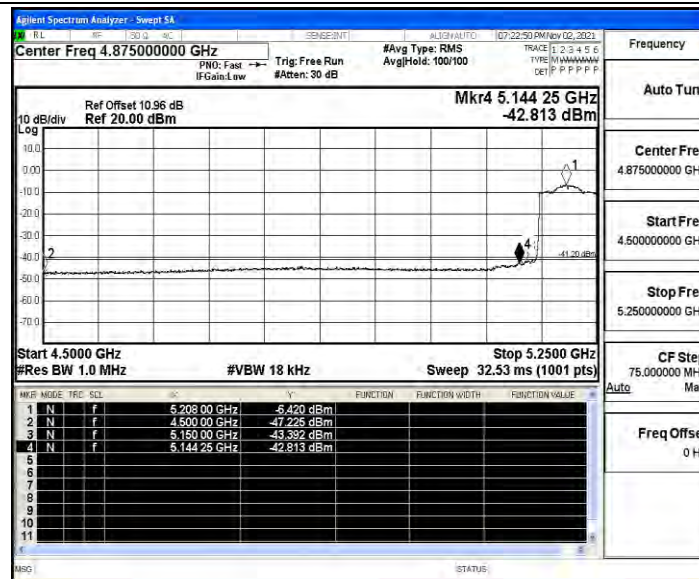
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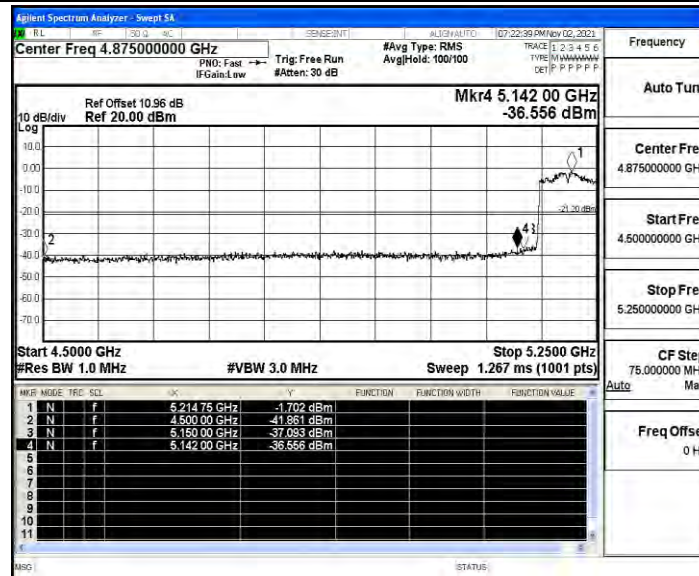
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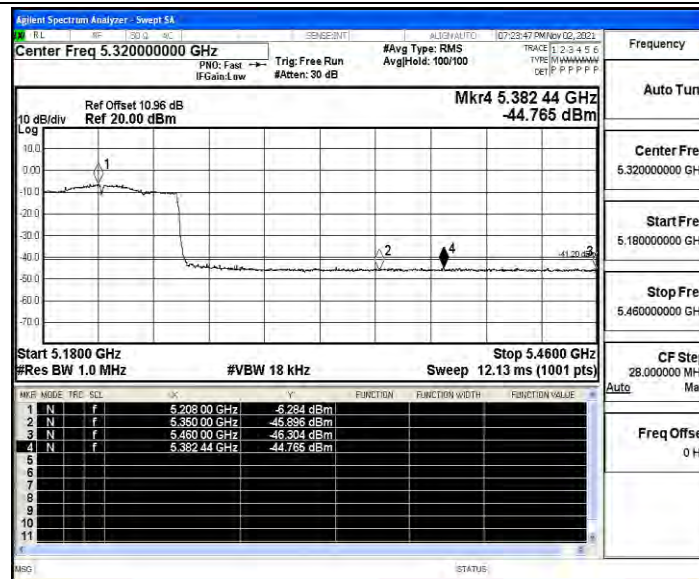
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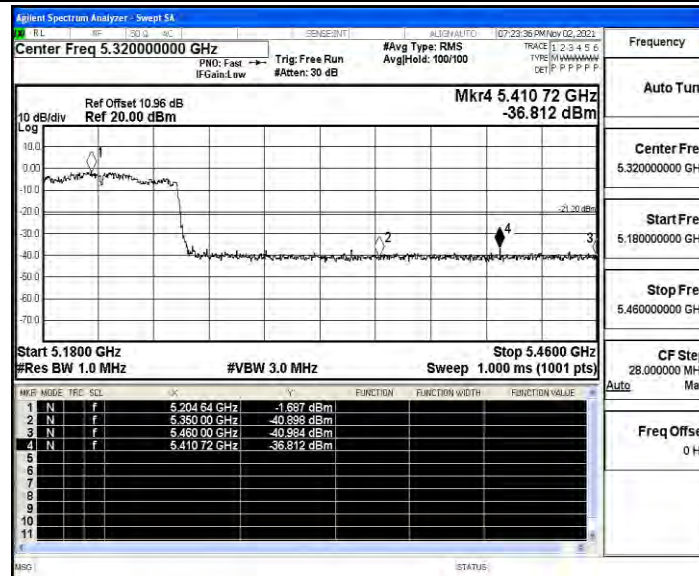
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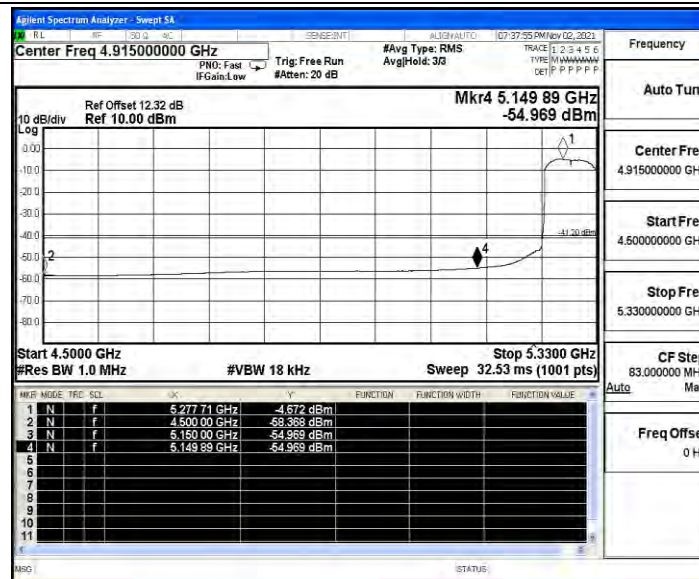
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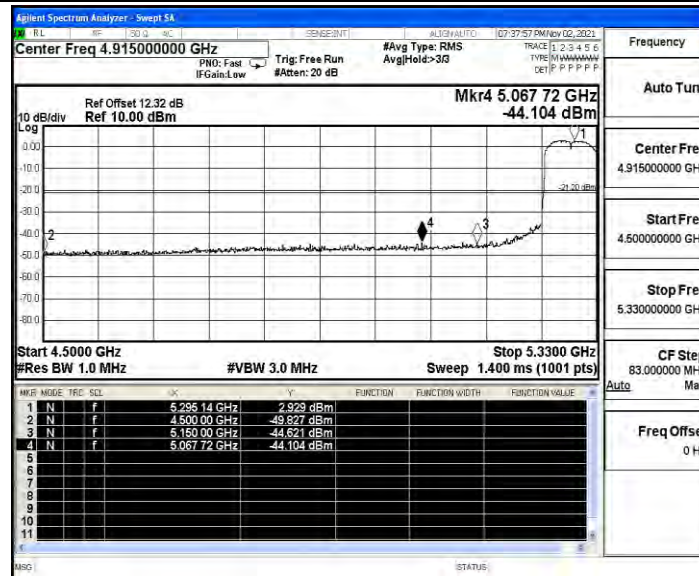
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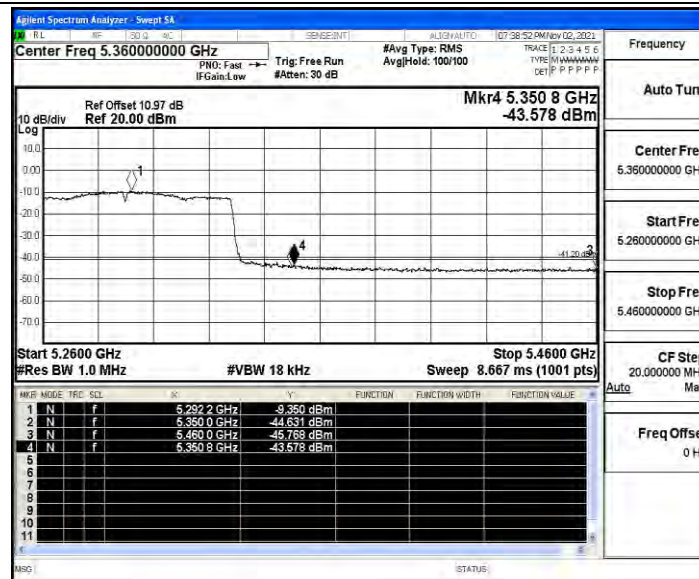
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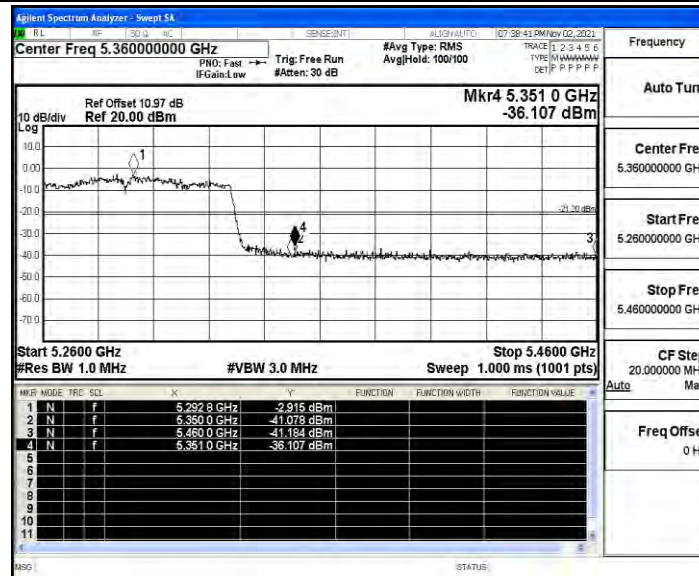
11AC80SISO_Ant1_Low_5290_Peak



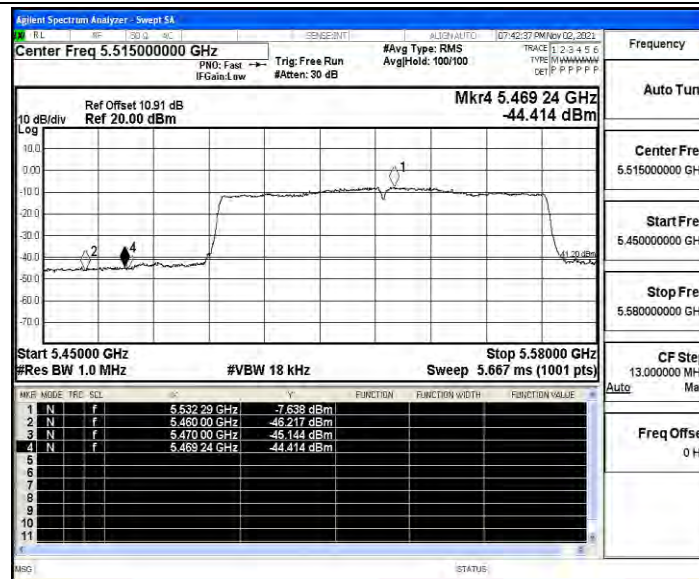
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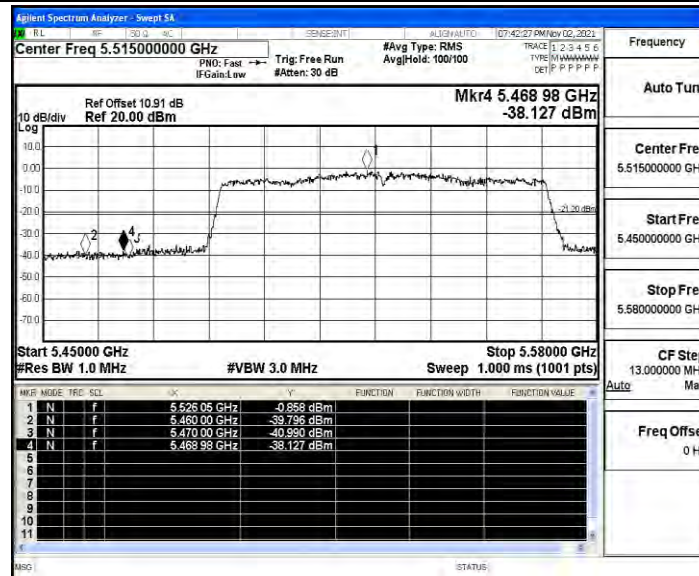
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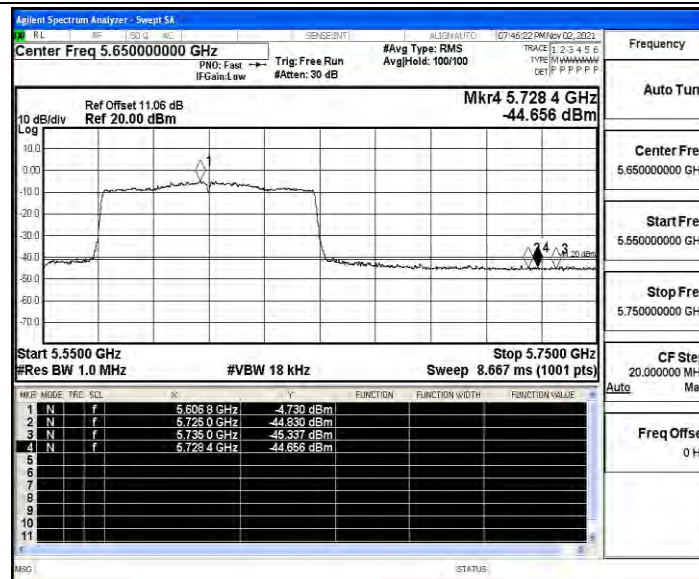
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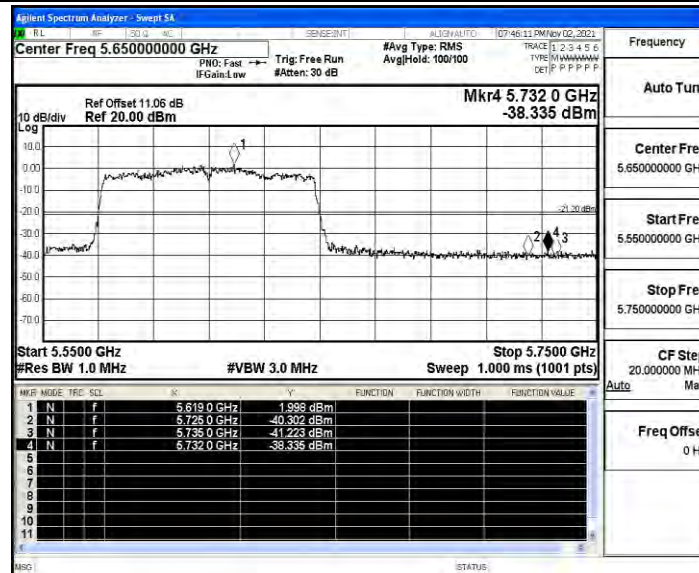
11AC80SISO_Ant1_Low_5530_Peak



11AC80SISO_Ant1_High_5610_AV



11AC80SISO_Ant1_High_5610_Peak





D.5 Duty Cycle

Test Result

TestMode	Antenna	Channel	Transmission Duration [ms]	Transmission Period [ms]	Duty Cycle [%]	Limit	Verdict
11A	Ant1	5180	1.39	1.43	97.20	---	PASS
		5200	1.39	1.44	96.53	---	PASS
		5240	1.39	1.44	96.53	---	PASS
		5260	1.39	1.43	97.20	---	PASS
		5280	1.39	1.43	97.20	---	PASS
		5320	1.39	1.44	96.53	---	PASS
		5500	1.39	1.44	96.53	---	PASS
		5580	1.39	1.43	97.20	---	PASS
		5700	1.39	1.43	97.20	---	PASS
11N20SISO	Ant1	5180	1.31	1.35	97.04	---	PASS
		5200	1.31	1.35	97.04	---	PASS
		5240	1.31	1.35	97.04	---	PASS
		5260	1.30	1.35	96.30	---	PASS
		5280	1.31	1.35	97.04	---	PASS
		5320	1.31	1.35	97.04	---	PASS
		5500	1.30	1.35	96.30	---	PASS
		5580	1.31	1.35	97.04	---	PASS
		5700	1.31	1.35	97.04	---	PASS
11N40SISO	Ant1	5190	0.64	0.68	94.12	---	PASS
		5230	0.64	0.69	92.75	---	PASS
		5270	0.64	0.68	94.12	---	PASS
		5310	0.65	0.69	94.20	---	PASS
		5510	0.64	0.68	94.12	---	PASS
		5550	0.64	0.68	94.12	---	PASS
		5670	0.65	0.69	94.20	---	PASS
11AC20SISO	Ant1	5180	1.31	1.35	97.04	---	PASS
		5200	1.31	1.36	96.32	---	PASS
		5240	1.31	1.35	97.04	---	PASS
		5260	1.31	1.36	96.32	---	PASS
		5280	1.31	1.35	97.04	---	PASS
		5320	1.31	1.36	96.32	---	PASS
		5500	1.31	1.36	96.32	---	PASS
		5580	1.31	1.36	96.32	---	PASS
		5700	1.31	1.35	97.04	---	PASS
11AC40SISO	Ant1	5190	0.64	0.69	92.75	---	PASS

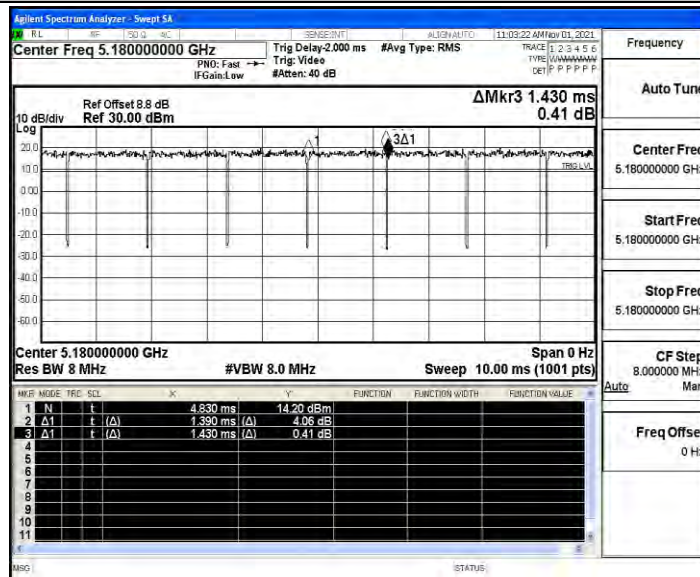


		5230	0.65	0.69	94.20	---	PASS
		5270	0.64	0.68	94.12	---	PASS
		5310	0.64	0.68	94.12	---	PASS
		5510	0.65	0.69	94.20	---	PASS
		5550	0.65	0.69	94.20	---	PASS
		5670	0.65	0.69	94.20	---	PASS
11AC80SISO	Ant1	5210	0.06	0.10	60.00	---	PASS
		5290	0.06	0.10	60.00	---	PASS
		5530	0.06	0.10	60.00	---	PASS
		5610	0.06	0.10	60.00	---	PASS

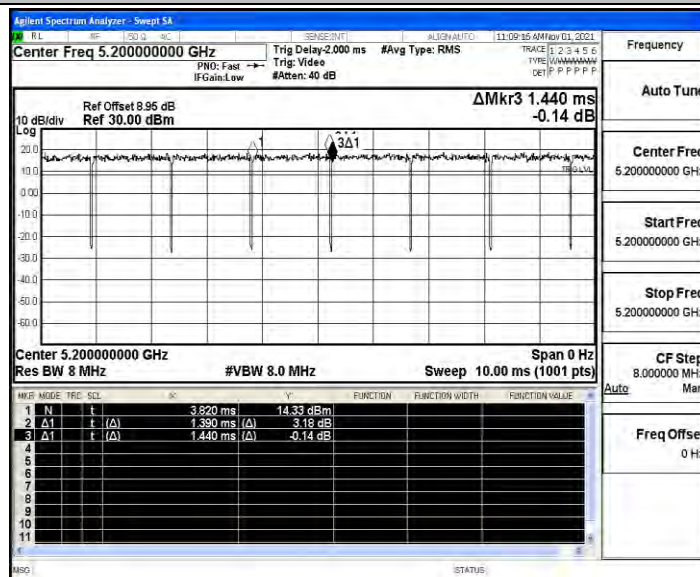


Test Graphs

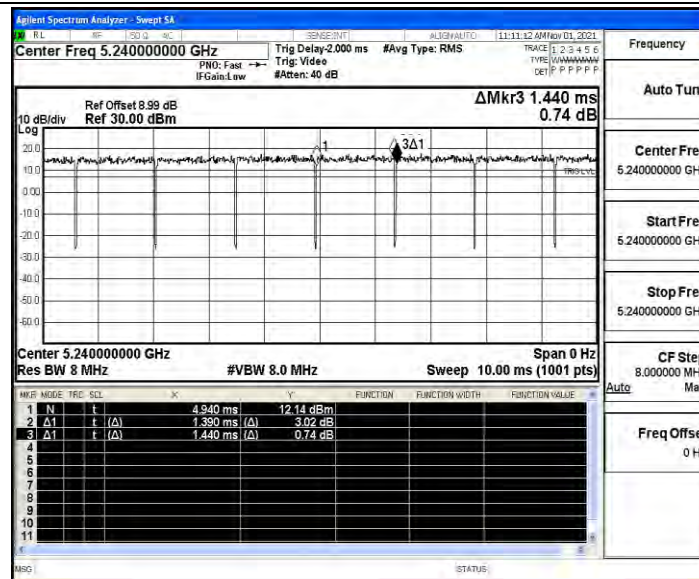
11A_Ant1_5180



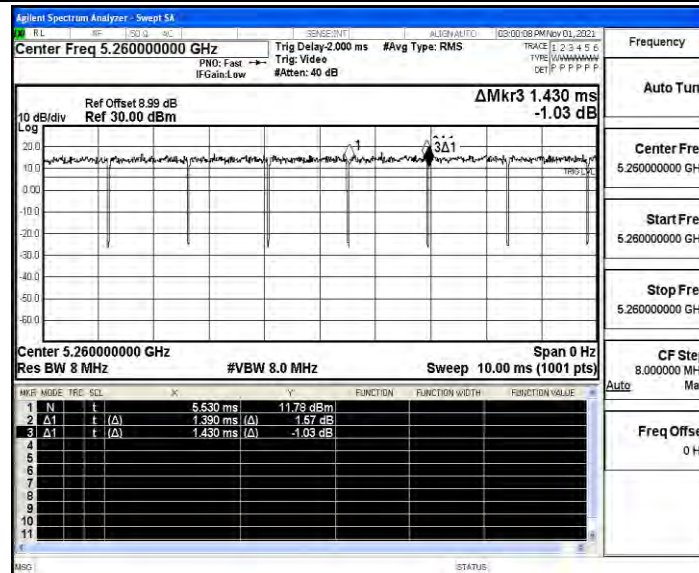
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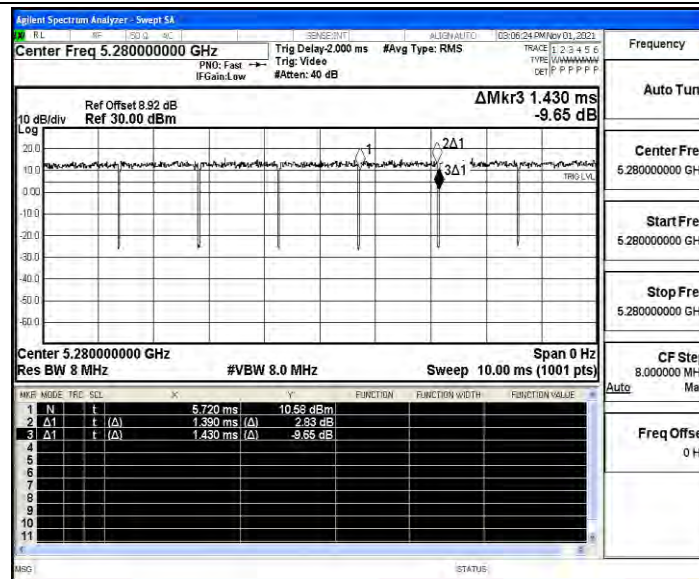
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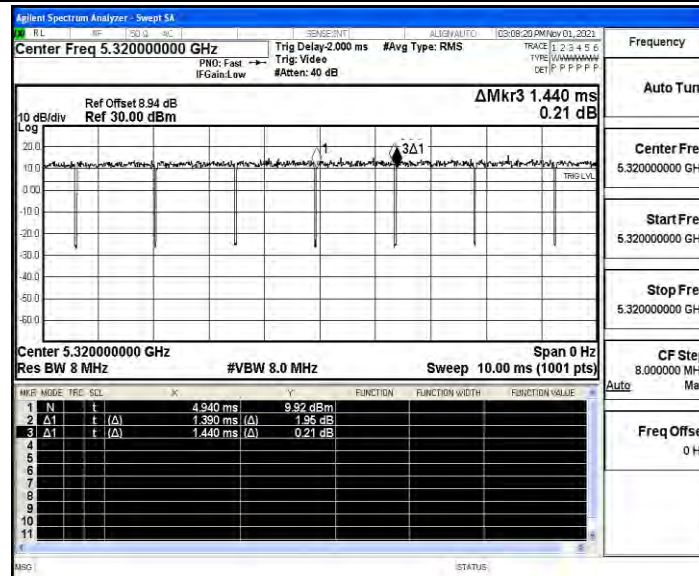
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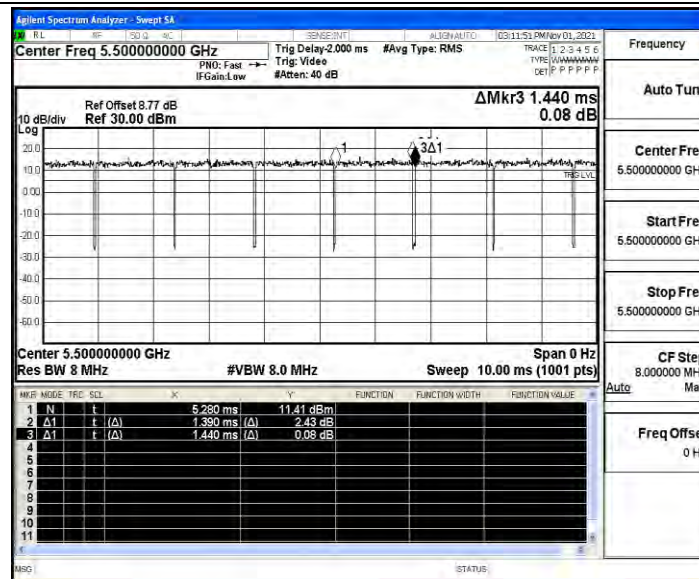
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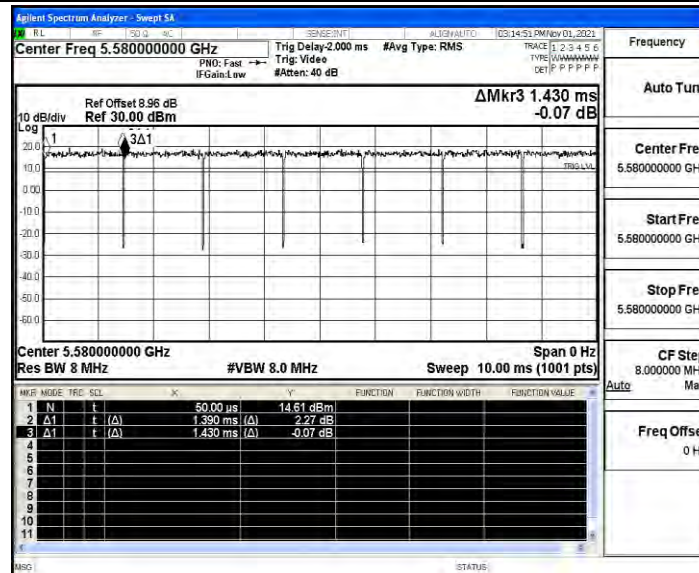
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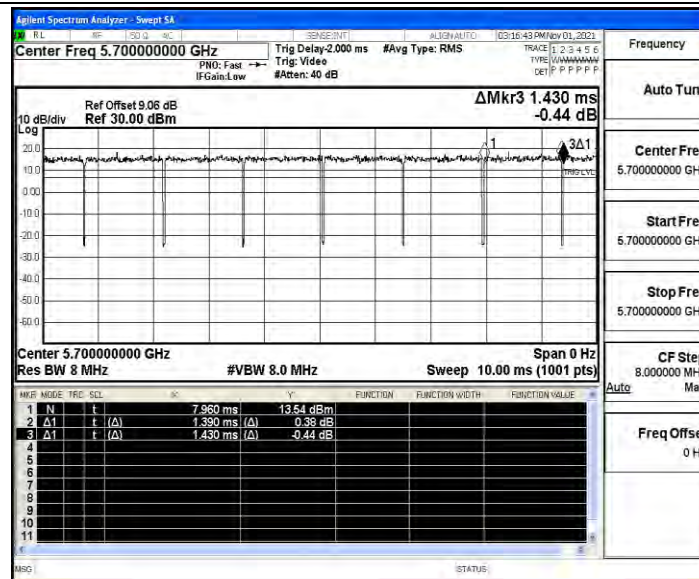
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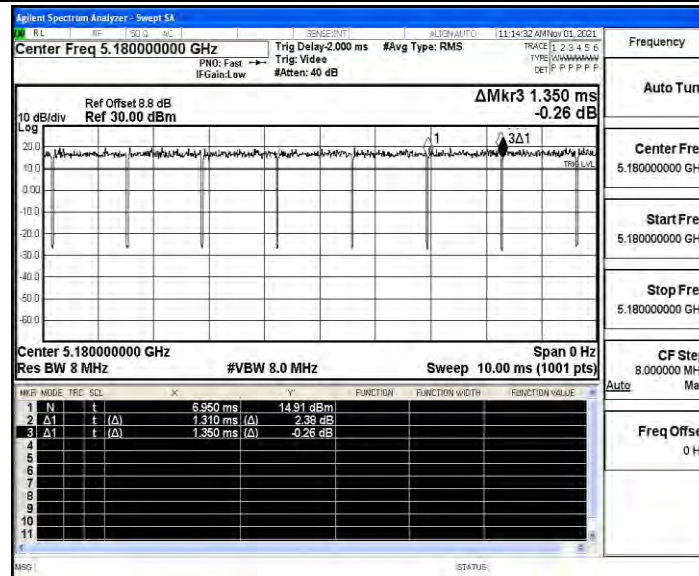
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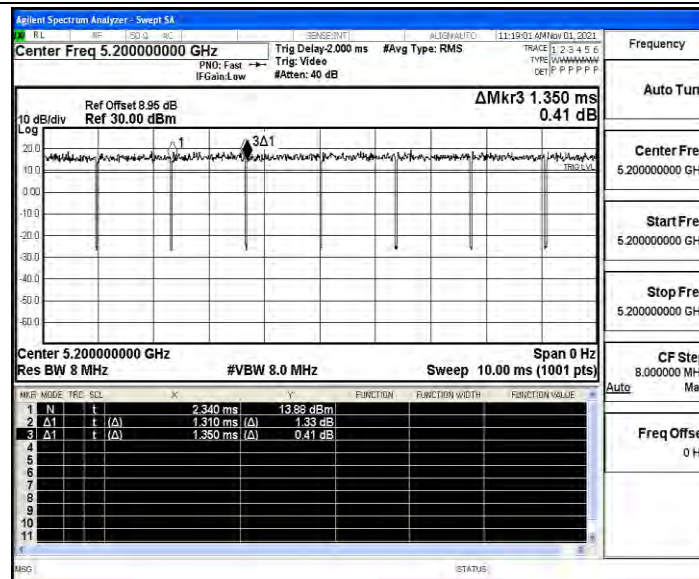
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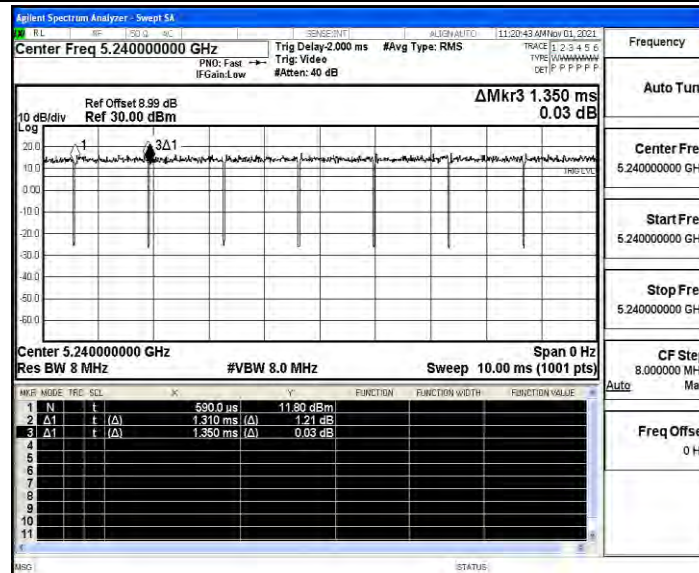
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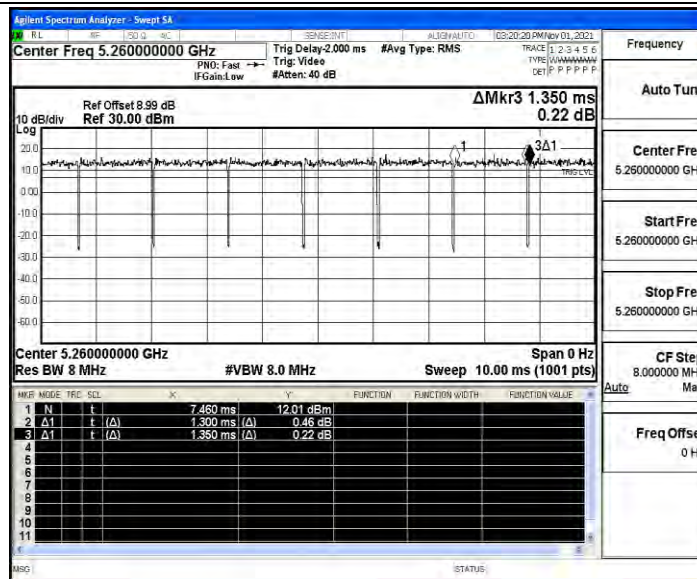
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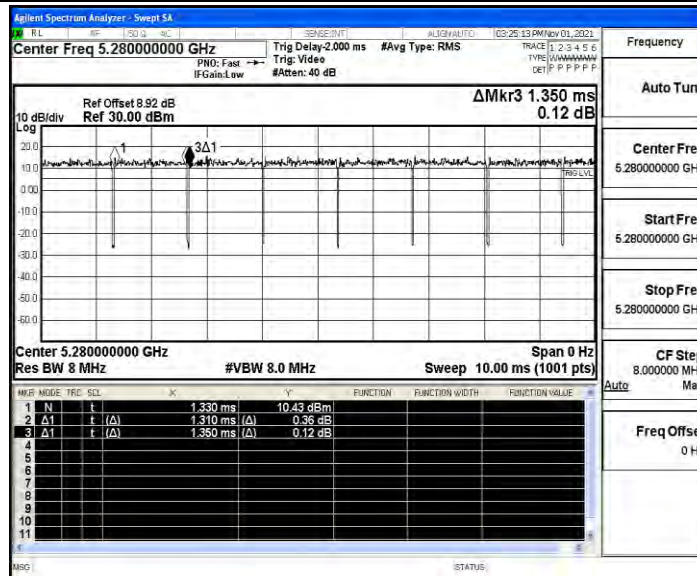
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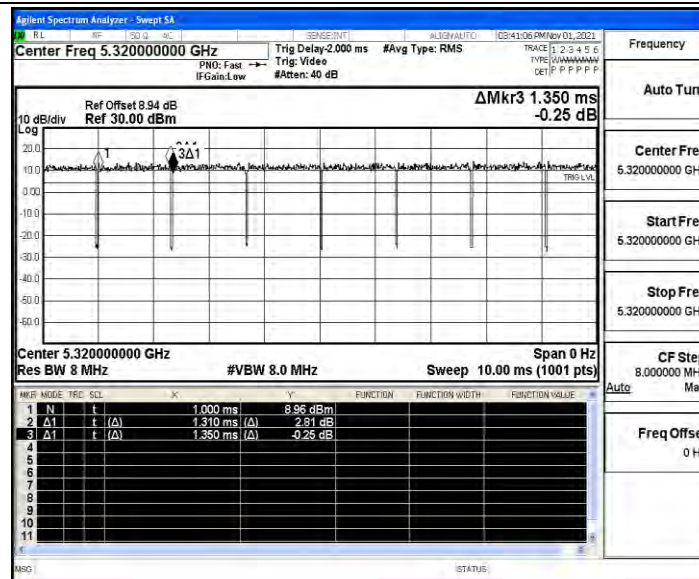
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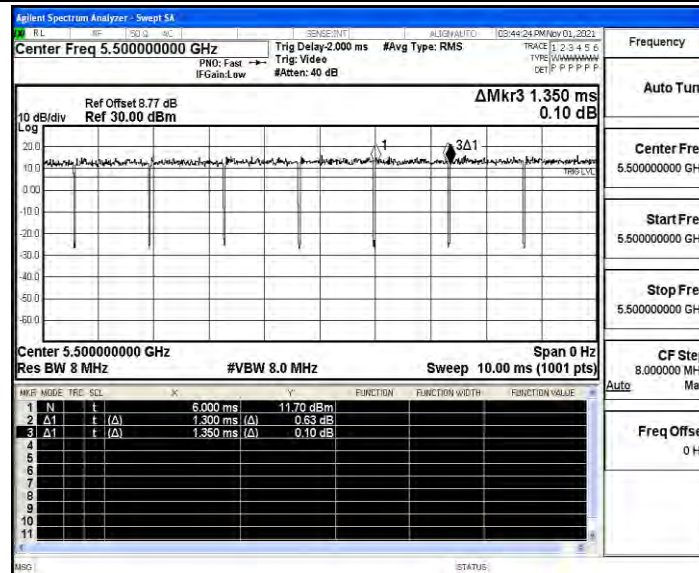
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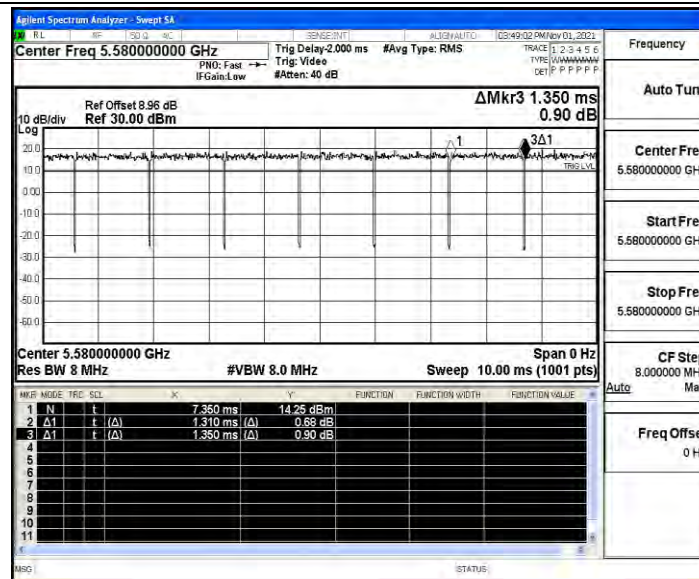
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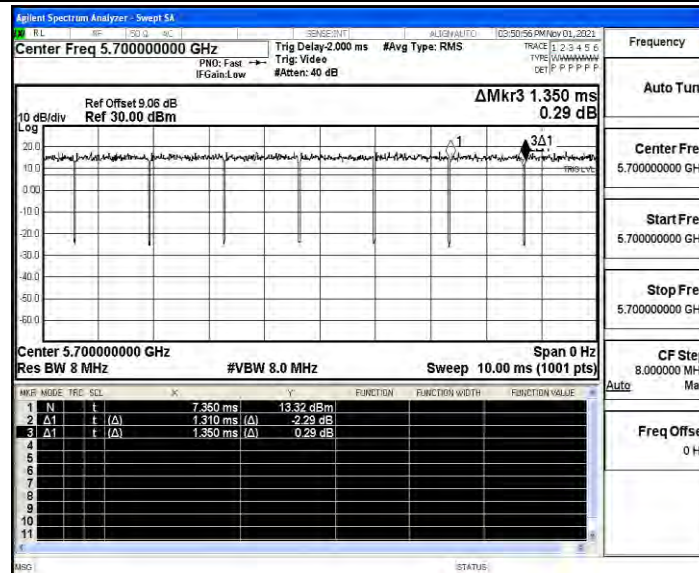
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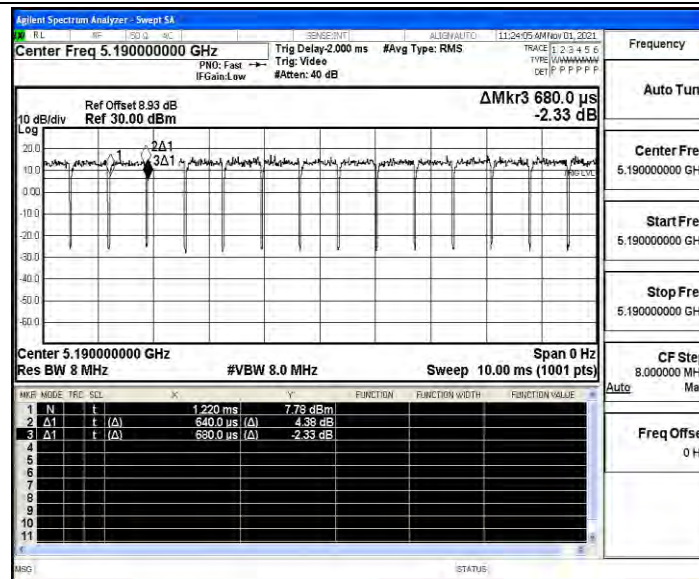
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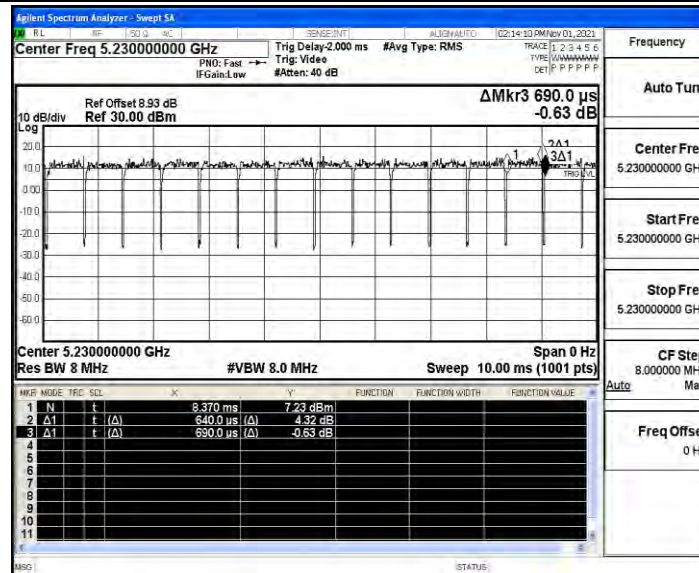
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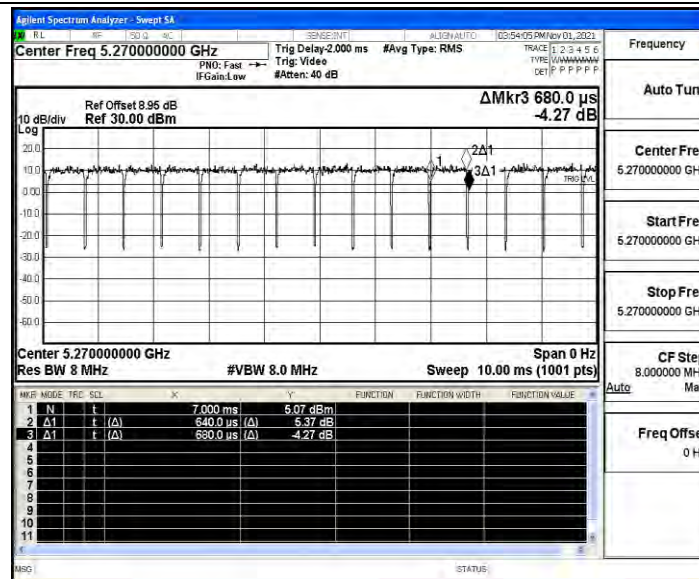
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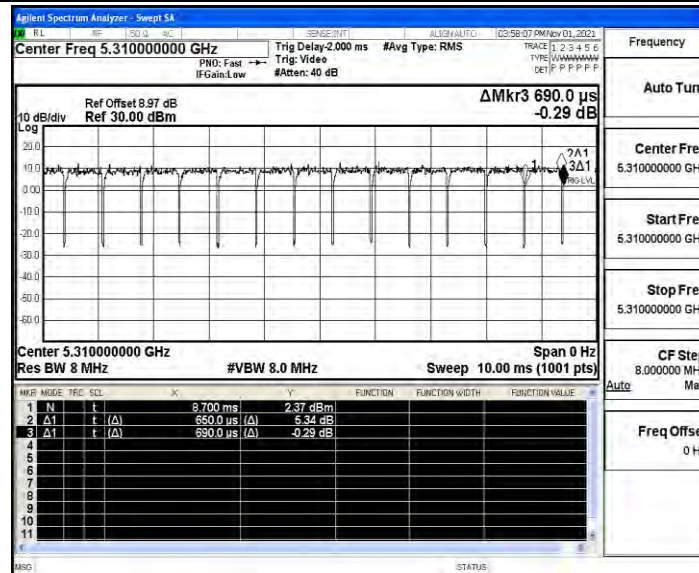
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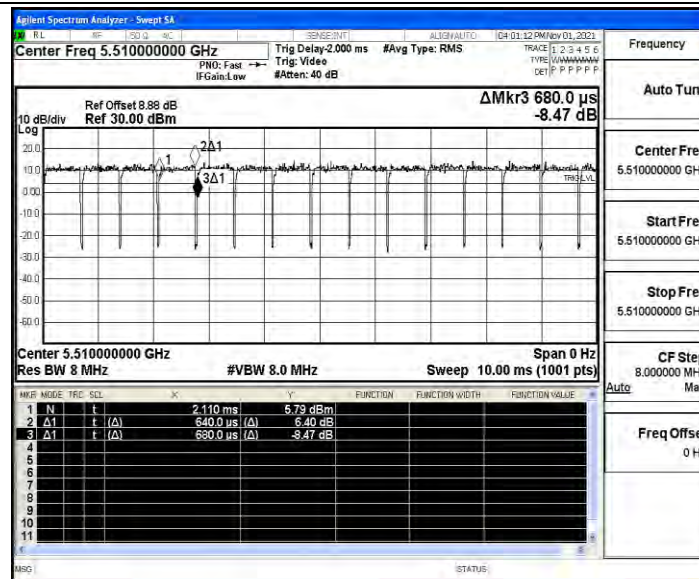
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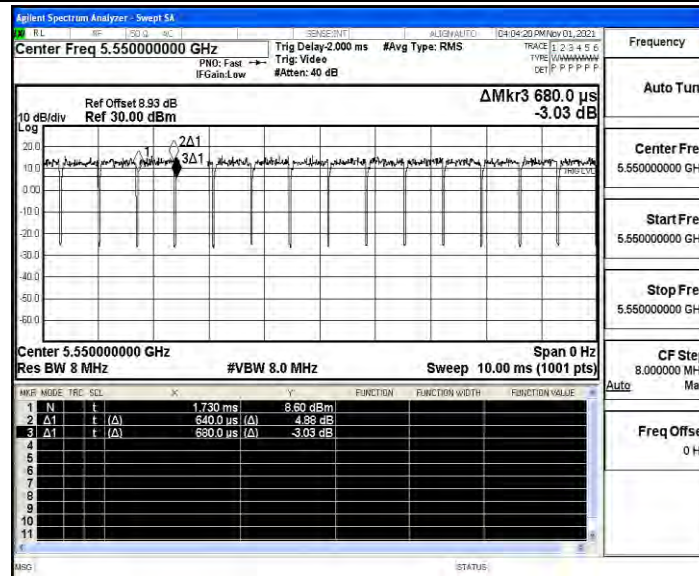
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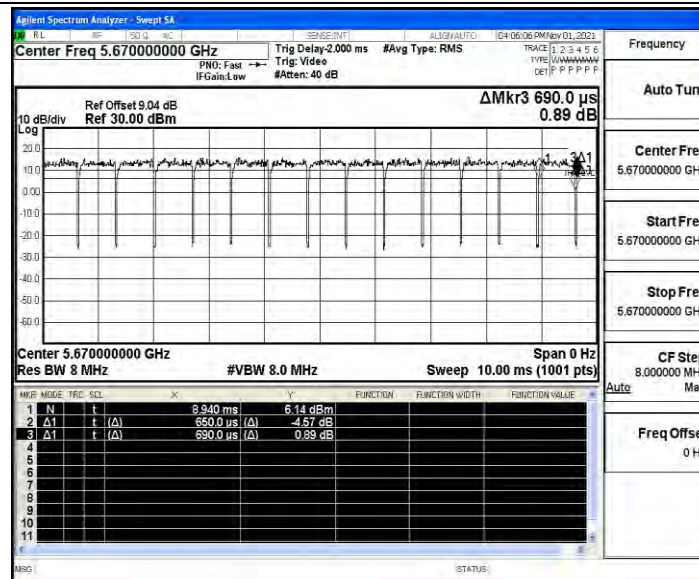
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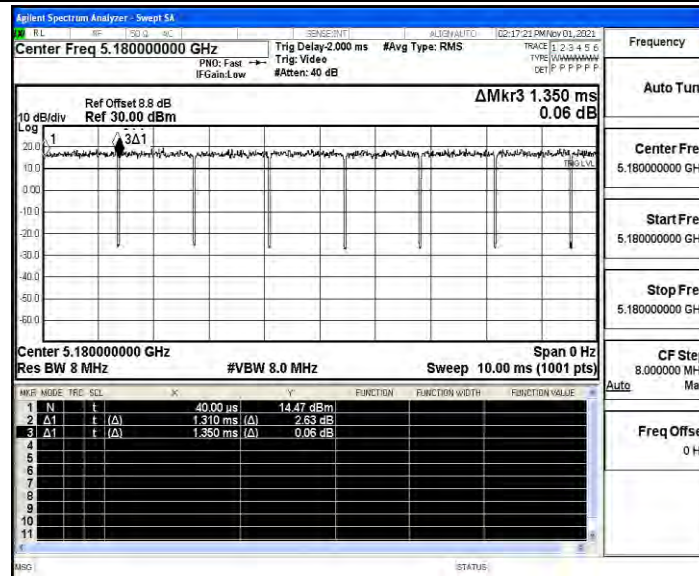
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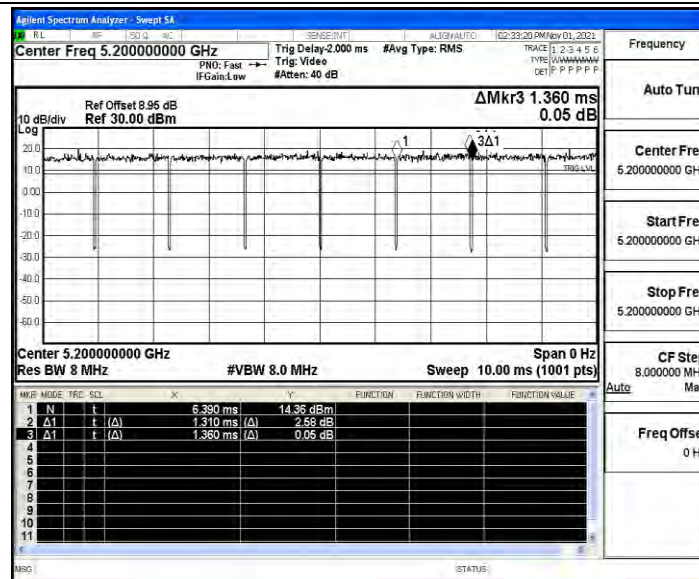
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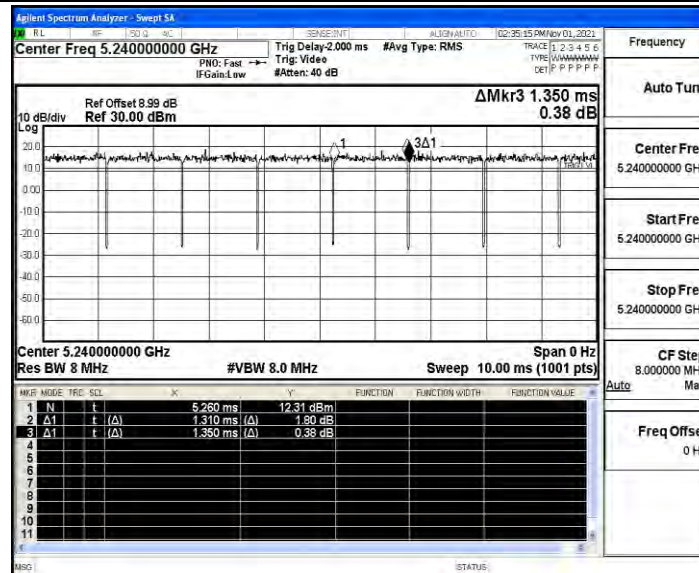
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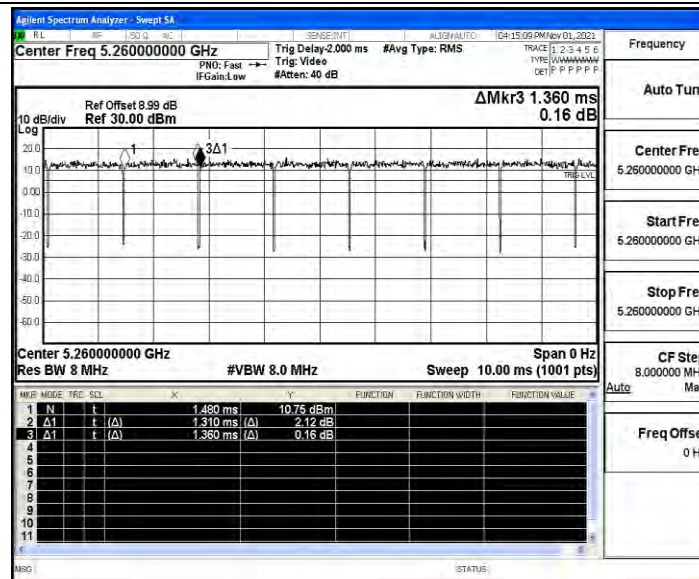
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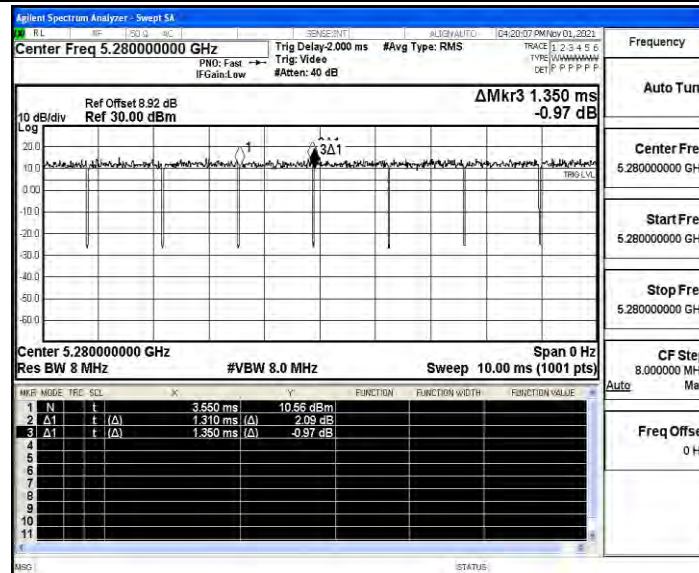
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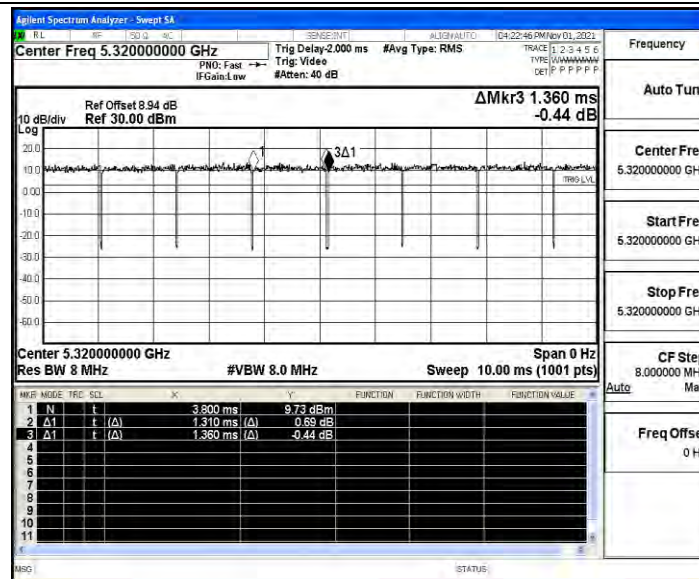
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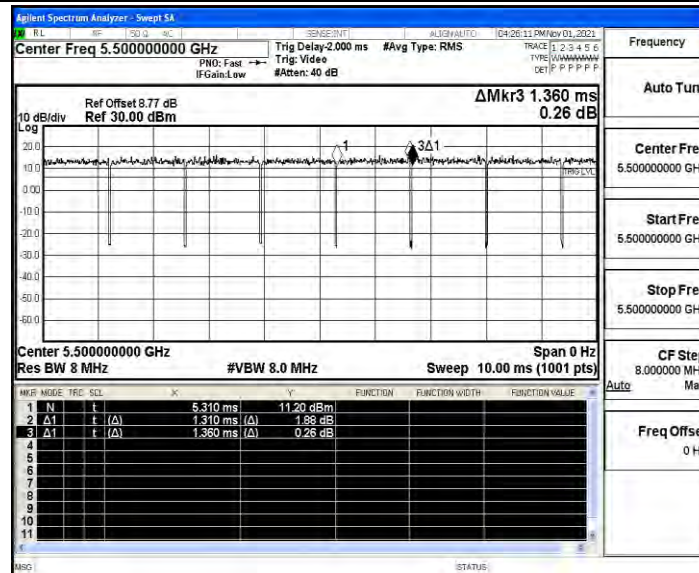
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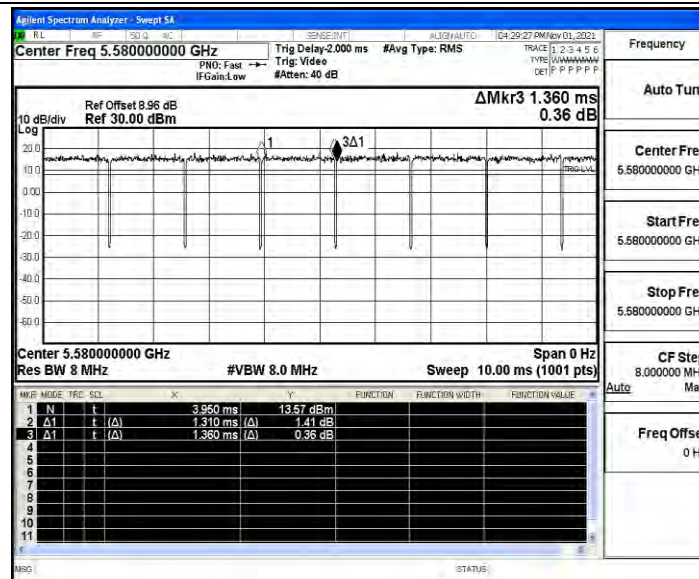
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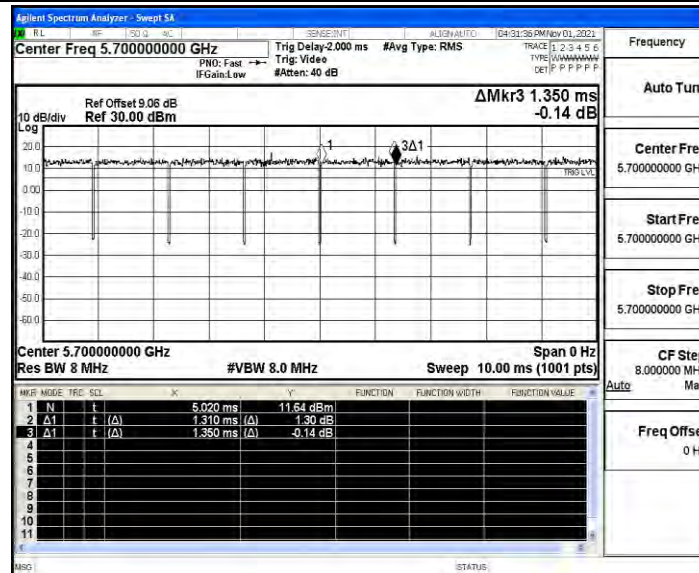
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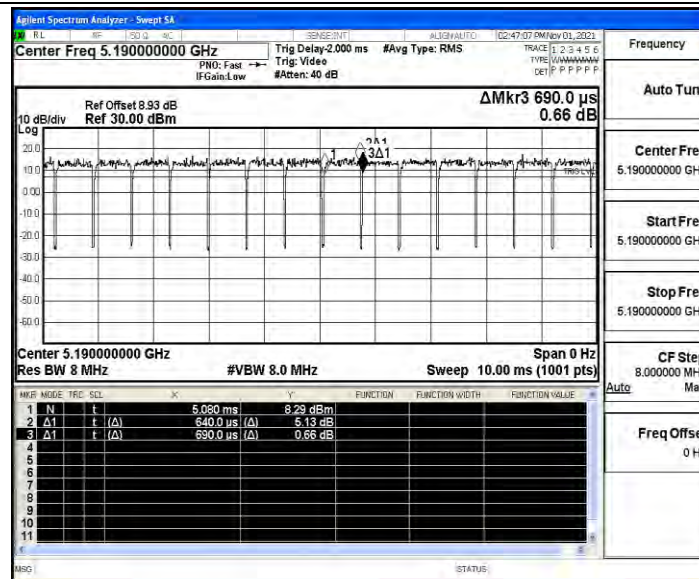
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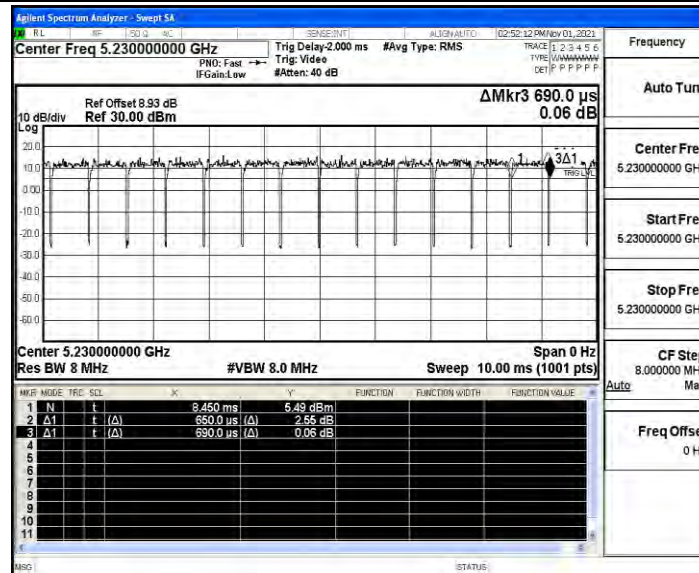
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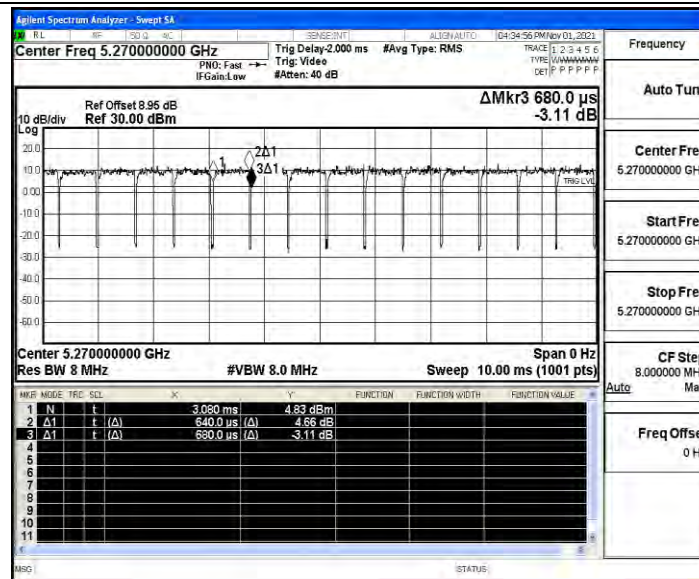
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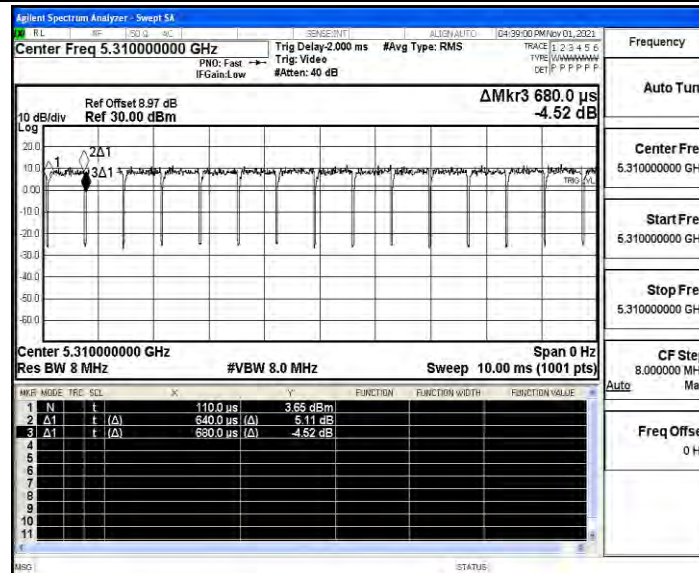
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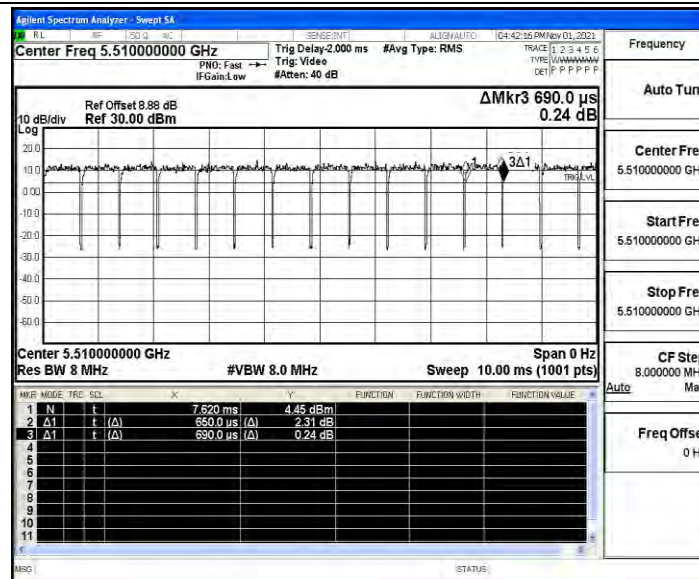
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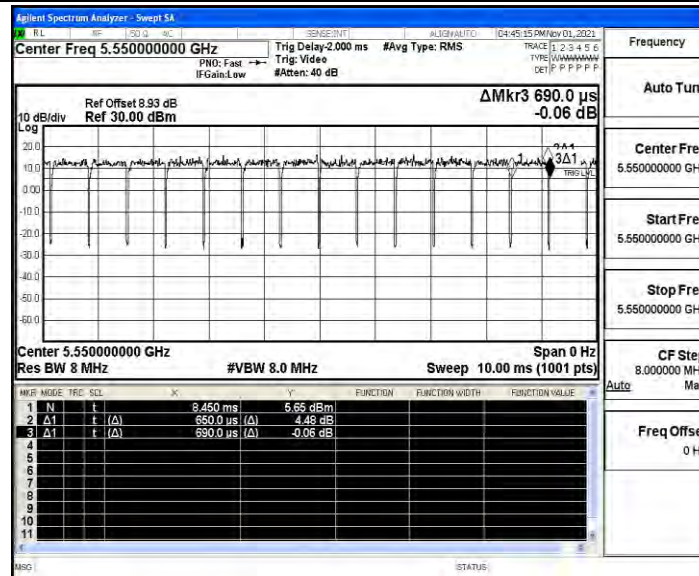
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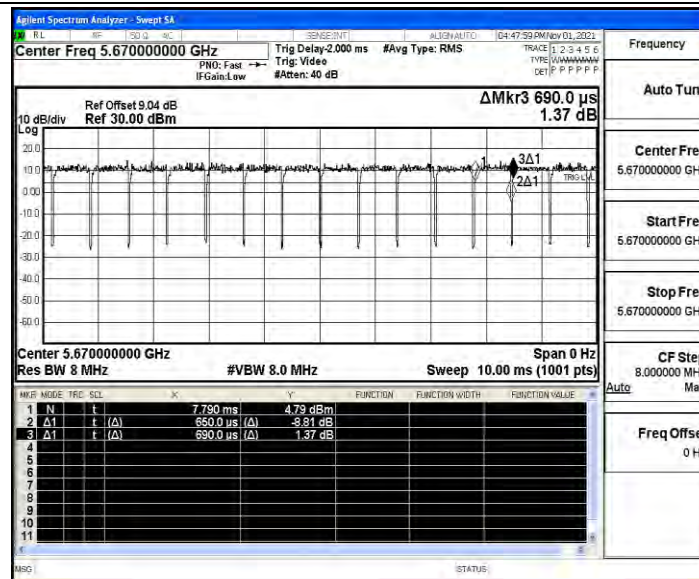
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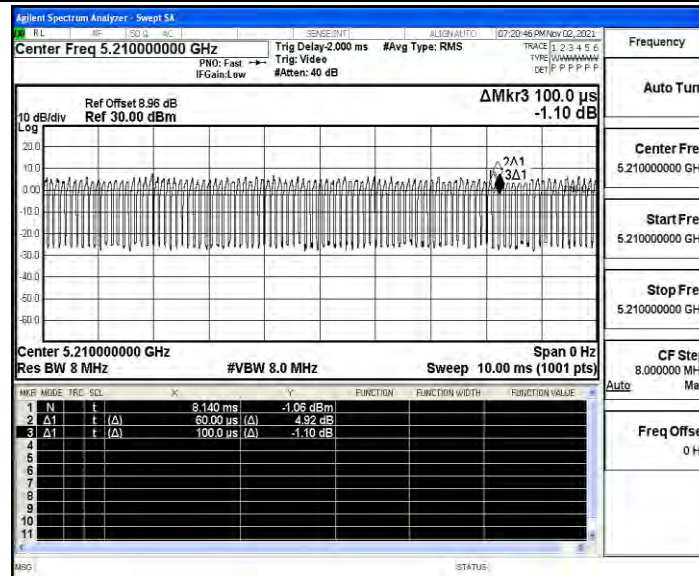
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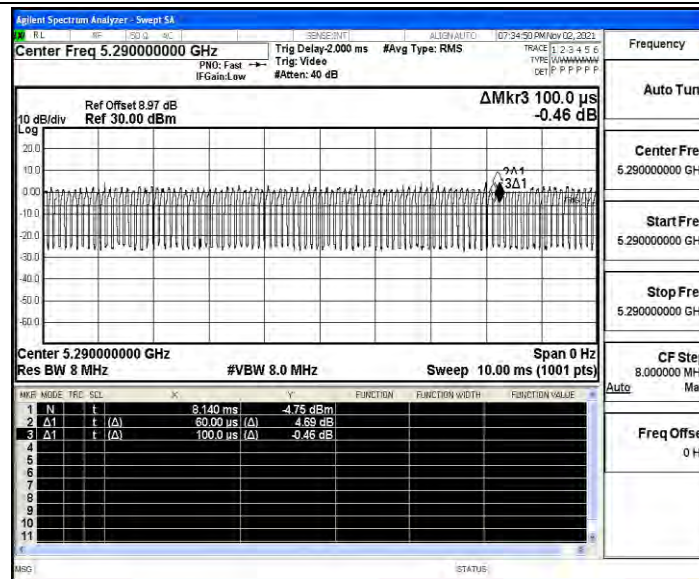
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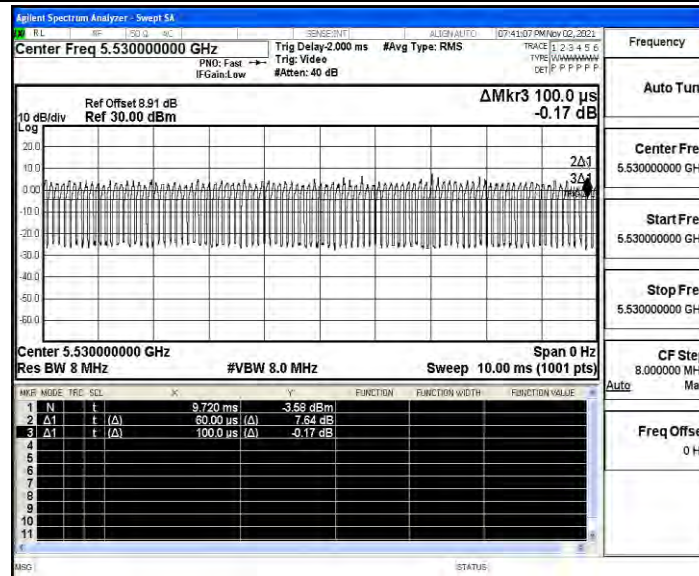
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11AC80SISO_Ant1_5290



11AC80SISO_Ant1_5530



11AC80SISO_Ant1_5610

