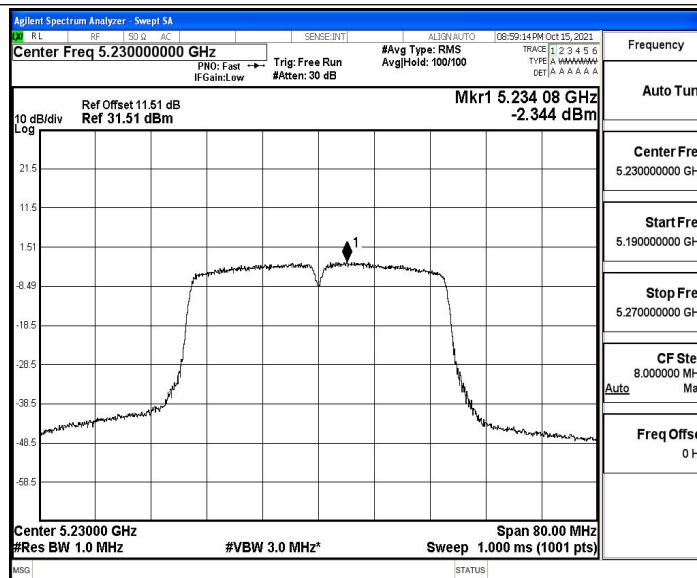
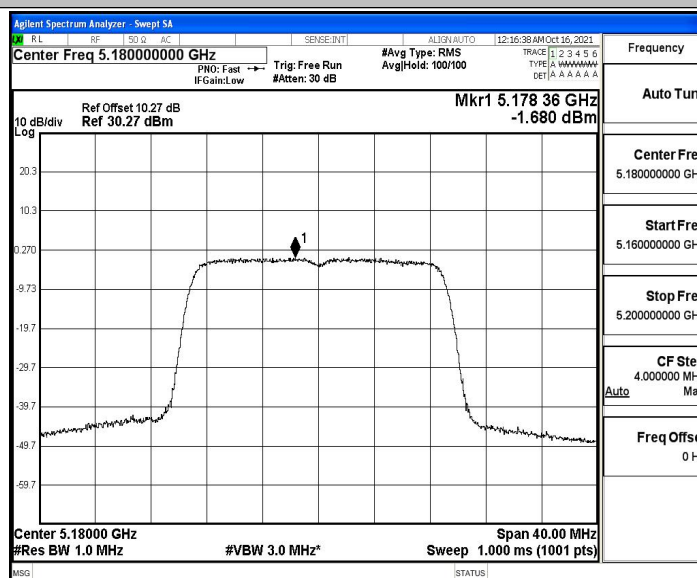
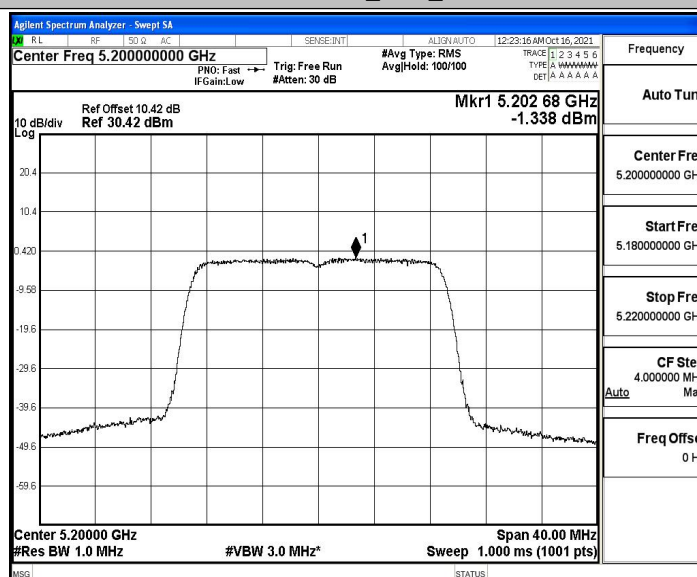




11AC20SISO_Ant1_5180

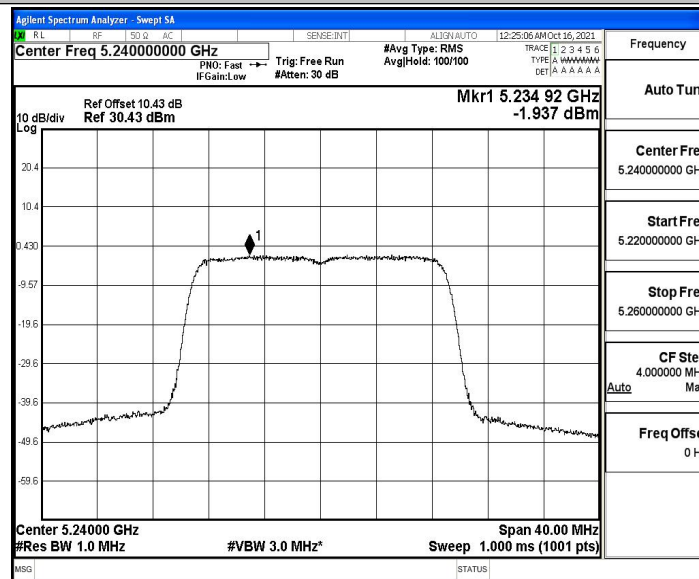


11AC20SISO_Ant1_5200

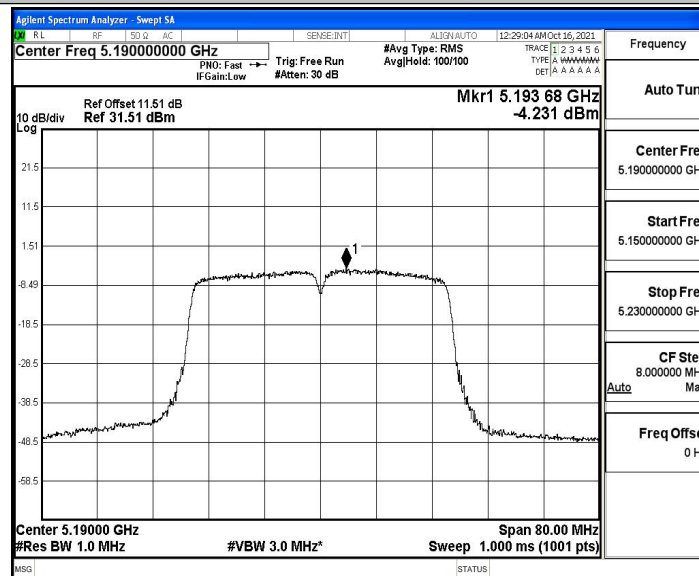




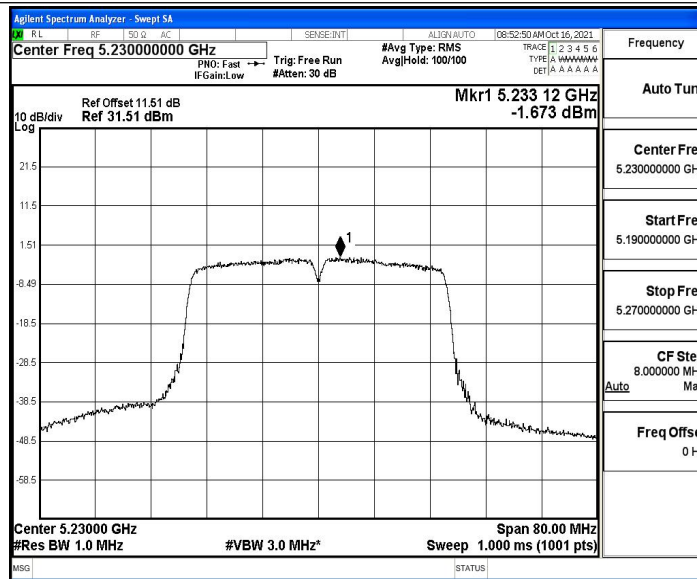
11AC20SISO_Ant1_5240



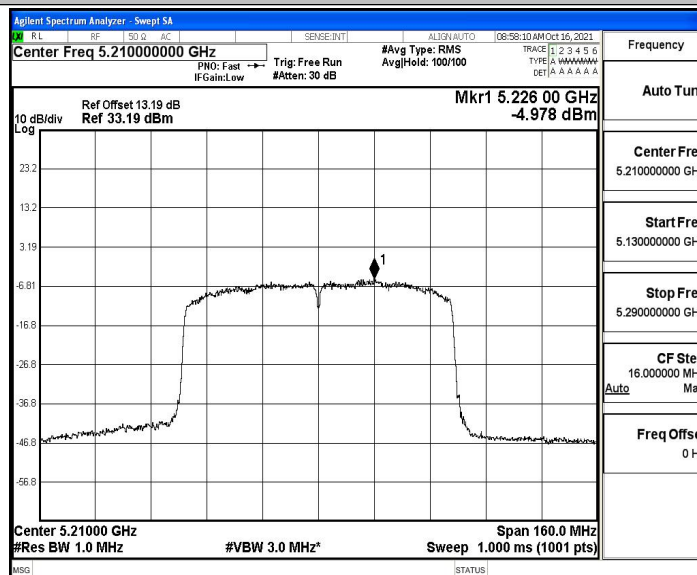
11AC40SISO_Ant1_5190



11AC40SISO_Ant1_5230



11AC80SISO_Ant1_5210



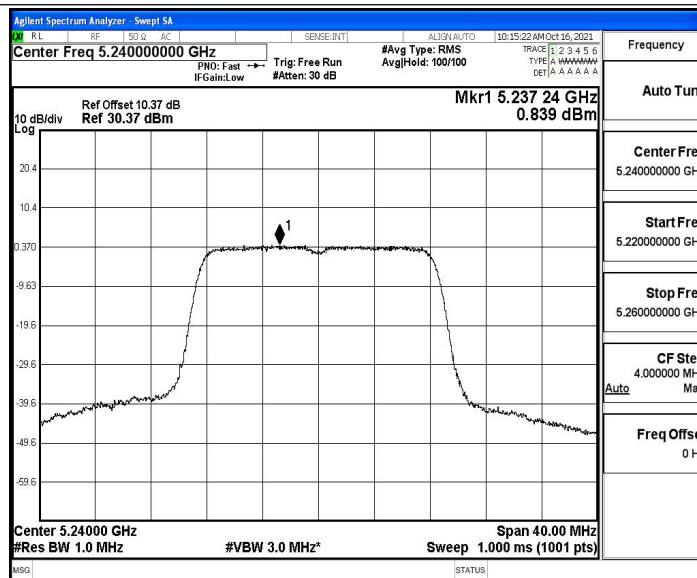


ANT2

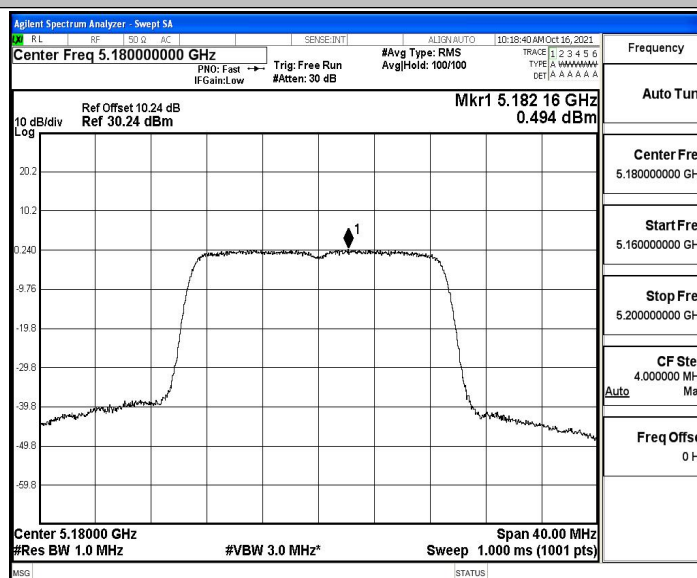
TestMode	Antenna	Channel	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant2	5180	0.67	≤11	PASS
		5200	0.57	≤11	PASS
		5240	0.84	≤11	PASS
11N20SISO	Ant2	5180	0.49	≤11	PASS
		5200	0.55	≤11	PASS
		5240	0.5	≤11	PASS
11N40SISO	Ant2	5190	-1.77	≤11	PASS
		5230	-1.68	≤11	PASS
11AC20SISO	Ant2	5180	0.56	≤11	PASS
		5200	1.15	≤11	PASS
		5240	0.78	≤11	PASS
11AC40SISO	Ant2	5190	-1.97	≤11	PASS
		5230	-1.52	≤11	PASS
11AC80SISO	Ant2	5210	-4.91	≤11	PASS

Note: 1.The Duty Cycle Factor and RBW Factor is compensated in the graph.

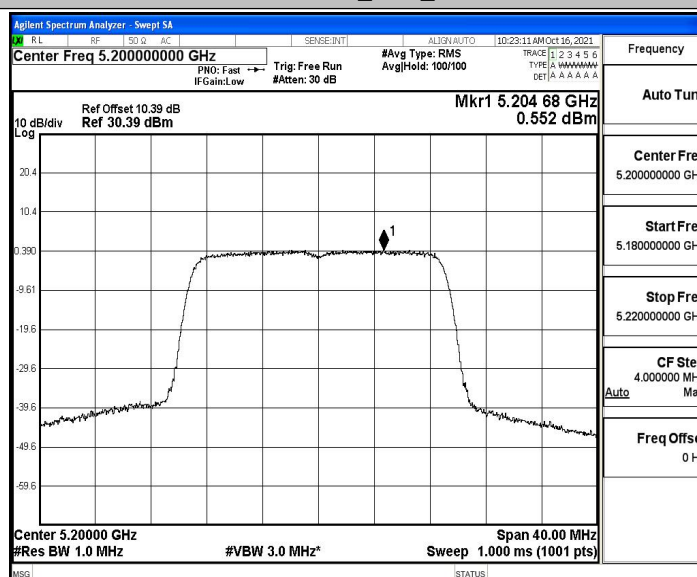




11N20SISO_Ant2_5180

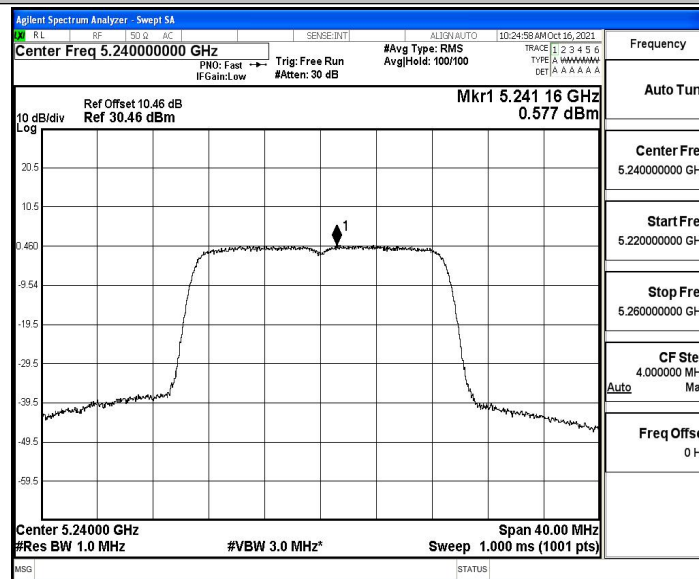


11N20SISO_Ant2_5200

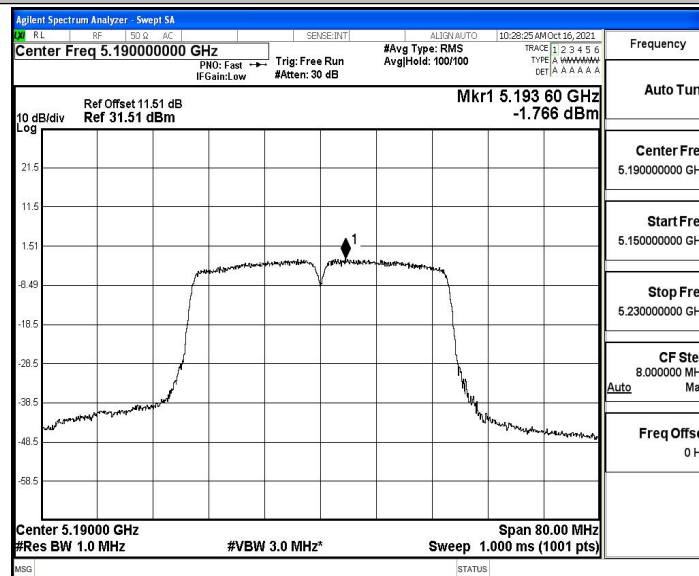




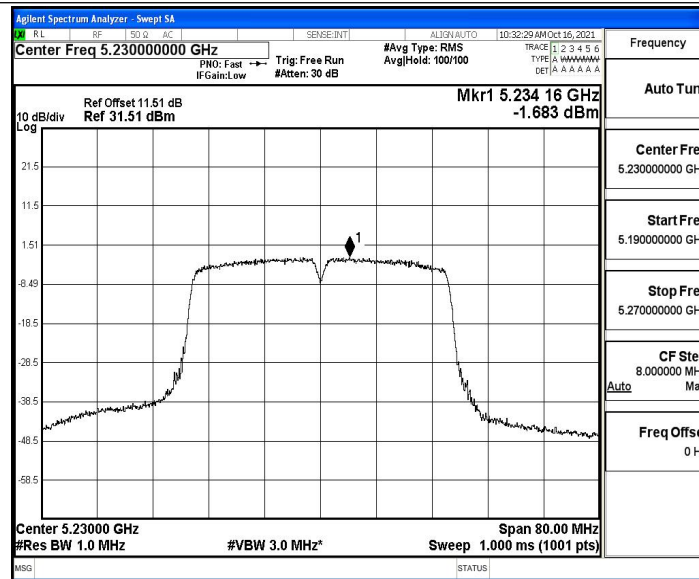
11N20SISO_Ant2_5240



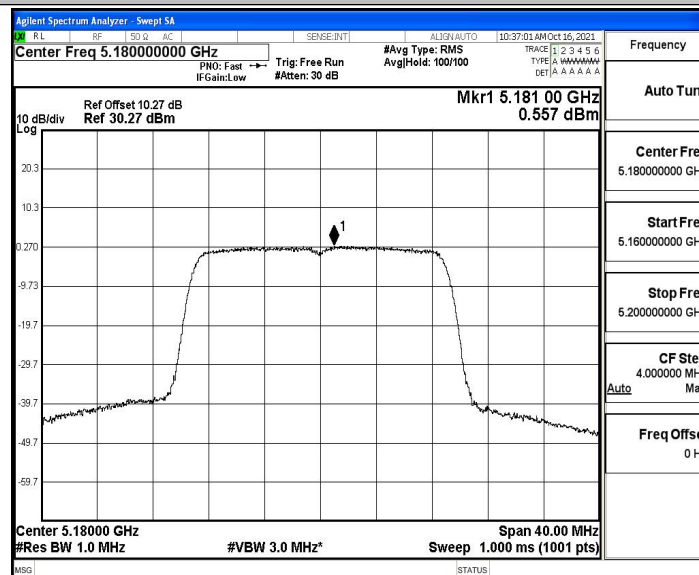
11N40SISO_Ant2_5190



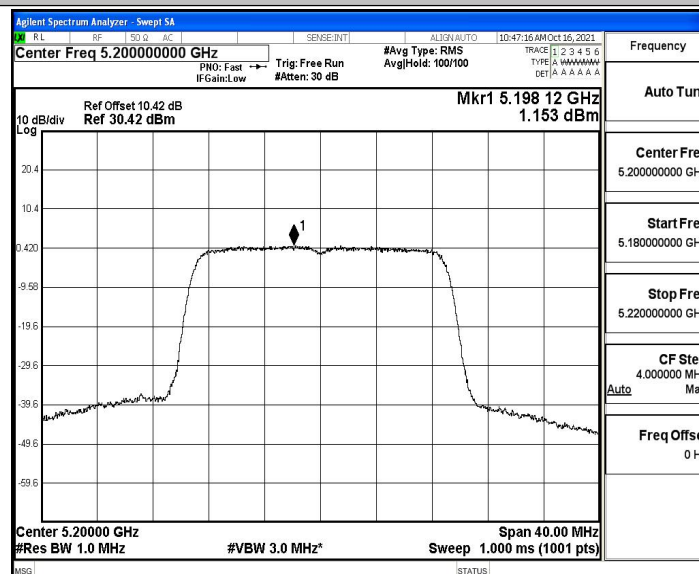
11N40SISO_Ant2_5230



11AC20SISO_Ant2_5180

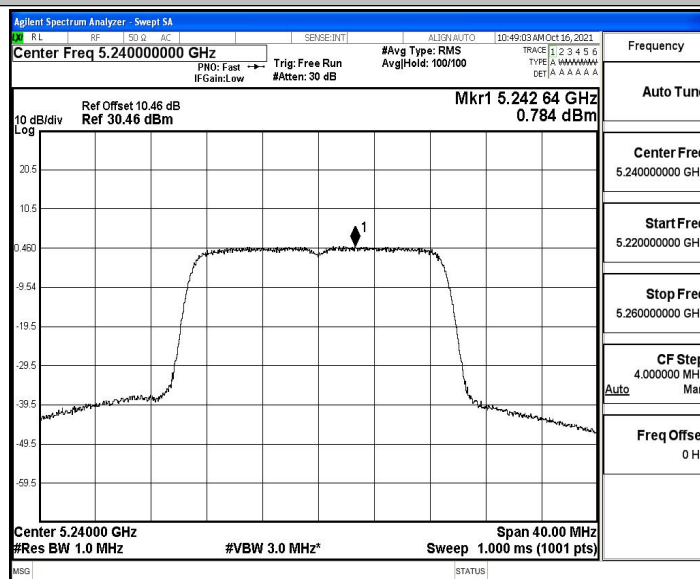


11AC20SISO_Ant2_5200

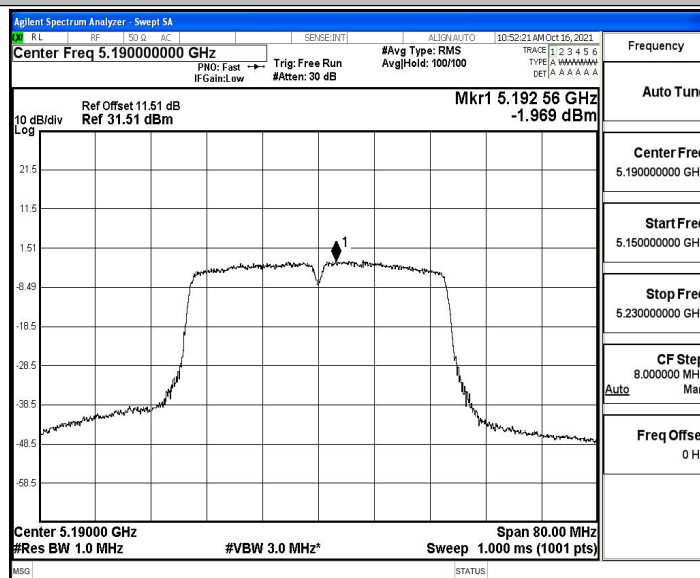




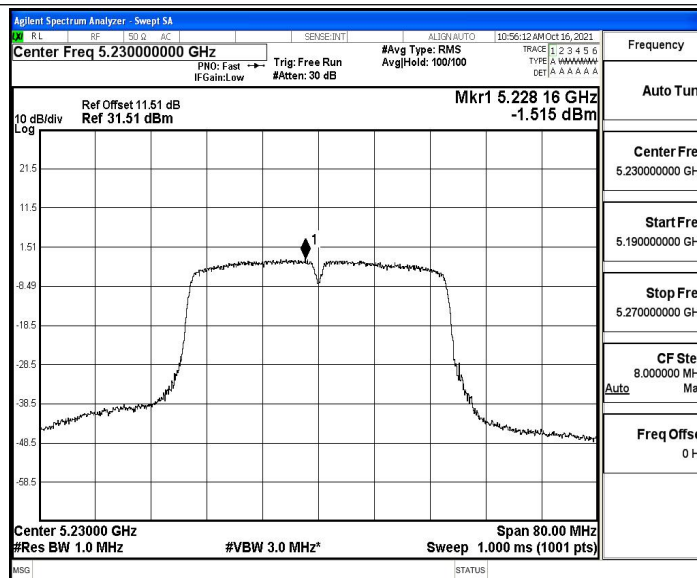
11AC20SISO_Ant2_5240



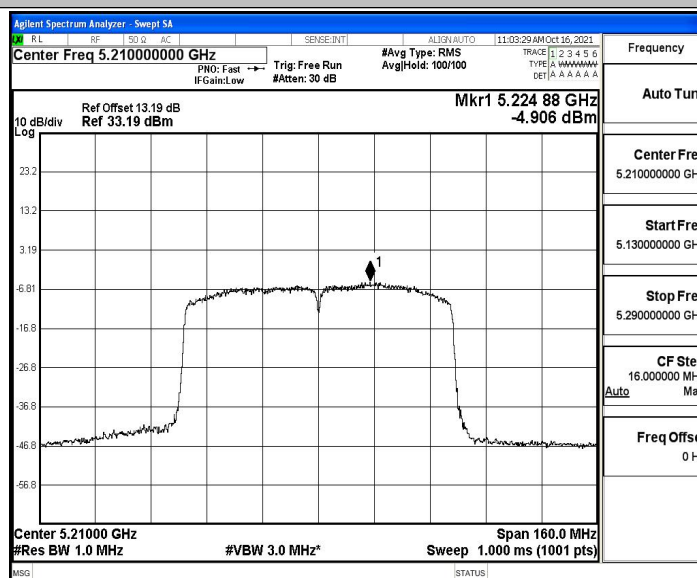
11AC40SISO_Ant2_5190



11AC40SISO_Ant2_5230



11AC80SISO_Ant2_5210





ANT3

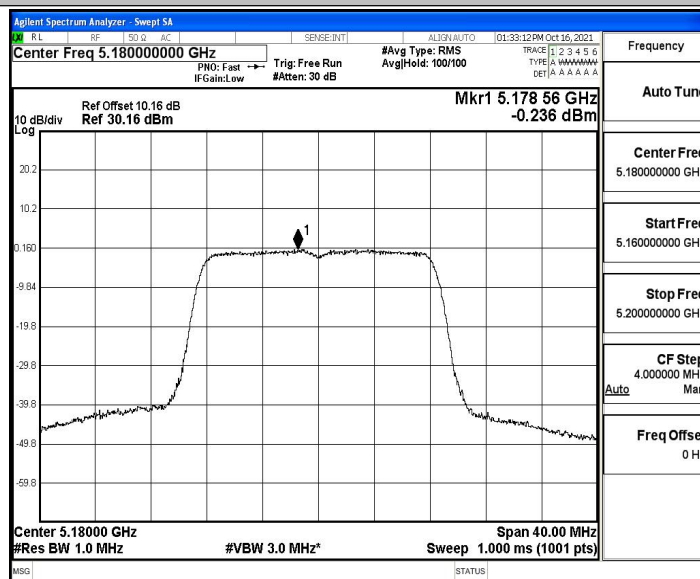
TestMode	Antenna	Channel	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant3	5180	-0.24	≤11	PASS
		5200	-0.37	≤11	PASS
		5240	0.58	≤11	PASS
11N20SISO	Ant3	5180	-0.11	≤11	PASS
		5200	-0.22	≤11	PASS
		5240	0.32	≤11	PASS
11N40SISO	Ant3	5190	-2.16	≤11	PASS
		5230	-1.88	≤11	PASS
11AC20SISO	Ant3	5180	-0.06	≤11	PASS
		5200	0.19	≤11	PASS
		5240	0.22	≤11	PASS
11AC40SISO	Ant3	5190	-2.24	≤11	PASS
		5230	-2.04	≤11	PASS
11AC80SISO	Ant3	5210	-4.36	≤11	PASS

Note: 1.The Duty Cycle Factor and RBW Factor is compensated in the graph.

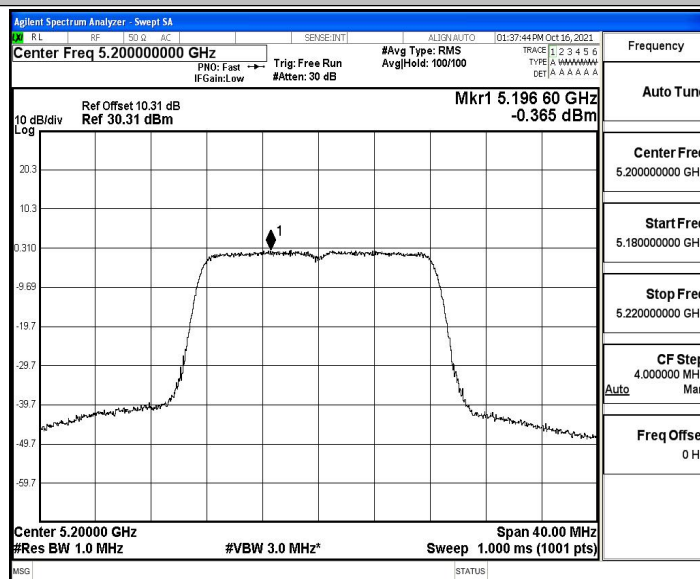


Test Graphs

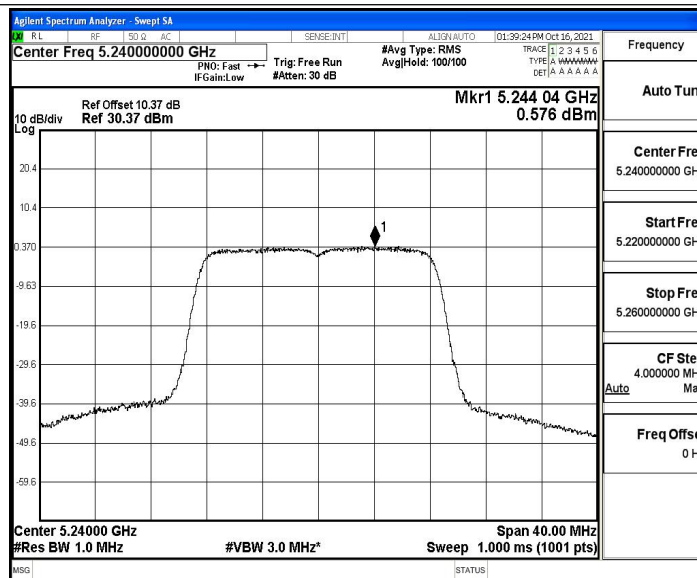
11A_Ant3_5180



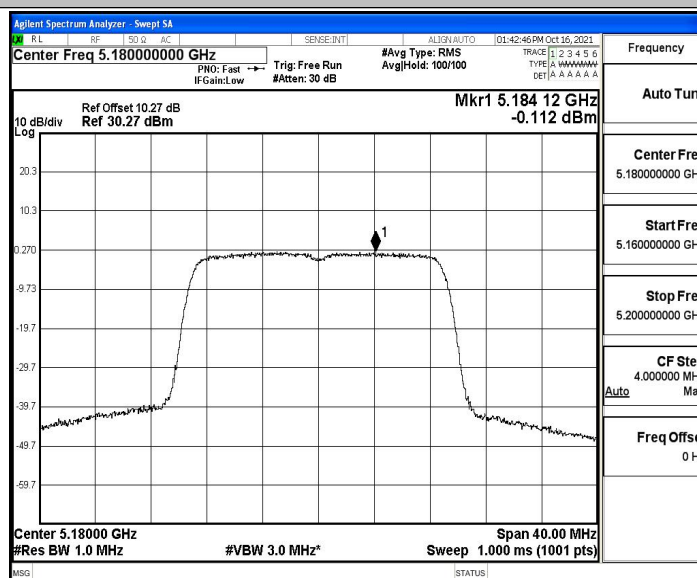
11A_Ant3_5200



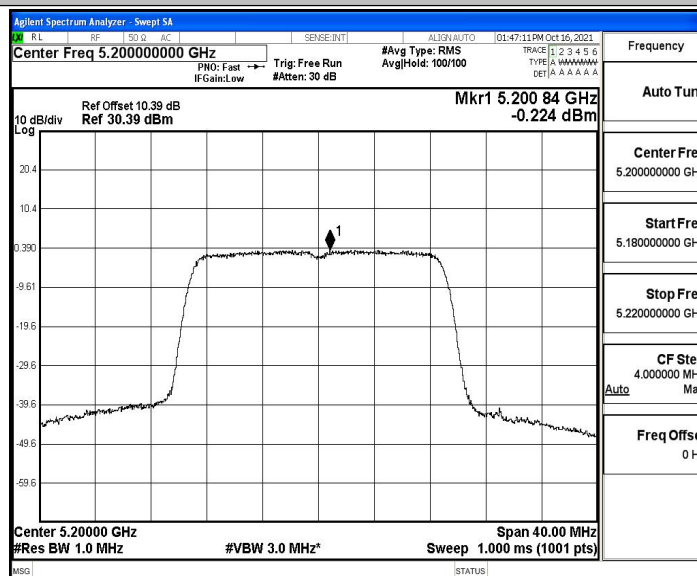
11A_Ant3_5240



11N20SISO_Ant3_5180

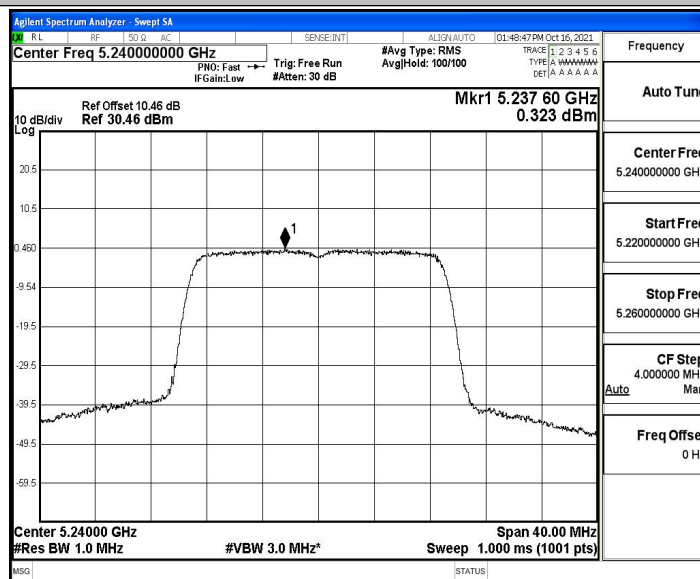


11N20SISO_Ant3_5200

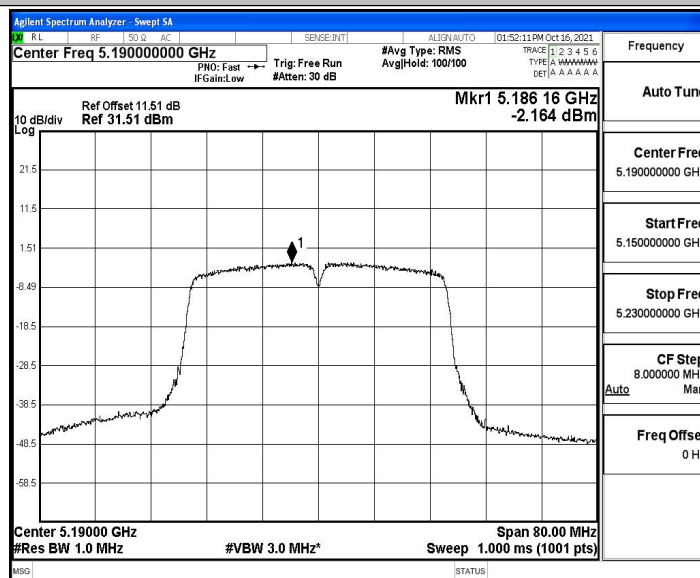




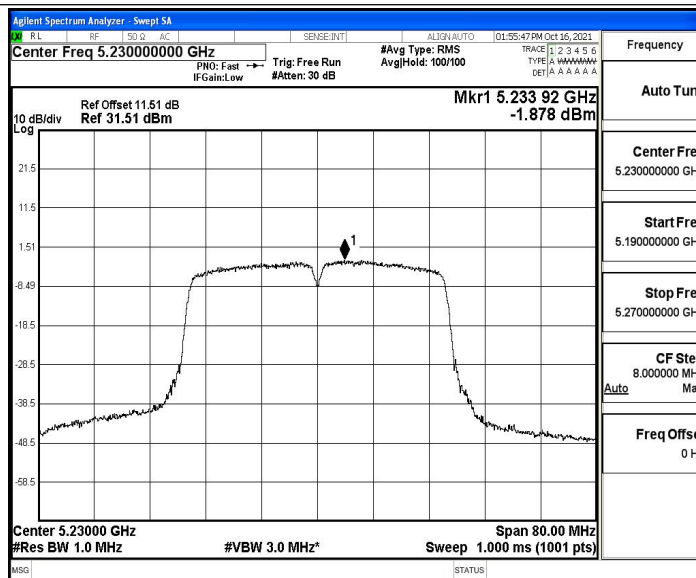
11N20SISO_Ant3_5240



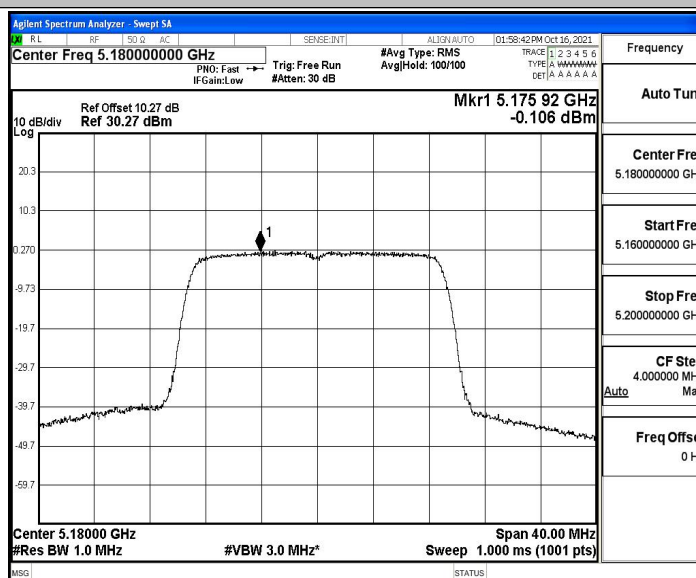
11N40SISO_Ant3_5190



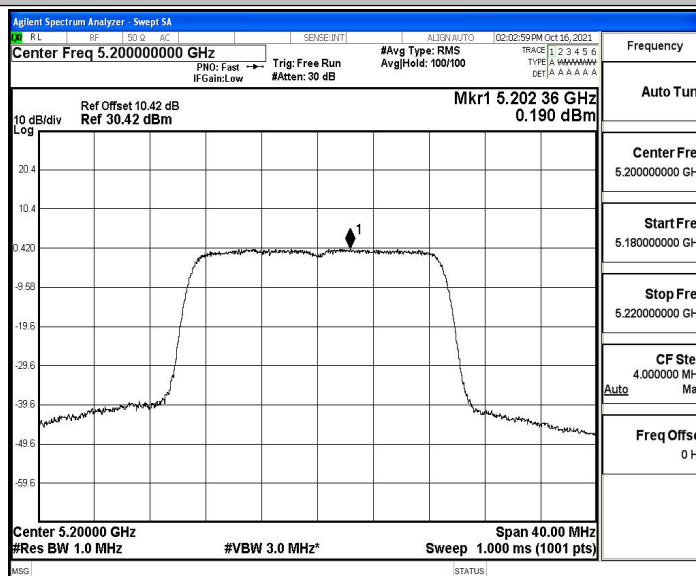
11N40SISO_Ant3_5230



11AC20SISO_Ant3_5180

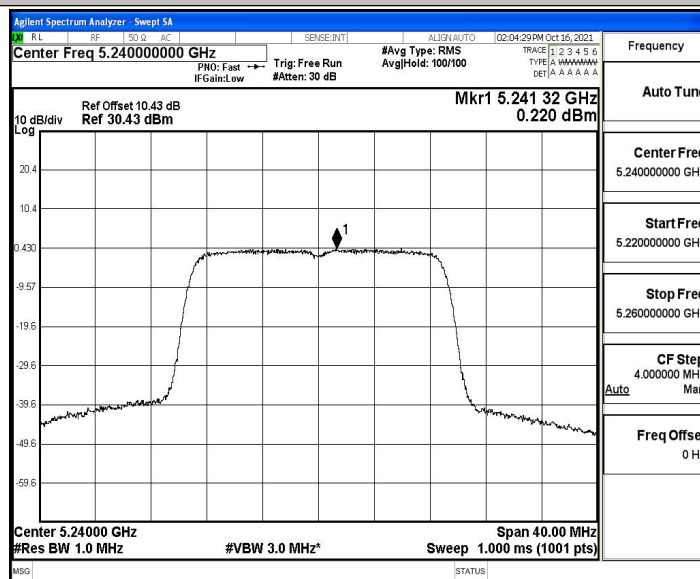


11AC20SISO_Ant3_5200

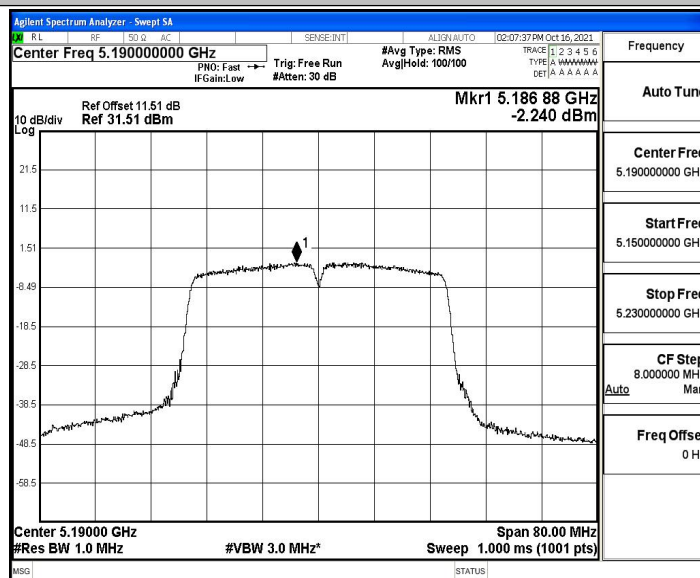




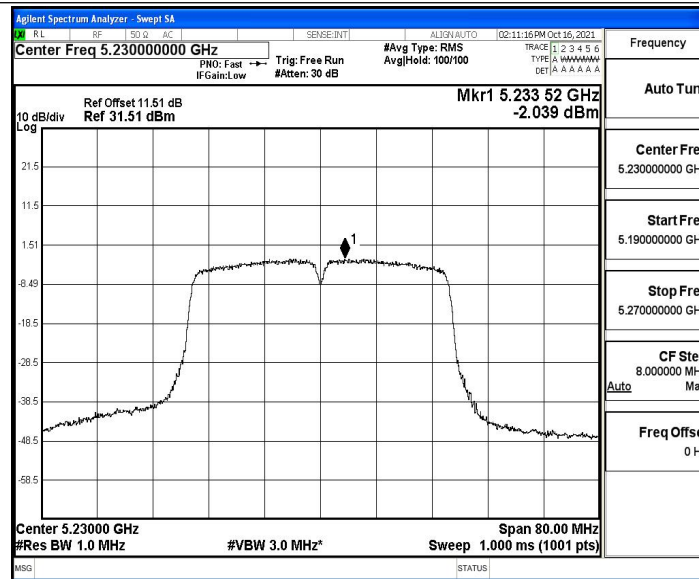
11AC20SISO_Ant3_5240



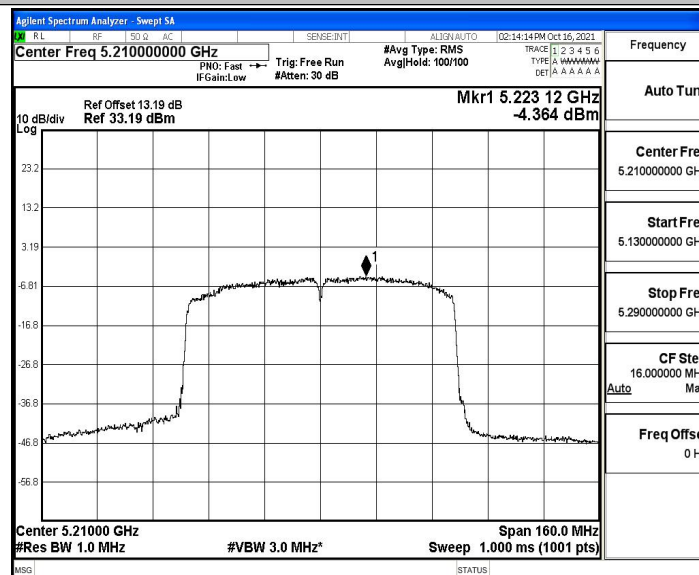
11AC40SISO_Ant3_5190



11AC40SISO_Ant3_5230



11AC80SISO_Ant3_5210





ANT4

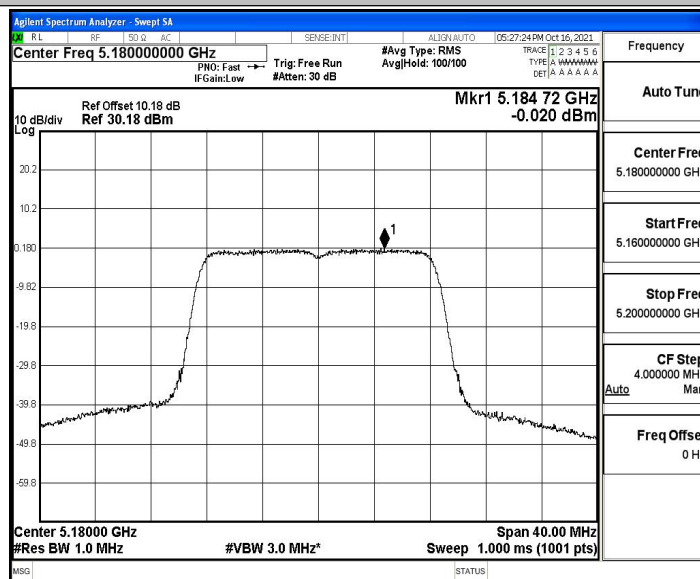
TestMode	Antenna	Channel	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant4	5180	-0.02	≤11	PASS
		5200	0.85	≤11	PASS
		5240	0.95	≤11	PASS
11N20SISO	Ant4	5180	0.18	≤11	PASS
		5200	1.18	≤11	PASS
		5240	0.72	≤11	PASS
11N40SISO	Ant4	5190	-2.1	≤11	PASS
		5230	-1.67	≤11	PASS
11AC20SISO	Ant4	5180	0.54	≤11	PASS
		5200	0.84	≤11	PASS
		5240	0.85	≤11	PASS
11AC40SISO	Ant4	5190	-1.76	≤11	PASS
		5230	-1.93	≤11	PASS
11AC80SISO	Ant4	5210	-7.35	≤11	PASS

Note: 1.The Duty Cycle Factor and RBW Factor is compensated in the graph.

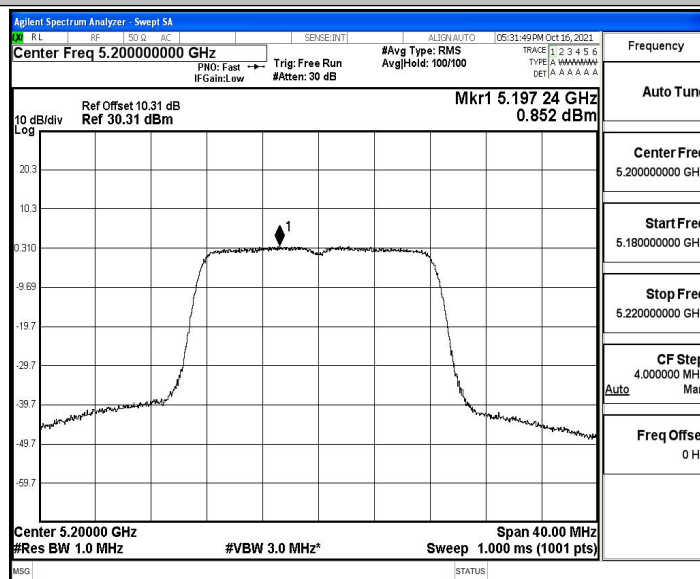


Test Graphs

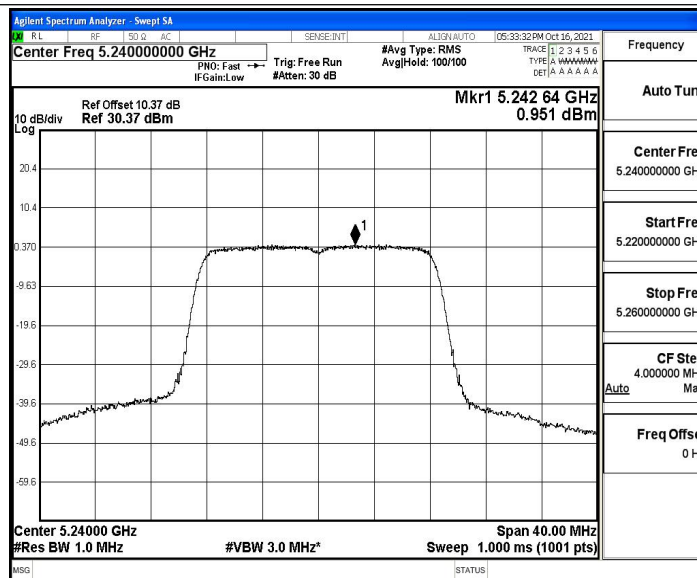
11A_Ant4_5180



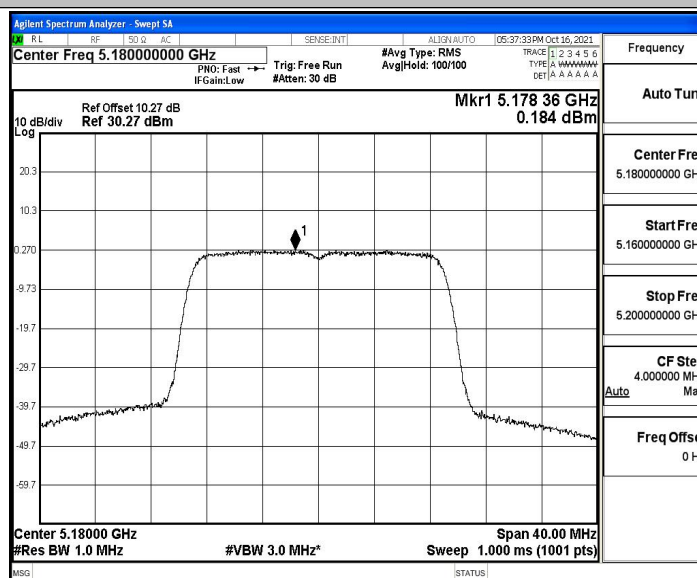
11A_Ant4_5200



11A_Ant4_5240



11N20SISO_Ant4_5180



11N20SISO_Ant4_5200

