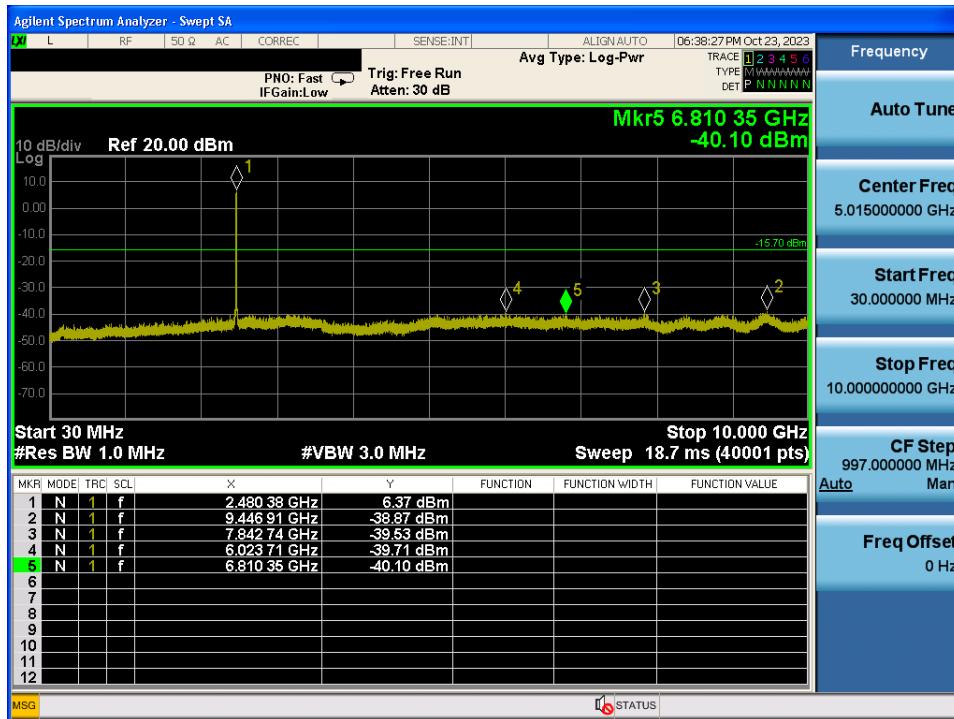
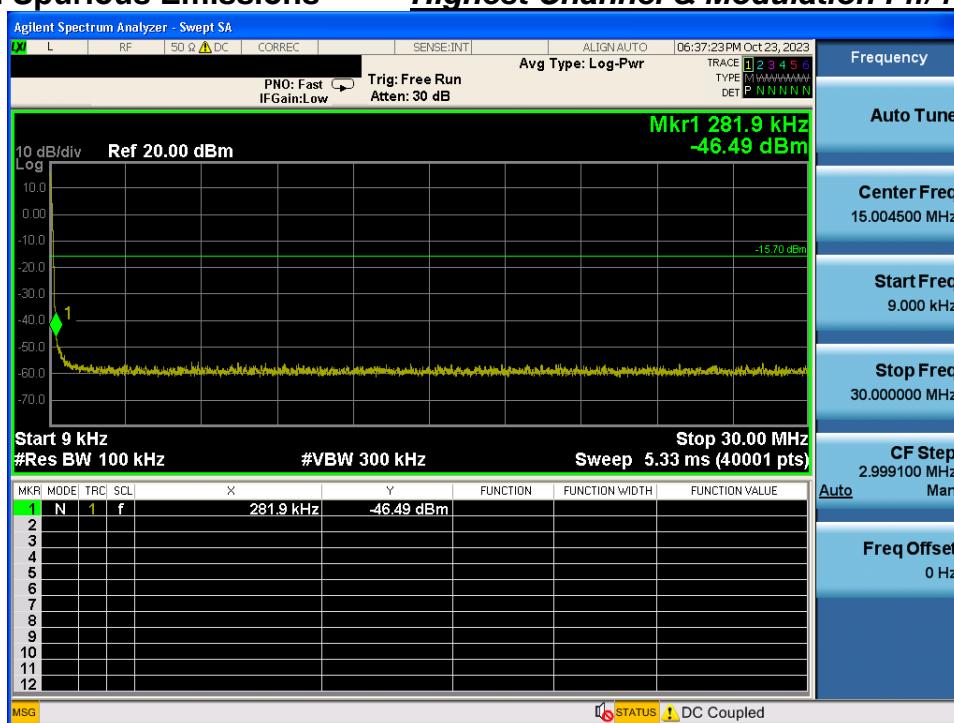


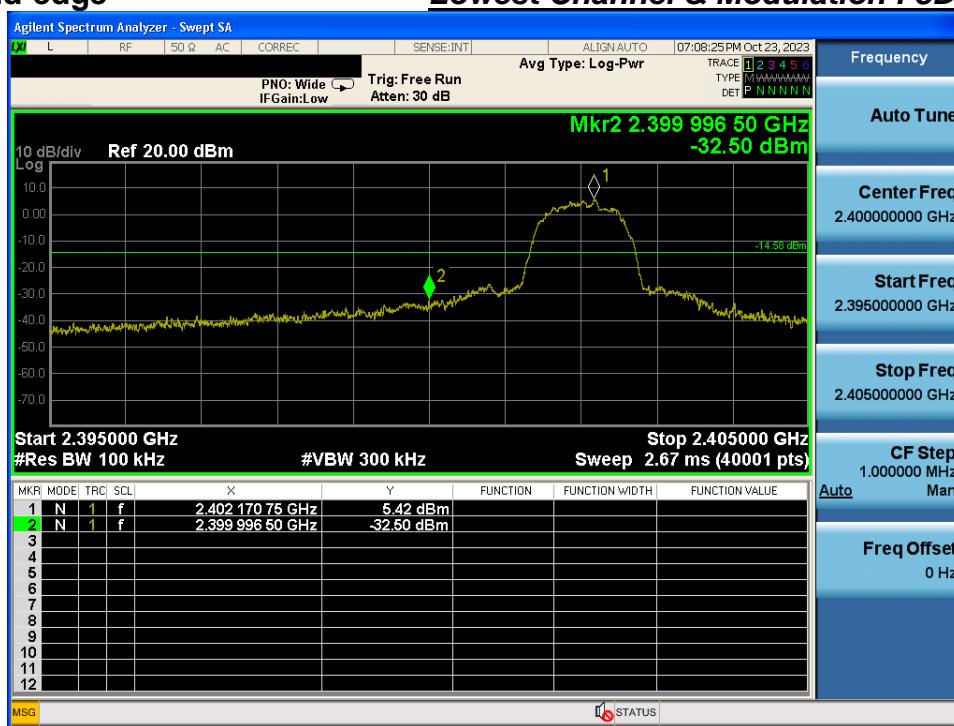
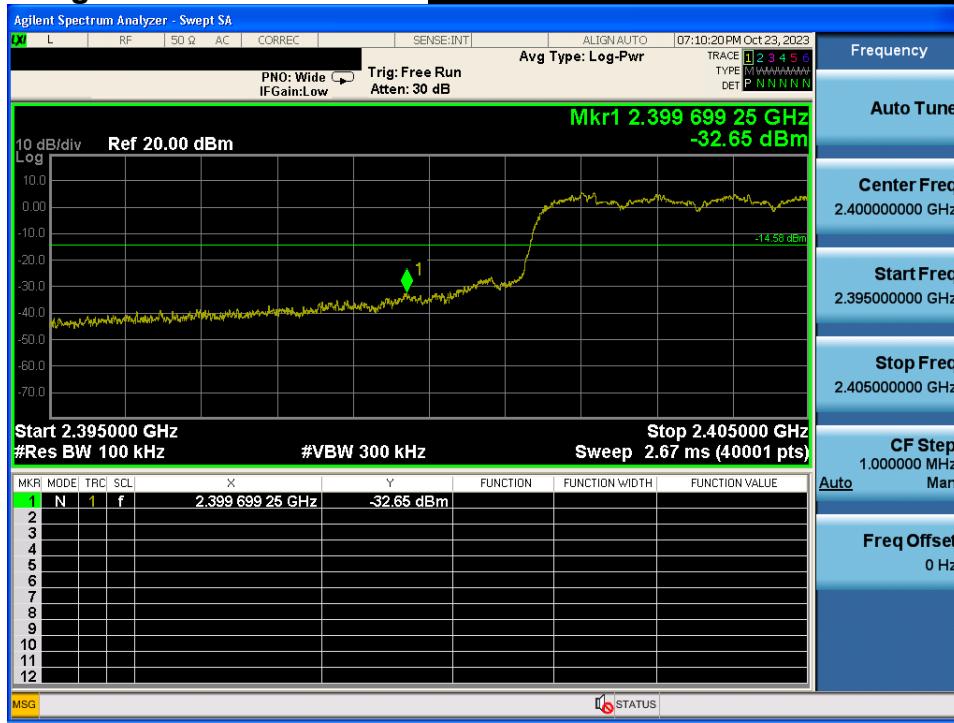
Conducted Spurious Emissions

Highest Channel & Modulation : π/4DQPSK



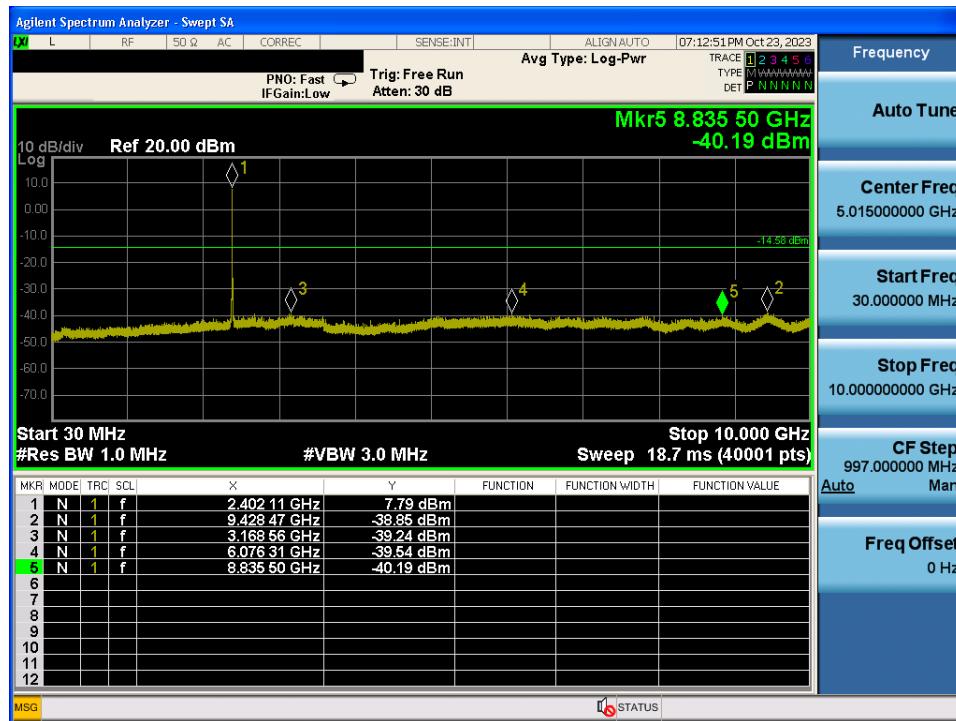
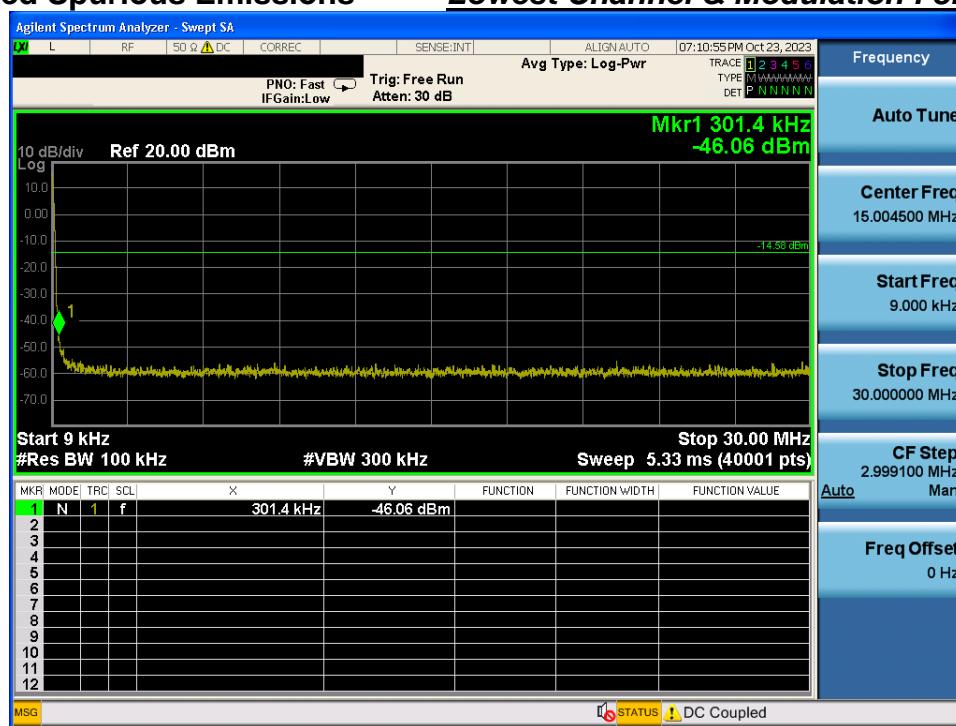
Conducted Spurious Emissions

Highest Channel & Modulation : π/4DQPSK


Low Band-edge
Lowest Channel & Modulation : 8DPSK

Low Band-edge
Hopping mode & Modulation : 8DPSK


Conducted Spurious Emissions

Lowest Channel & Modulation : 8DPSK



Conducted Spurious Emissions

Lowest Channel & Modulation : 8DPSK



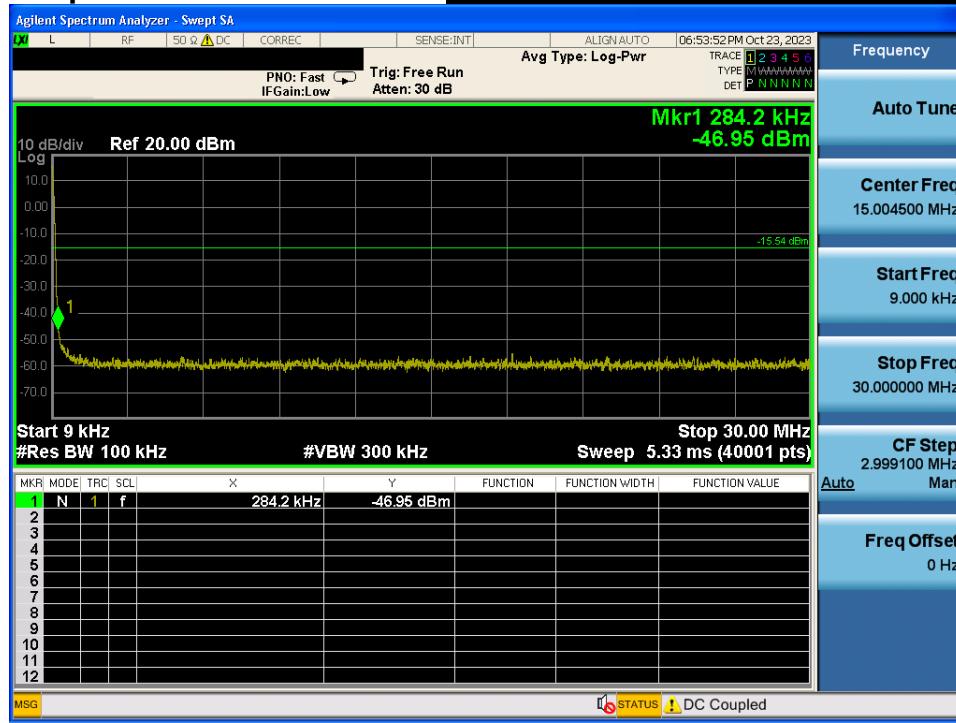
Reference for limit

Middle Channel & Modulation : 8DPSK



Conducted Spurious Emissions

Middle Channel & Modulation : 8DPSK



Conducted Spurious Emissions

Middle Channel & Modulation : 8DPSK

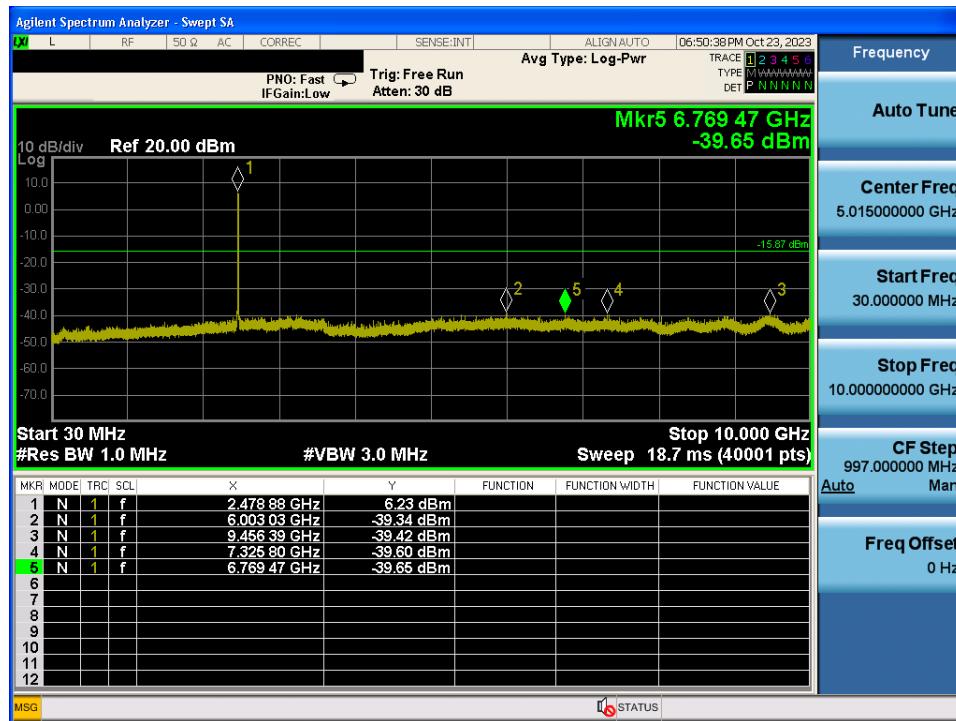
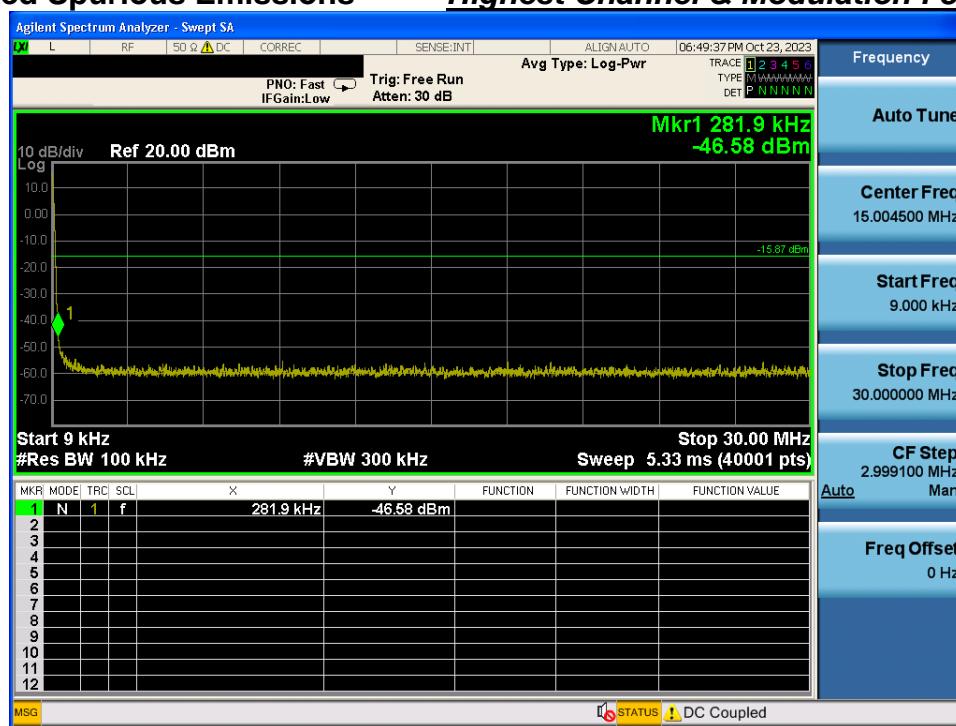


High Band-edge
Highest Channel & Modulation : 8DPSK

High Band-edge
Hopping mode & Modulation : 8DPSK


Conducted Spurious Emissions

Highest Channel & Modulation : 8DPSK



Conducted Spurious Emissions

Highest Channel & Modulation : 8DPSK



10. AC Power-Line Conducted Emissions

10.1. Test Setup

See test photographs for the actual connections between EUT and support equipment.

10.2. Limit

According to §15.207(a) for an intentional radiator that is designed to be connected to the public utility (AC) power line, the radio frequency voltage that is conducted back onto the AC power line on any frequency or frequencies, within the band 150 kHz to 30 MHz, shall not exceed the limits in the following table, as measured using a 50 uH/50 ohm line impedance stabilization network (LISN).

Compliance with the provision of this paragraph shall be on the measurement of the radio frequency voltage between each power line and ground at the power terminal. The lower applies at the boundary between the frequency ranges.

Frequency Range (MHz)	Conducted Limit (dBuV)	
	Quasi-Peak	Average
0.15 ~ 0.50	66 to 56 *	56 to 46 *
0.5 ~ 5.0	56	46
5 ~ 30	60	50

* Decreases with the logarithm of the frequency

10.3. Test Procedure

Conducted emissions from the EUT were measured according to the ANSI C63.10.

1. The test procedure is performed in a 6.5 m x 3.5 m x 3.5 m (L x W x H) shielded room. The EUT along with its peripherals were placed on a 1.0 m (W) x 1.5 m (L) and 0.8 m in height wooden table and the EUT was adjusted to maintain a 0.4 meter space from a vertical reference plane.
2. The EUT was connected to power mains through a line impedance stabilization network (LISN) which provides 50 ohm coupling impedance for measuring instrument and the chassis ground was bounded to the horizontal ground plane of shielded room.
3. All peripherals were connected to the second LISN and the chassis ground also bounded to the horizontal ground plane of shielded room.
4. The excess power cable between the EUT and the LISN was bundled. The power cables of peripherals were unbundled. All connecting cables of EUT and peripherals were moved to find the maximum emission.

10.4. Test Results

AC Power-Line Conducted Emissions (Graph) = Modulation : 8DPSK

Results of Conducted Emission

Date 2023-11-10

Order No. **ZVCI-02**
Model No. **ZVCI-02**
Serial No. **BT**
Test Condition

Reference No.
Power Supply **120V, 60Hz**
Temp/Humi. **21 °C / 30 %**
Operator **S.M.GIL**

Memo

LIMIT : FCC P15.207 AV
FCC P15.207 QP

Lisn Factor

1. -9999

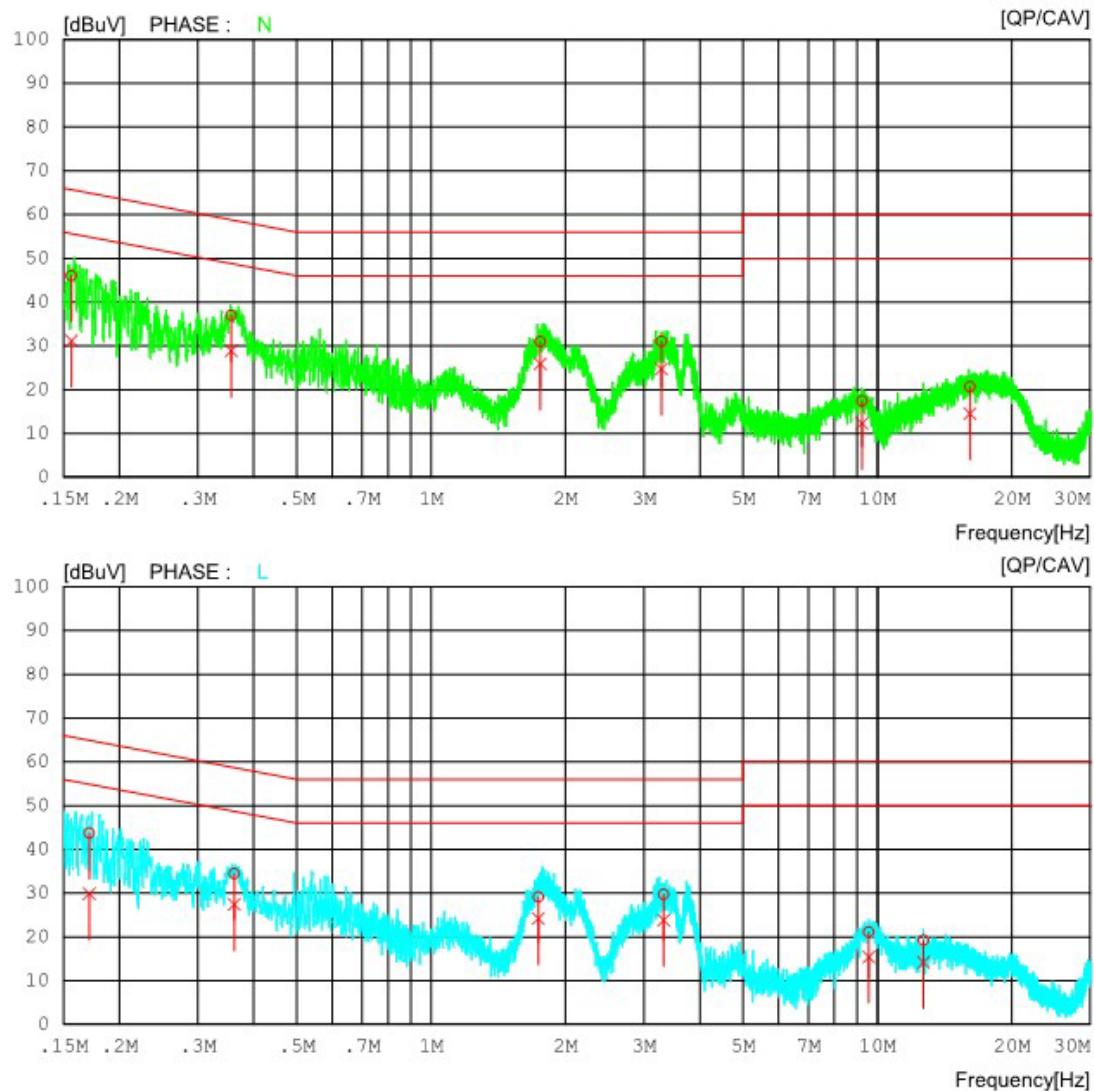
2. -9999

Cable Loss

1. C1_LISN TO RECEIVER_2023.02.14

Pulse Limiter

1. PULSE LIMITER_ESH3-Z2_101333_2023.08.21



AC Power-Line Conducted Emissions (List) = Modulation : 8DPSK**Results of Conducted Emission**

Date 2023-11-10

Order No.		Referrence No.
Model No.	ZVCI-02	Power Supply
Serial No.		Temp/Humi.
Test Condition	BT	Operator

Memo

LIMIT : FCC P15.207 AV
FCC P15.207 QP

Lisn Factor

1. -9999

2. -9999

Cable Loss

1. C1_LISN TO RECEIVER_2023.02.14

Pulse Lmitter

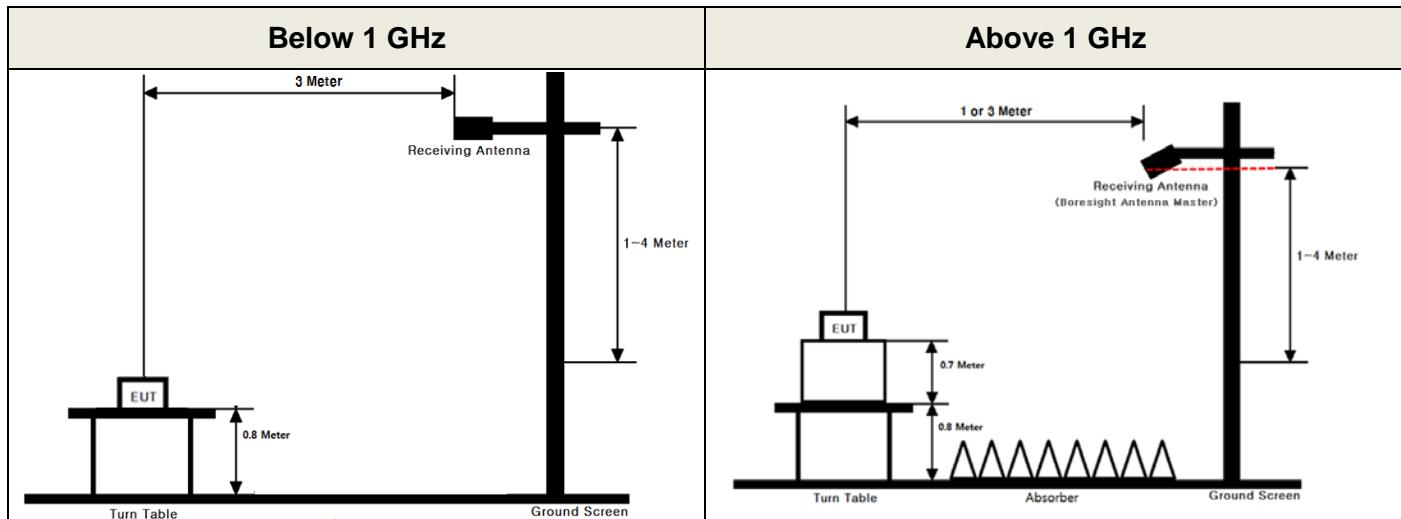
1. PULSE LIMITER_ESH3-Z2_101333_2023.08.21

NO	FREQ	READING		C.FACTOR	RESULT		LIMIT		MARGIN	PHASE
		QP	CAV		QP	CAV	QP	CAV		
	[MHz]	[dBuV]	[dBuV]	[dB]	[dBuV]	[dBuV]	[dBuV]	[dBuV]	[dBuV]	[dBuV]
1	0.15620	36.14	21.25	9.91	46.05	31.16	65.66	55.66	19.61	24.50
2	0.35644	27.04	18.99	9.89	36.93	28.88	58.81	48.81	21.88	19.93
3	1.75580	21.00	15.93	9.94	30.94	25.87	56.00	46.00	25.06	20.13
4	3.28240	21.07	14.75	9.96	31.03	24.71	56.00	46.00	24.97	21.29
5	9.24220	7.39	2.21	10.09	17.48	12.30	60.00	50.00	42.52	37.70
6	16.12660	10.46	4.31	10.20	20.66	14.51	60.00	50.00	39.34	35.49
7	0.17126	33.83	19.96	9.90	43.73	29.86	64.90	54.90	21.17	25.04
8	0.36189	24.58	17.54	9.89	34.47	27.43	58.69	48.69	24.22	21.26
9	1.73820	19.22	14.29	9.93	29.15	24.22	56.00	46.00	26.85	21.78
10	3.32000	19.77	13.89	9.96	29.73	23.85	56.00	46.00	26.27	22.15
11	9.56500	11.02	5.34	10.10	21.12	15.44	60.00	50.00	38.88	34.56
12	12.67220	9.15	4.03	10.15	19.30	14.18	60.00	50.00	40.70	35.82

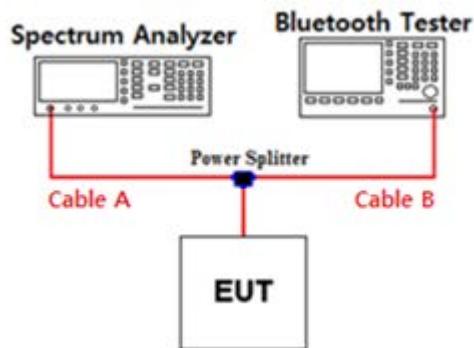
APPENDIX I

Test set up diagrams

▪ Radiated Measurement



▪ Conducted Measurement



Path loss information

Frequency (GHz)	Path Loss (dB)	Frequency (GHz)	Path Loss (dB)
0.03	6.14	15	11.13
1	7.00	20	12.32
2.402 & 2.441 & 2.480	7.92	25	13.25
5	9.05	-	-
10	10.03	-	-

Note 1: The path loss from EUT to Spectrum analyzer was measured and used for test.

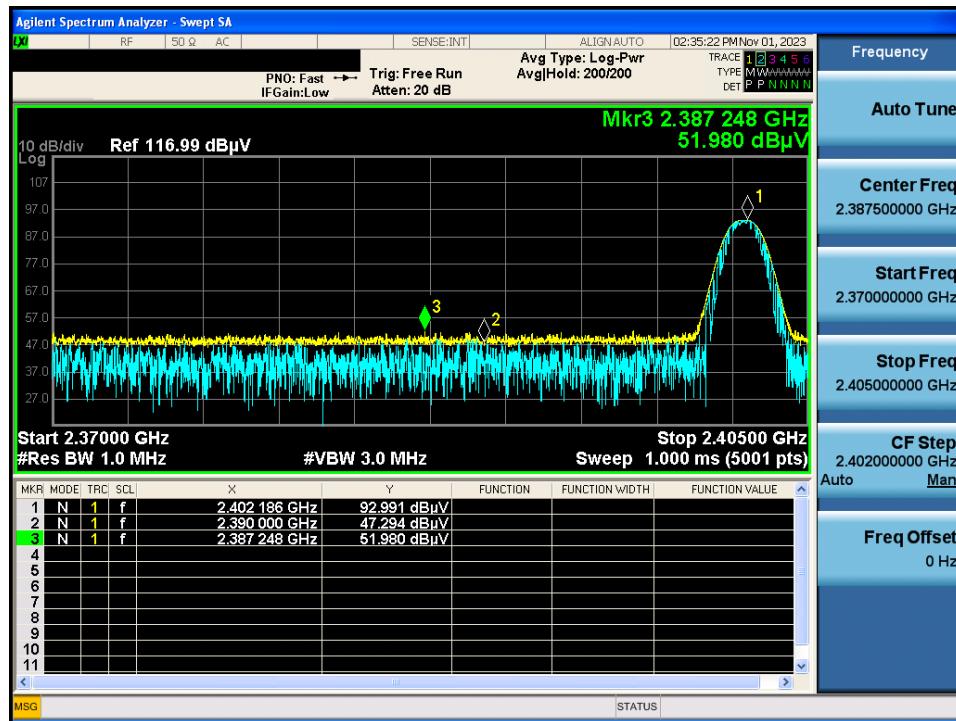
Path loss (S/A's correction factor) = Cable A + Power Splitter

APPENDIX II

Unwanted Emissions (Radiated) Test Plot

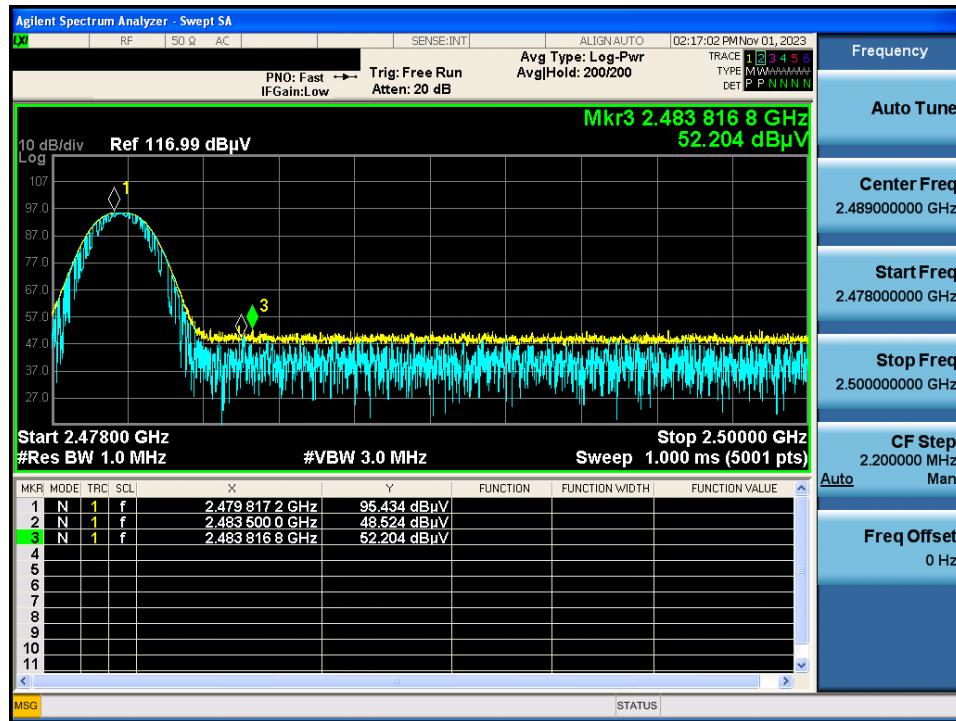
GFSK & Lowest & X & Hor

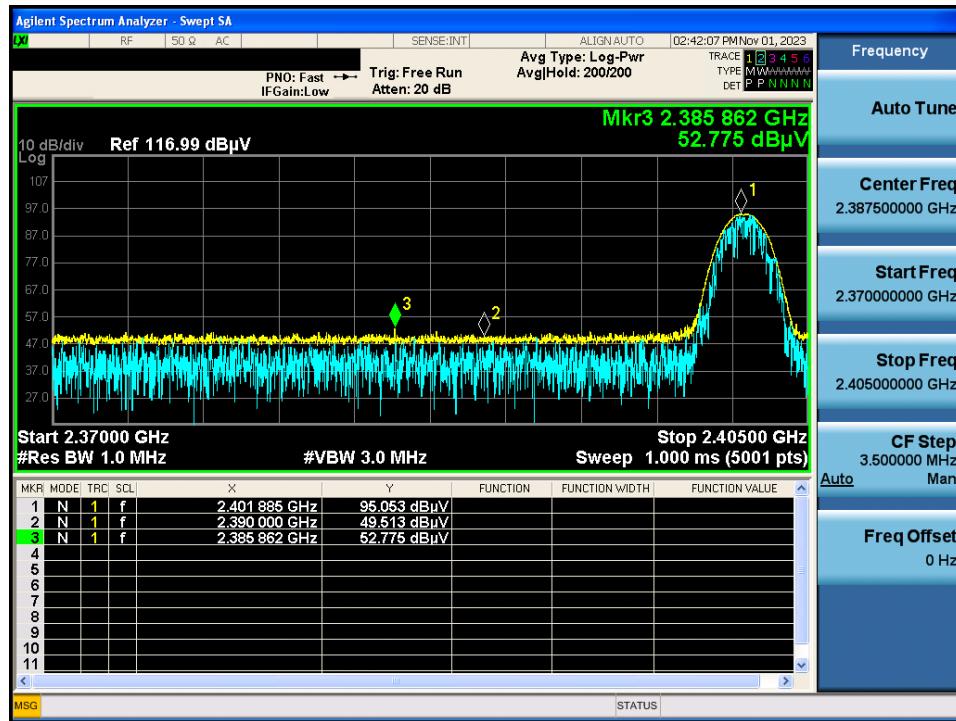
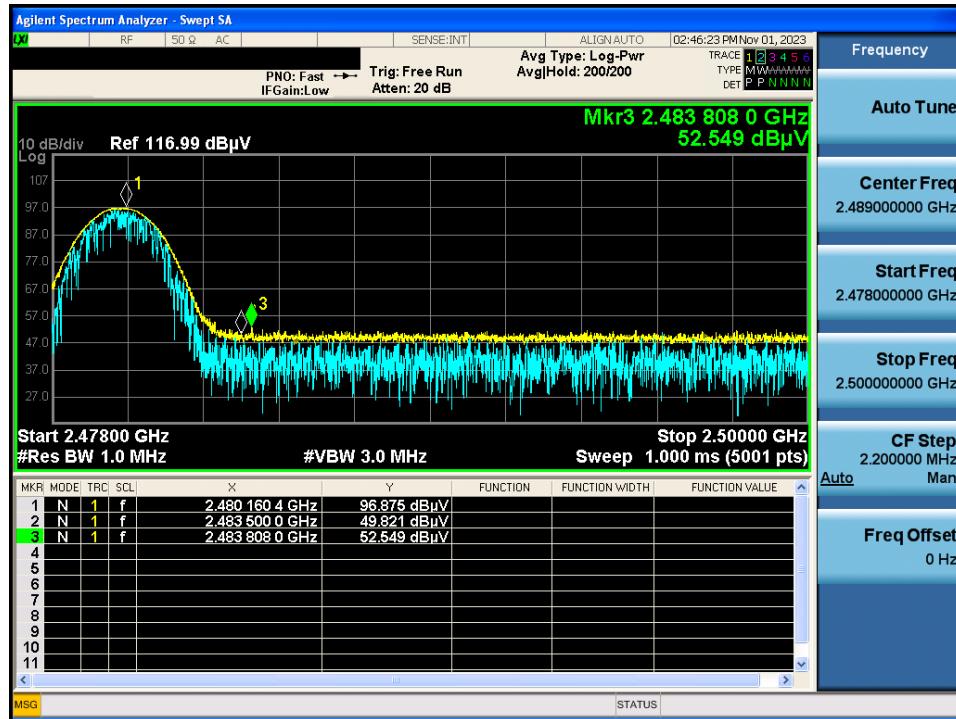
Detector Mode : PK



GFSK & Highest & X & Hor

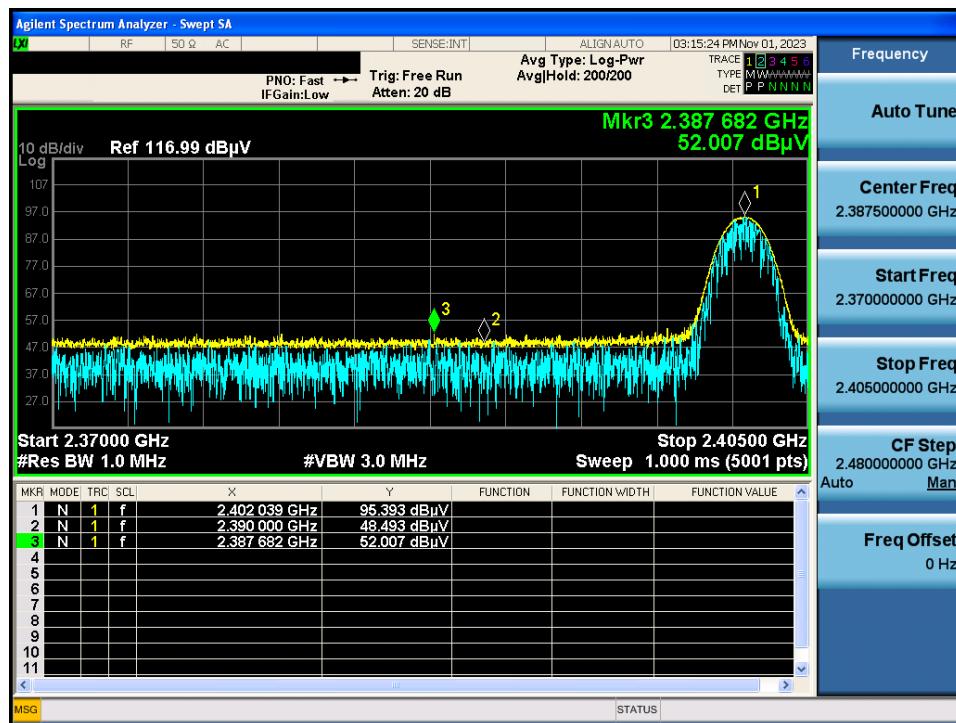
Detector Mode : PK



π/4DQPSK & Lowest & X & Hor
Detector Mode : PK

π/4DQPSK & Highest & X & Hor
Detector Mode : PK


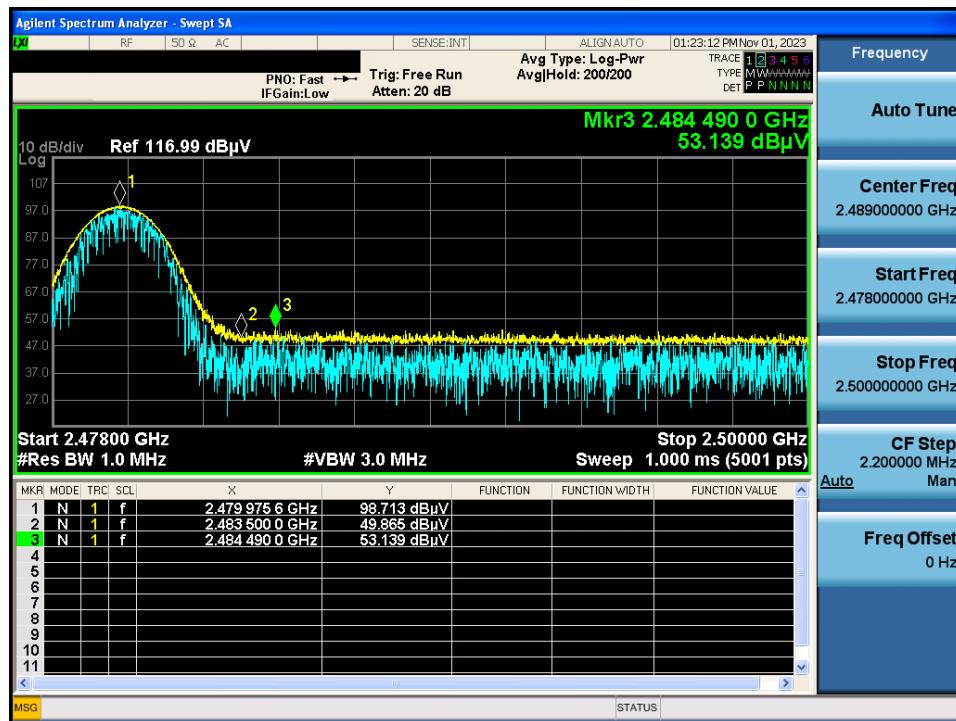
8DPSK & Lowest & X & Hor

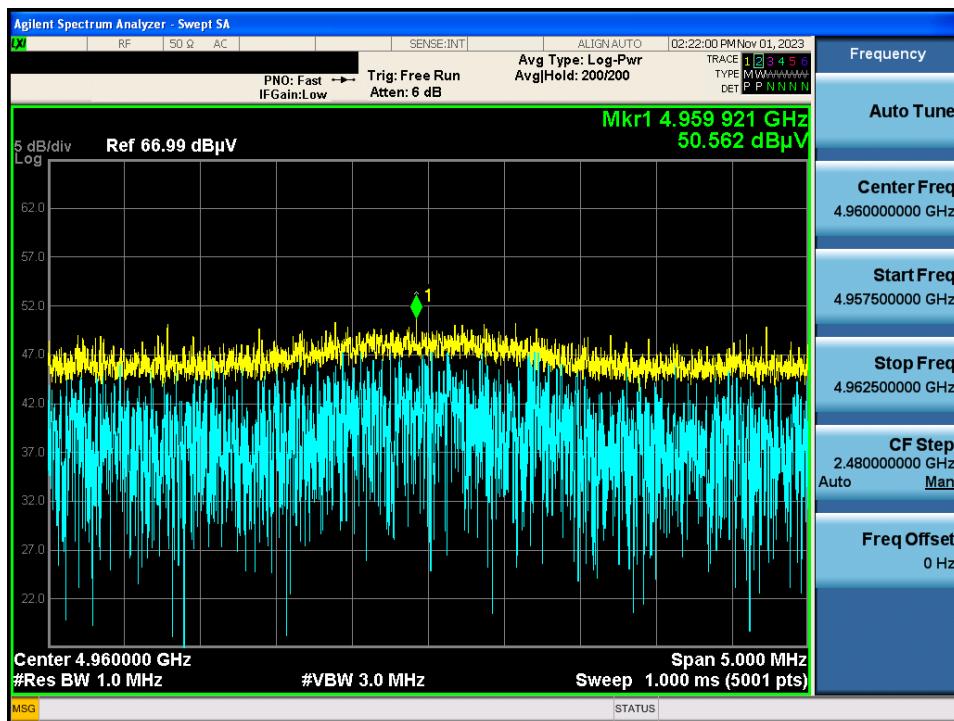
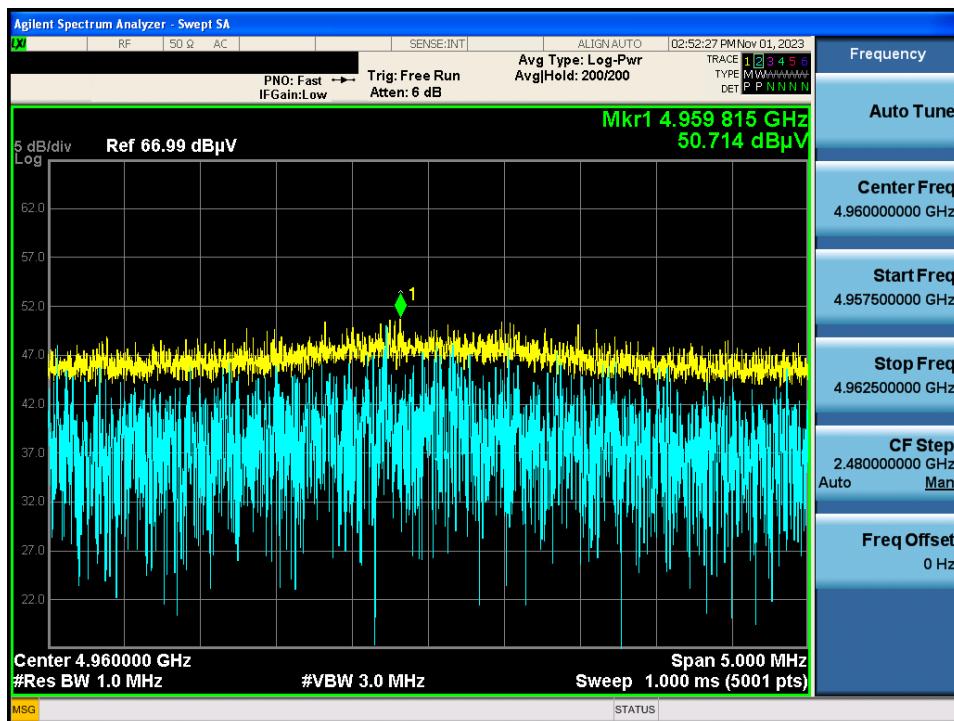
Detector Mode : PK



8DPSK & Highest & X & Hor

Detector Mode : PK



GFSK & Highest & Z & Hor
Detector Mode : PK

π/4DQPSK & Highest & Z & Hor
Detector Mode : PK


8DPSK & Highest & Z & Hor**Detector Mode : PK**