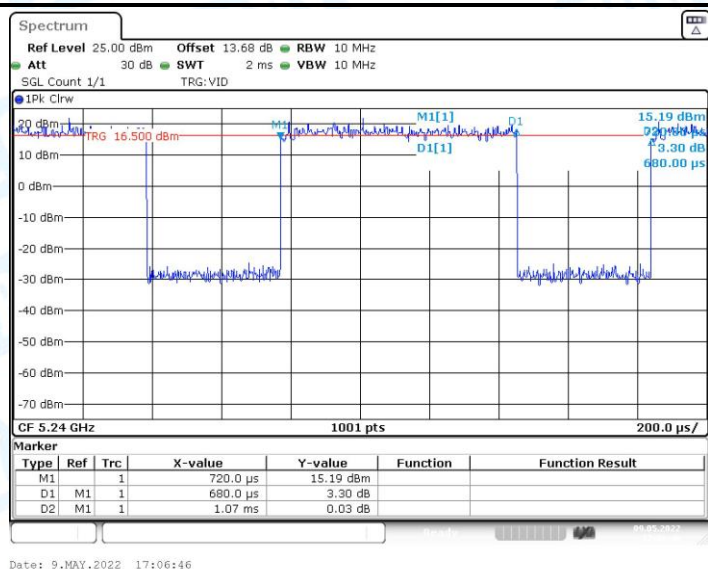
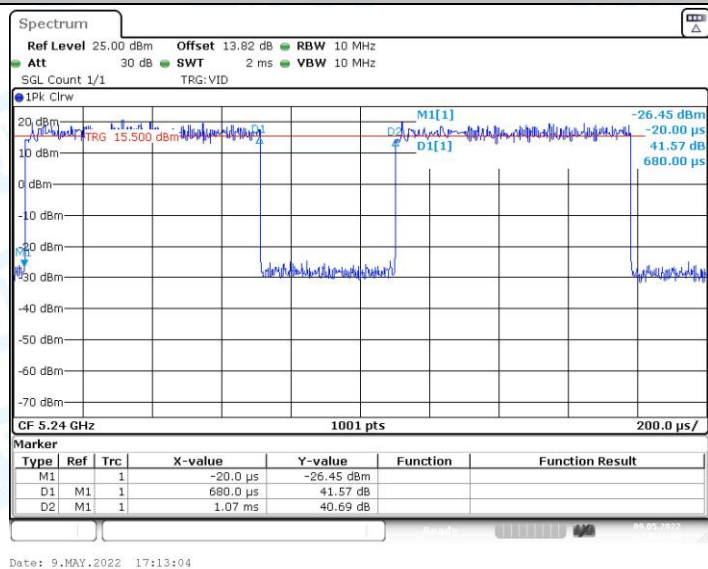
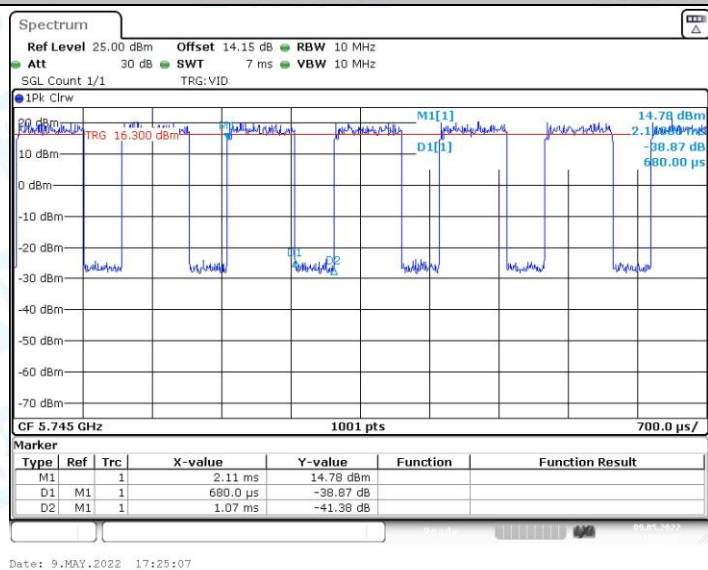




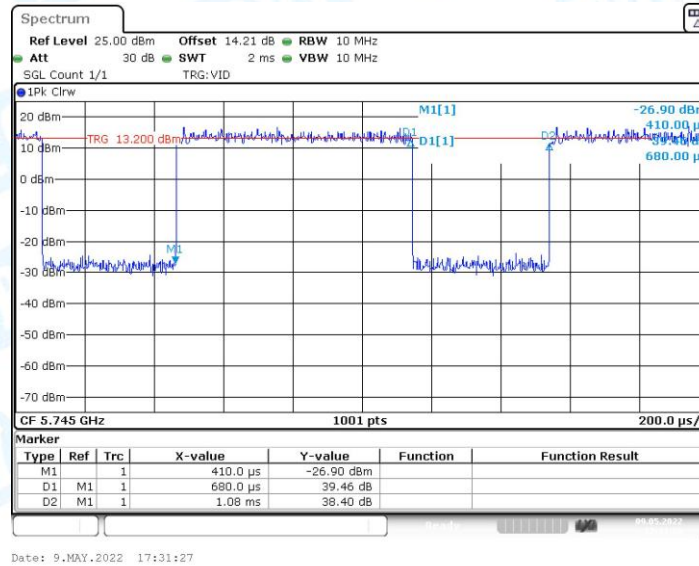
### 11AC20MIMO\_Ant0\_5240



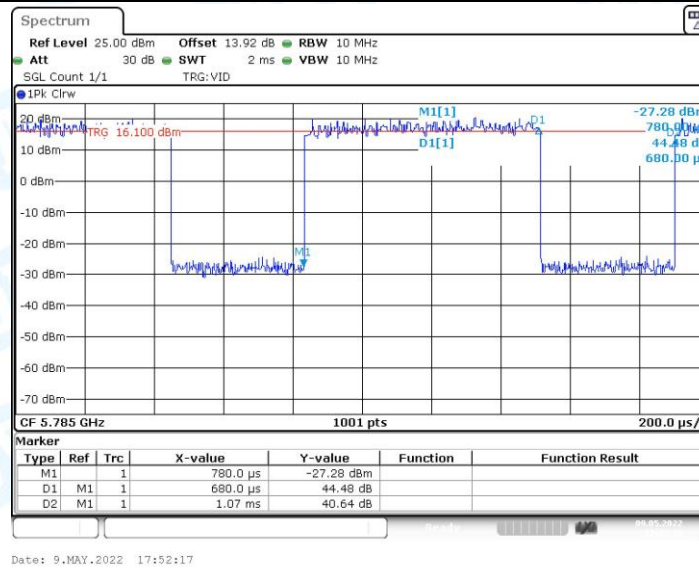
### 11AC20MIMO\_Ant1\_5240



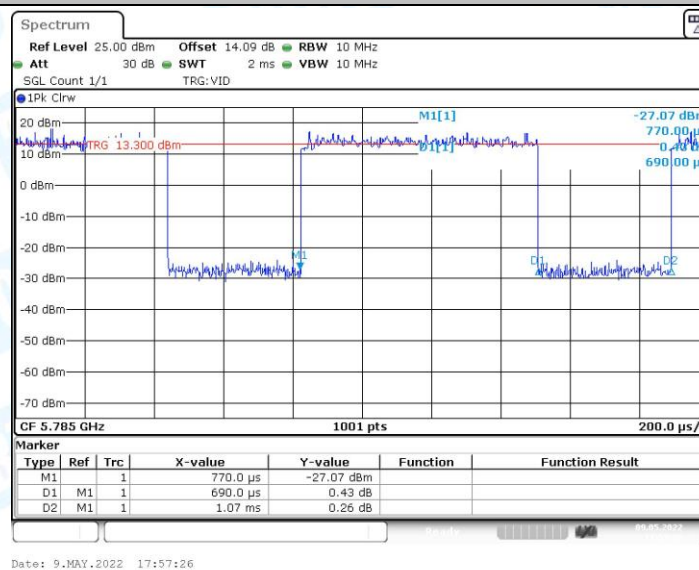
### 11AC20MIMO\_Ant0\_5745



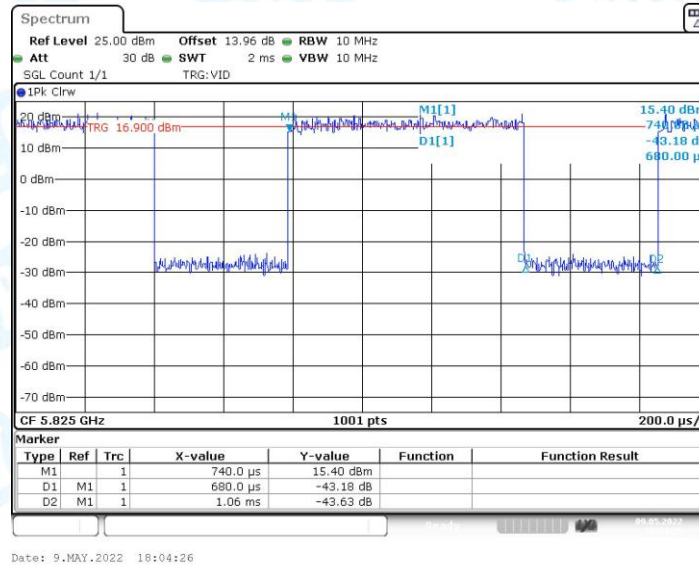
11AC20MIMO\_Ant1\_5745



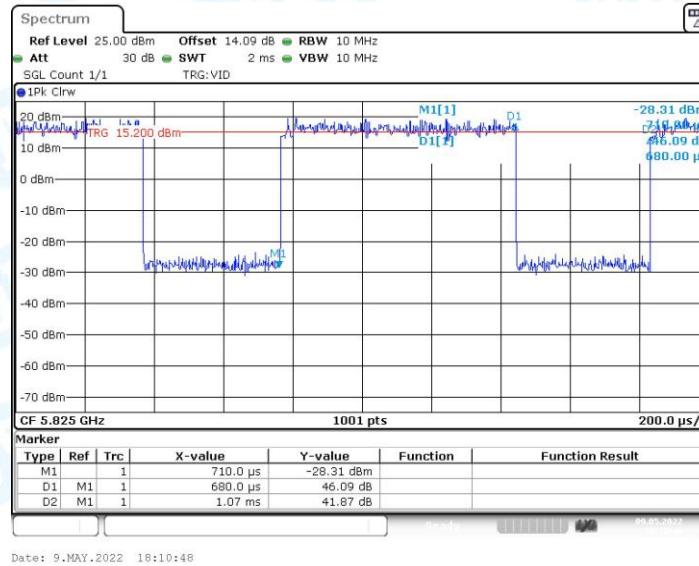
11AC20MIMO\_Ant0\_5785



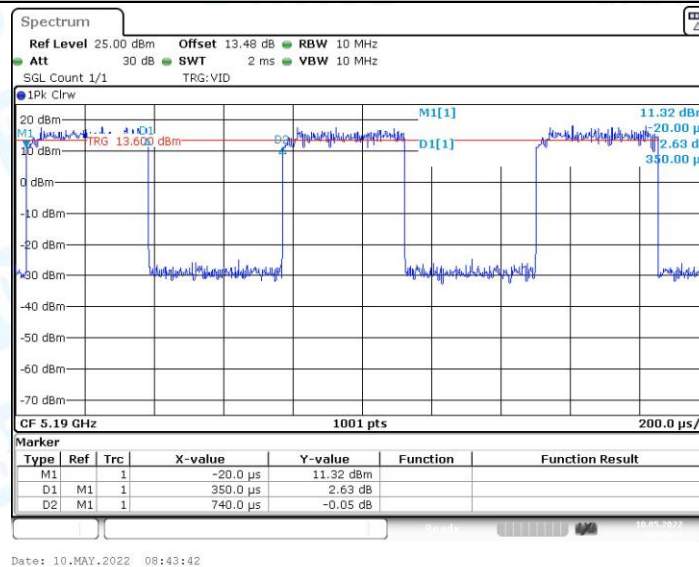
11AC20MIMO\_Ant1\_5785



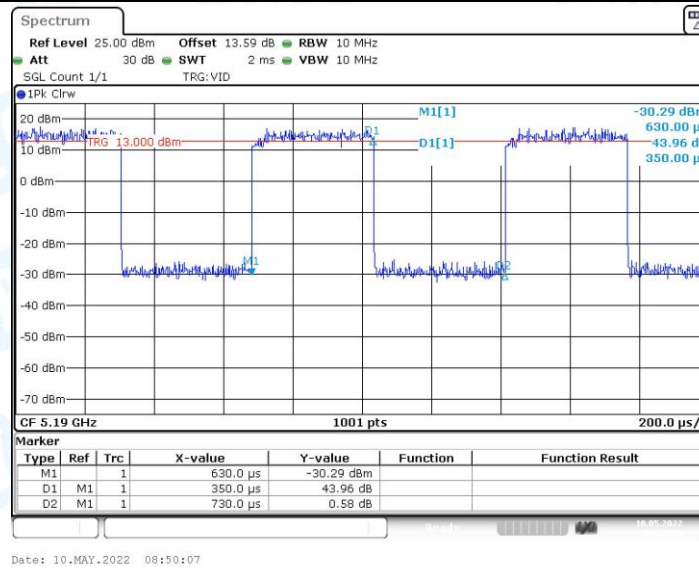
## 11AC20MIMO\_Ant0\_5825



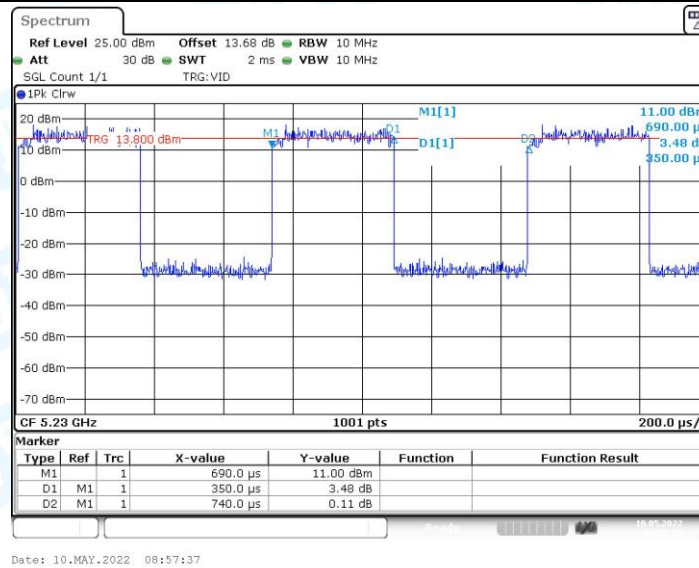
## 11AC20MIMO\_Ant1\_5825



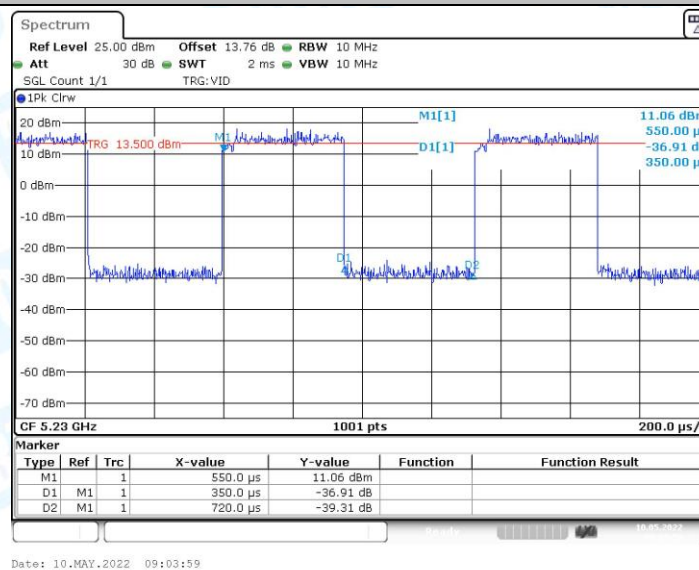
## 11AC40MIMO\_Ant0\_5190



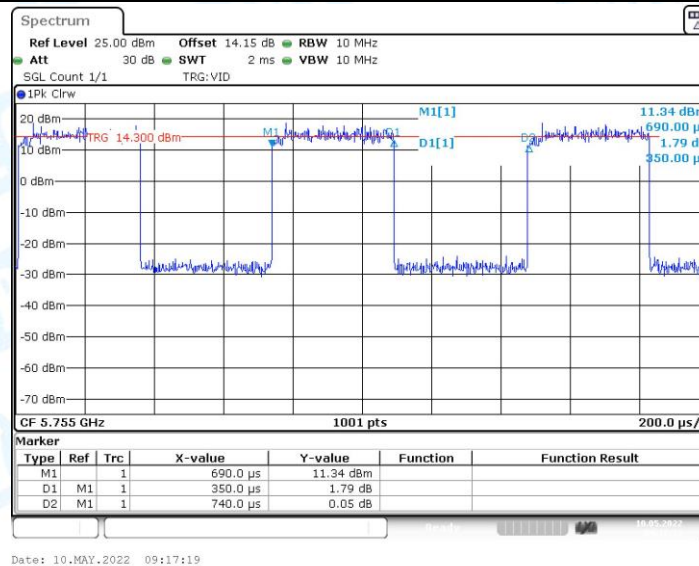
## 11AC40MIMO\_Ant1\_5190



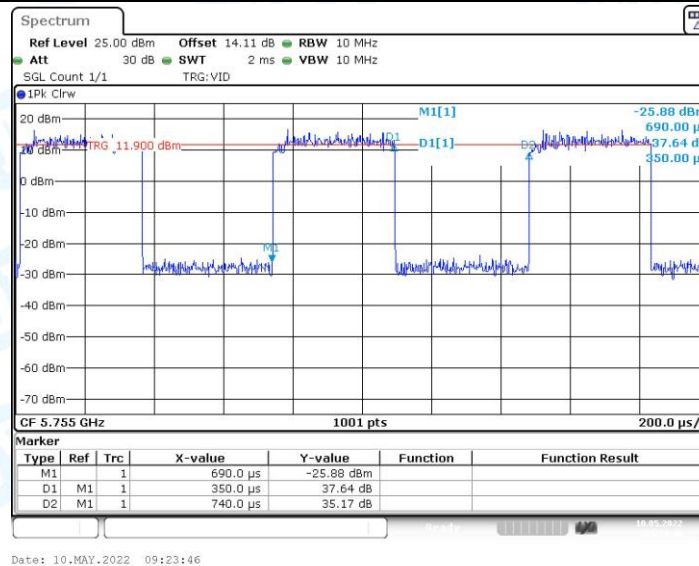
## 11AC40MIMO\_Ant0\_5230



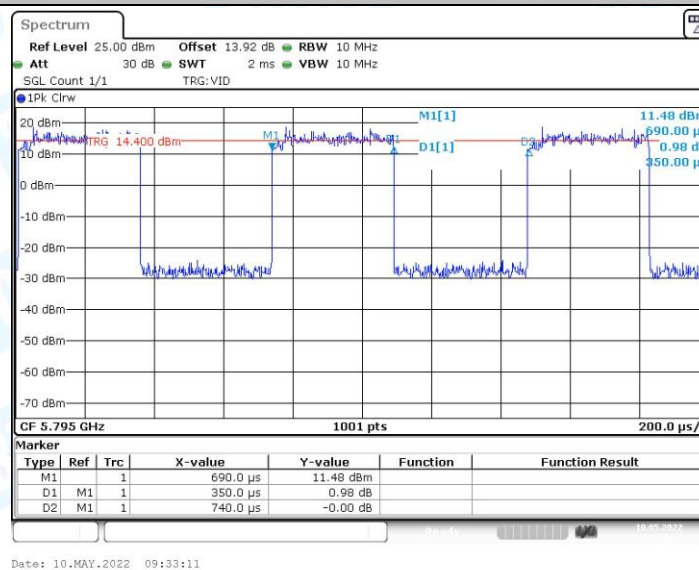
## 11AC40MIMO\_Ant1\_5230



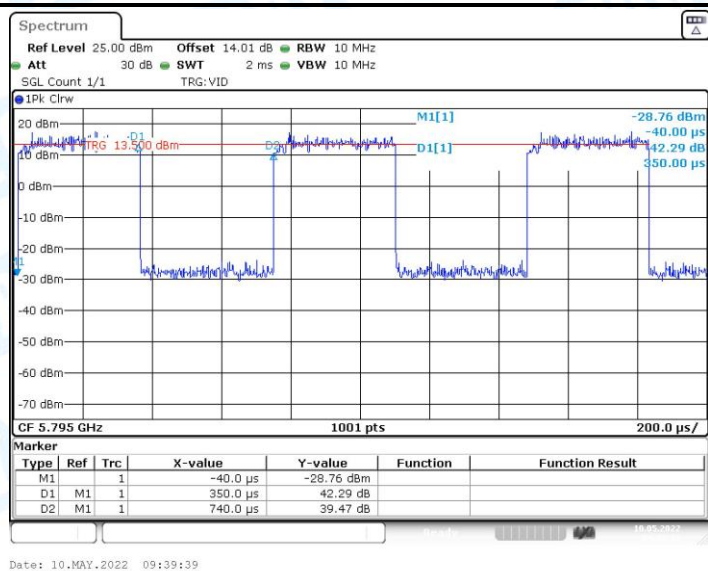
## 11AC40MIMO\_Ant0\_5755



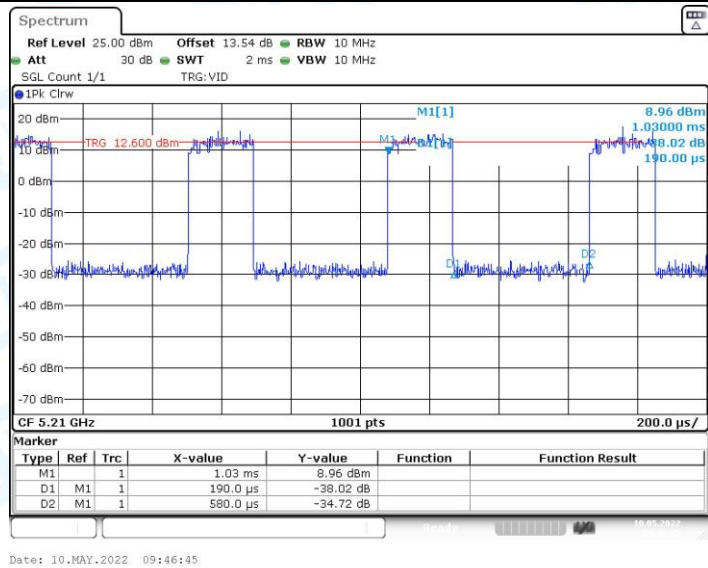
## 11AC40MIMO\_Ant1\_5755



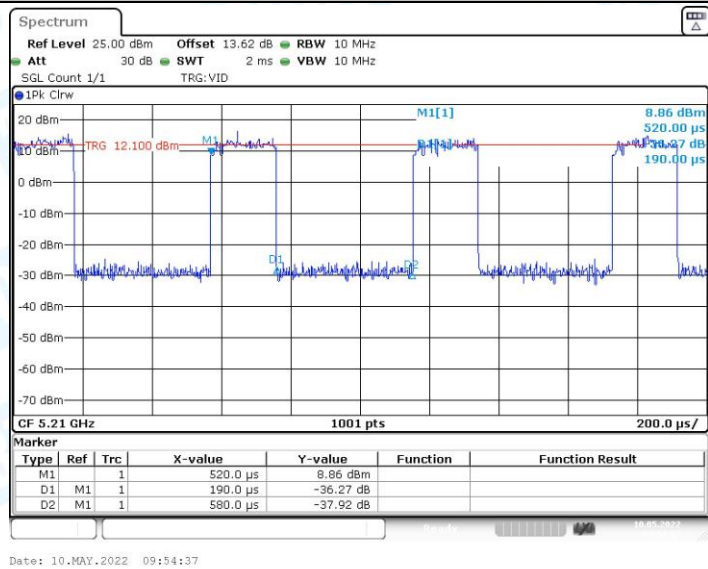
## 11AC40MIMO\_Ant0\_5795



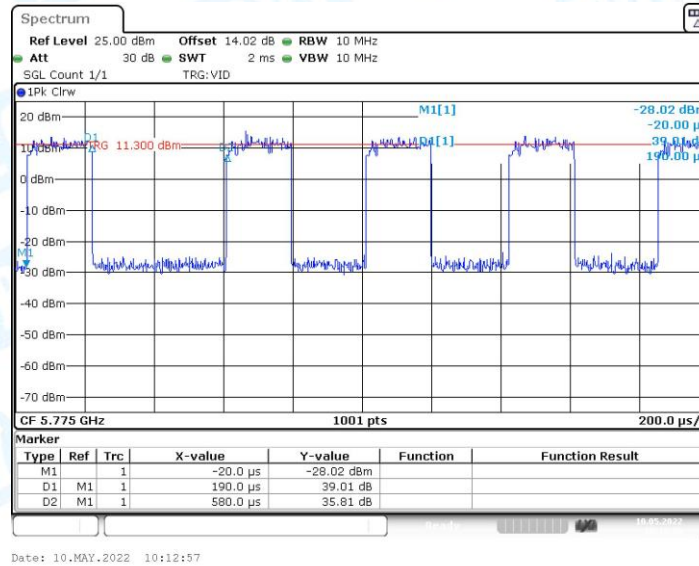
### 11AC40MIMO\_Ant1\_5795



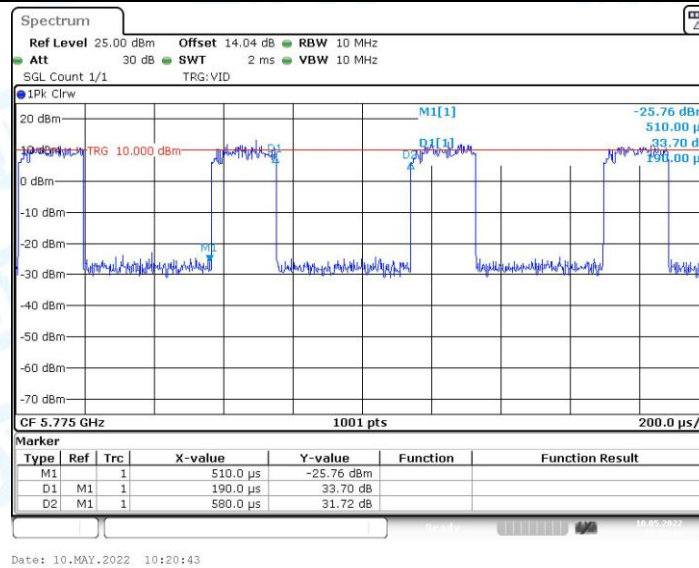
### 11AC80MIMO\_Ant0\_5210



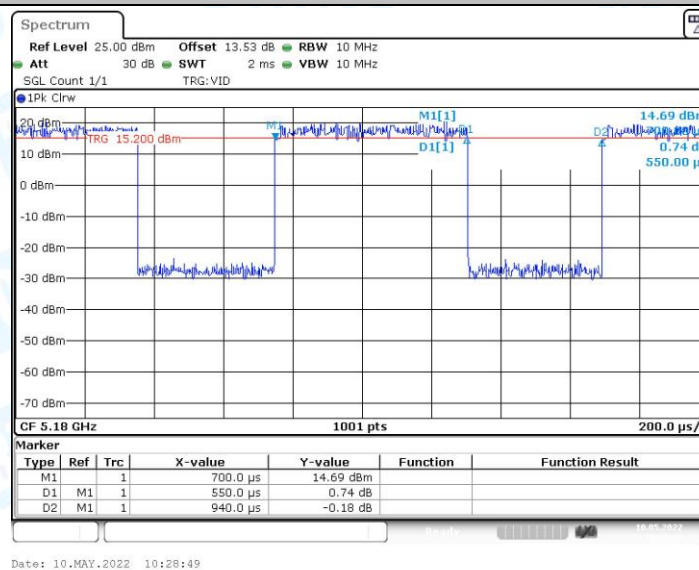
### 11AC80MIMO\_Ant1\_5210



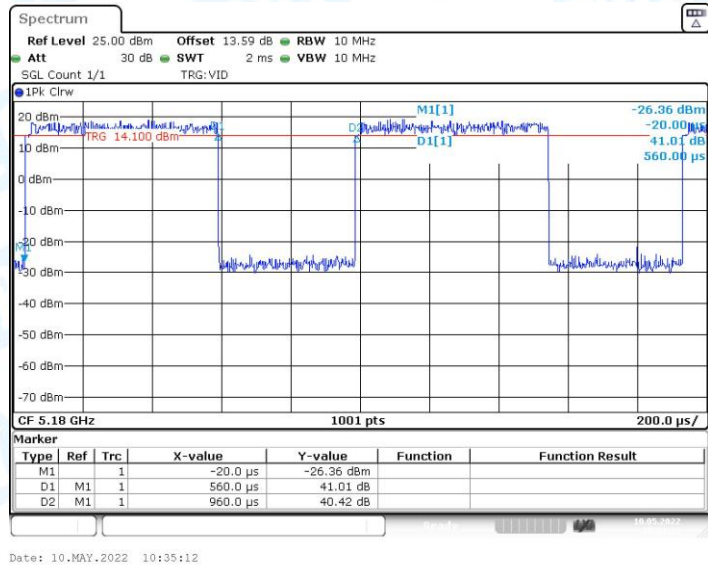
## 11AC80MIMO\_Ant0\_5775



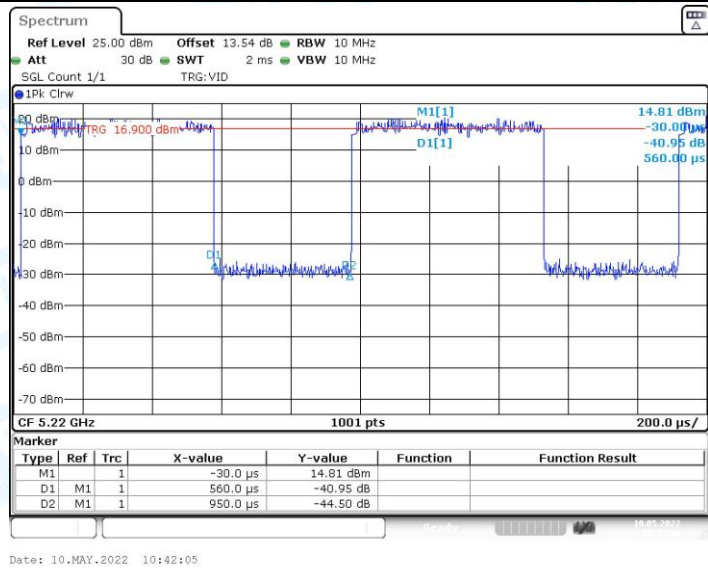
## 11AC80MIMO\_Ant1\_5775



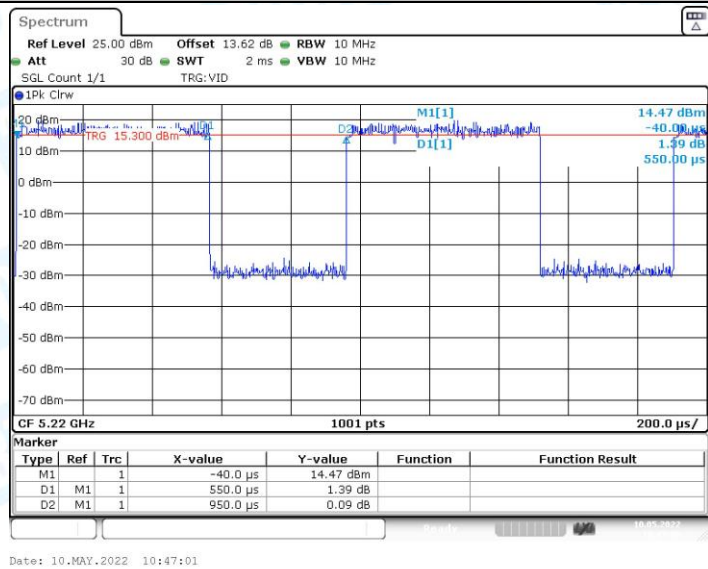
## 11AX20MIMO\_Ant0\_5180



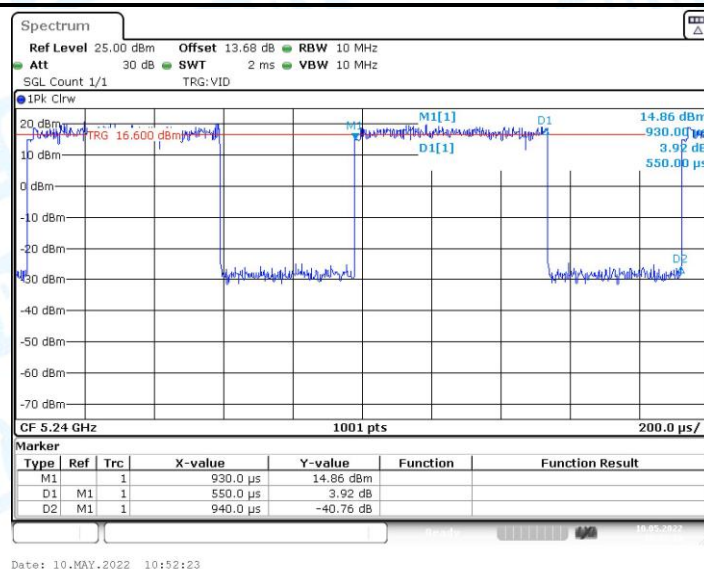
### 11AX20MIMO\_Ant1\_5180



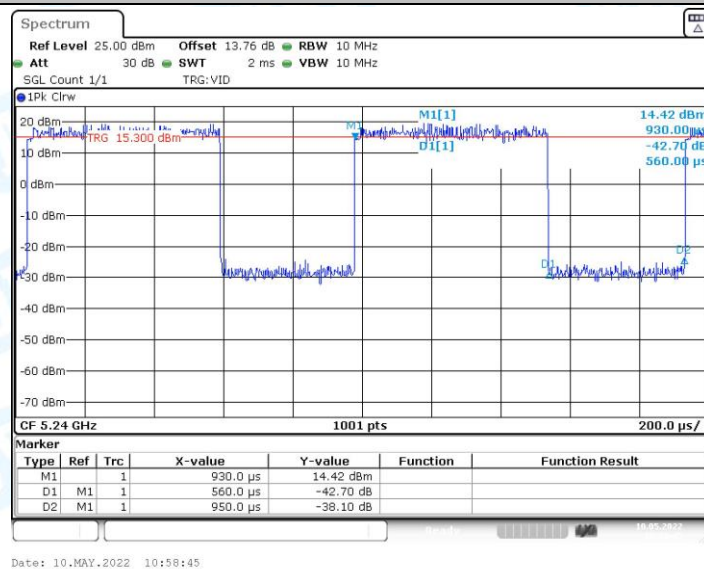
### 11AX20MIMO\_Ant0\_5220



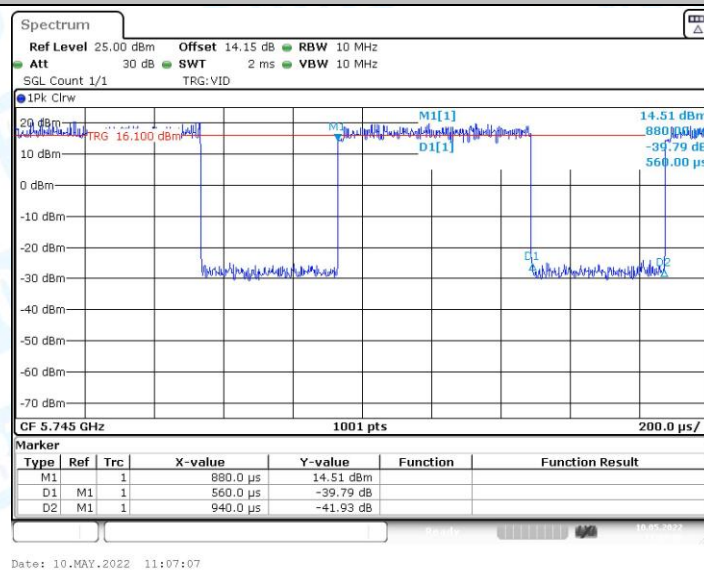
### 11AX20MIMO\_Ant1\_5220



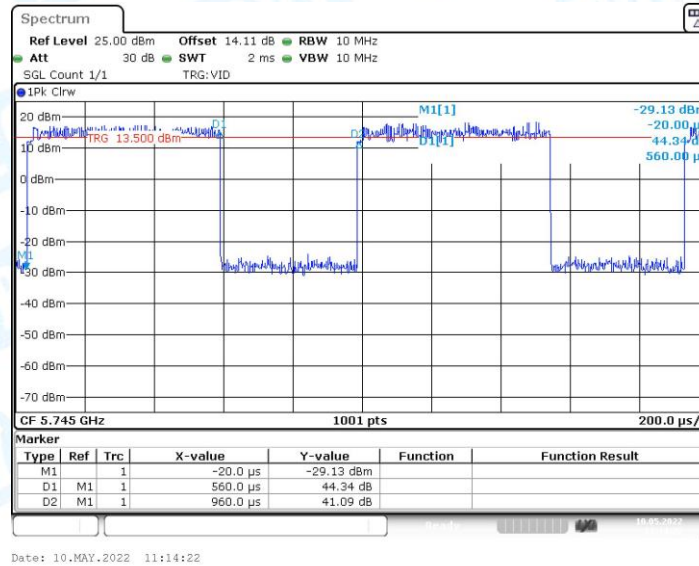
### 11AX20MIMO\_Ant0\_5240



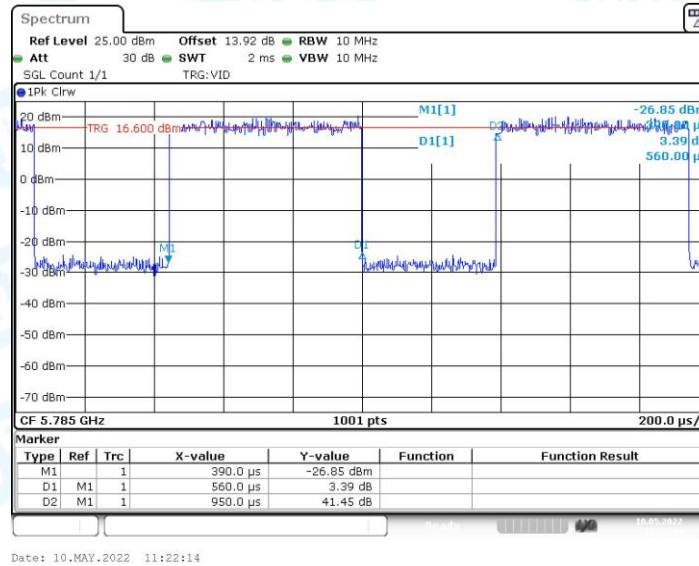
### 11AX20MIMO\_Ant1\_5240



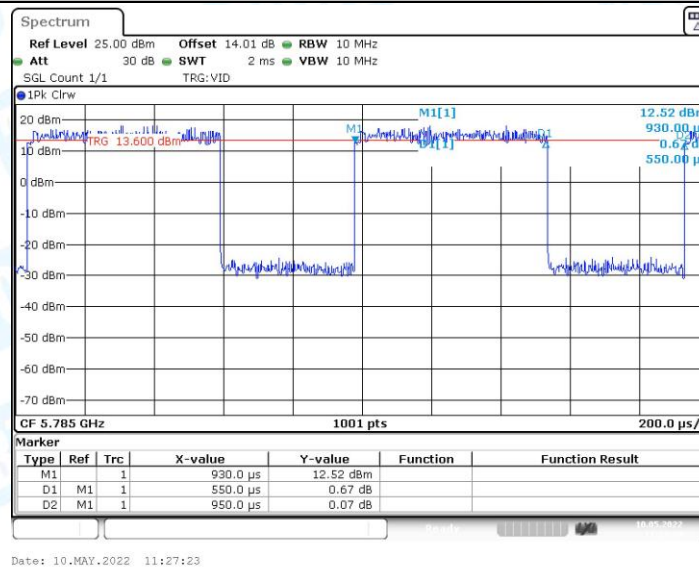
### 11AX20MIMO\_Ant0\_5745



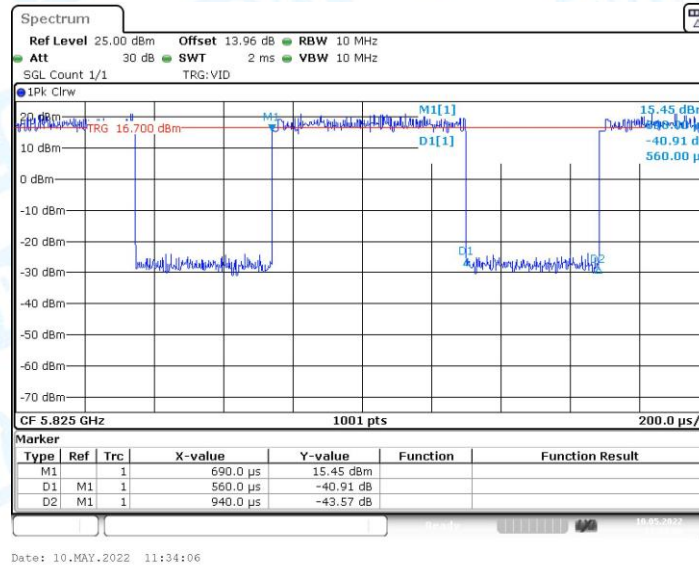
## 11AX20MIMO\_Ant1\_5745



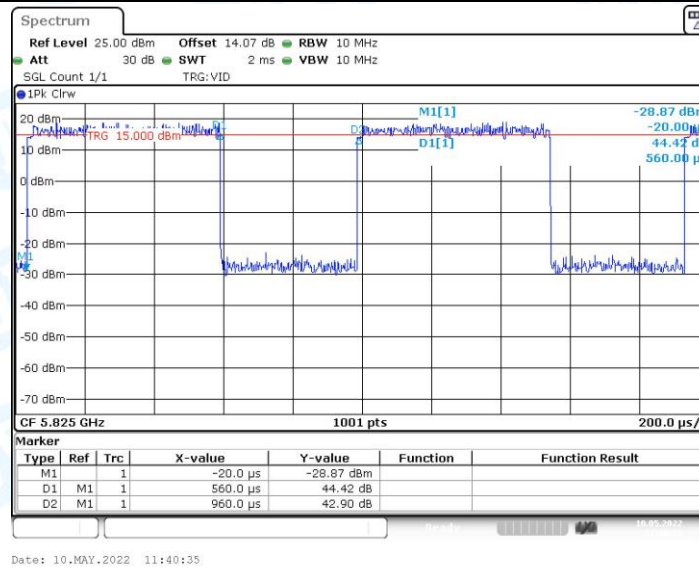
## 11AX20MIMO\_Ant0\_5785



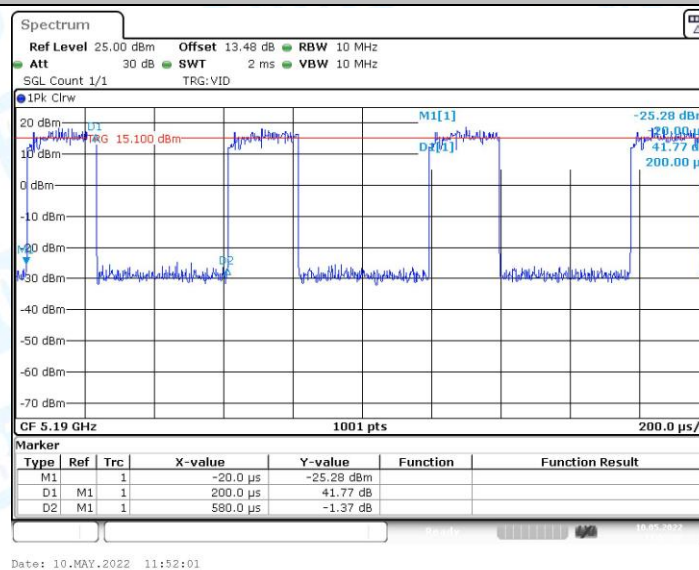
## 11AX20MIMO\_Ant1\_5785



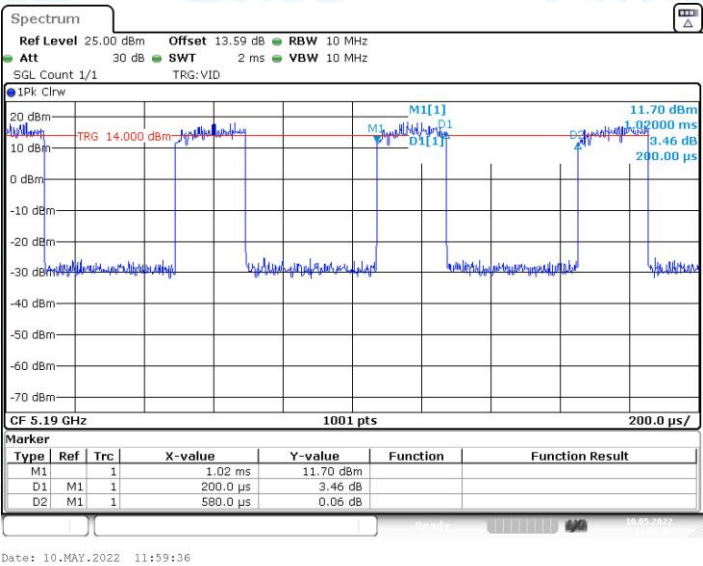
## 11AX20MIMO\_Ant0\_5825



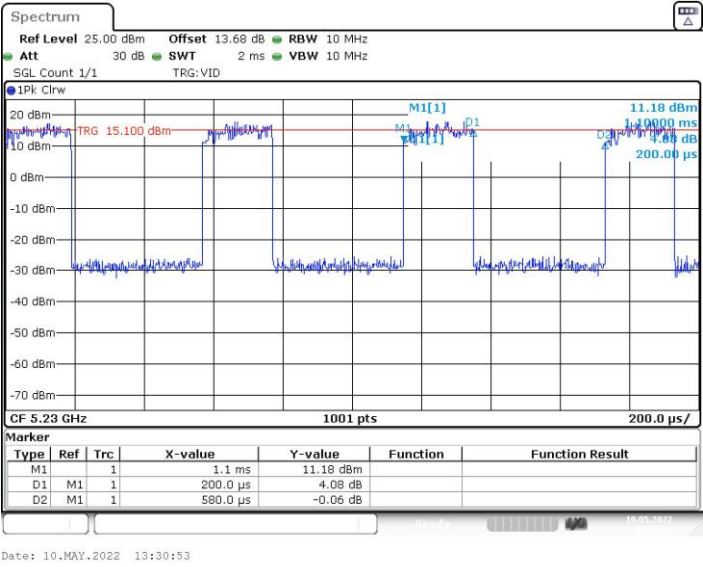
## 11AX20MIMO\_Ant1\_5825



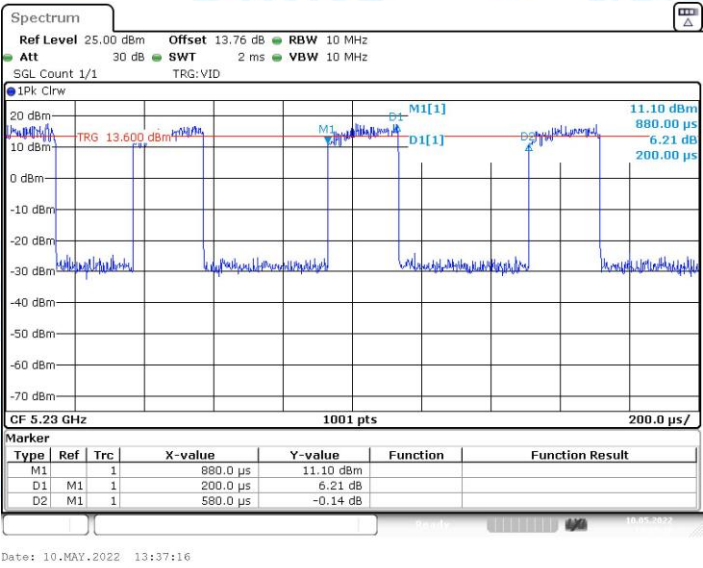
## 11AX40MIMO\_Ant0\_5190



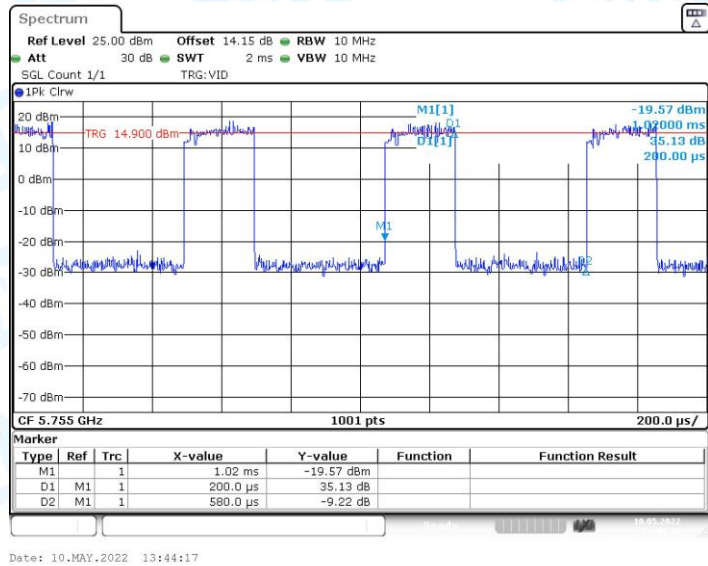
11AX40MIMO\_Ant1\_5190



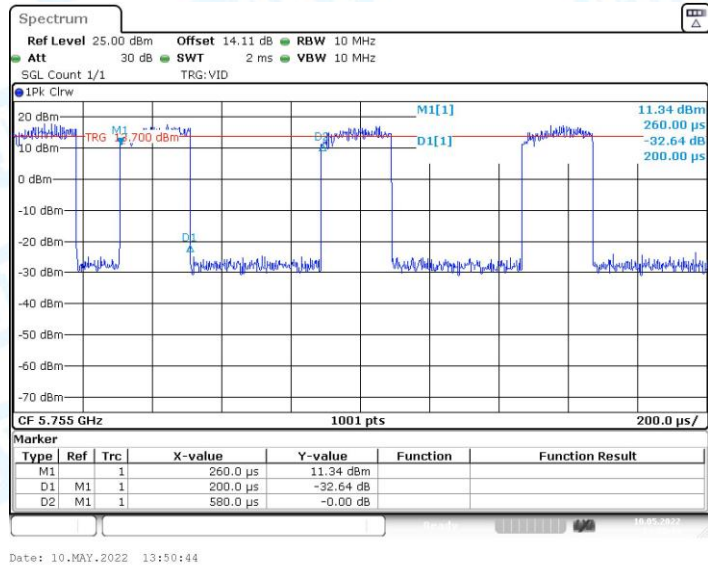
11AX40MIMO\_Ant0\_5230



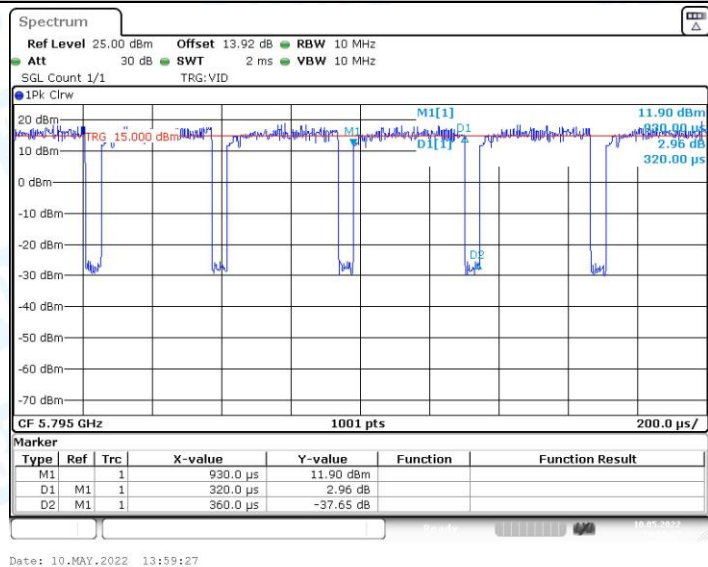
11AX40MIMO\_Ant1\_5230



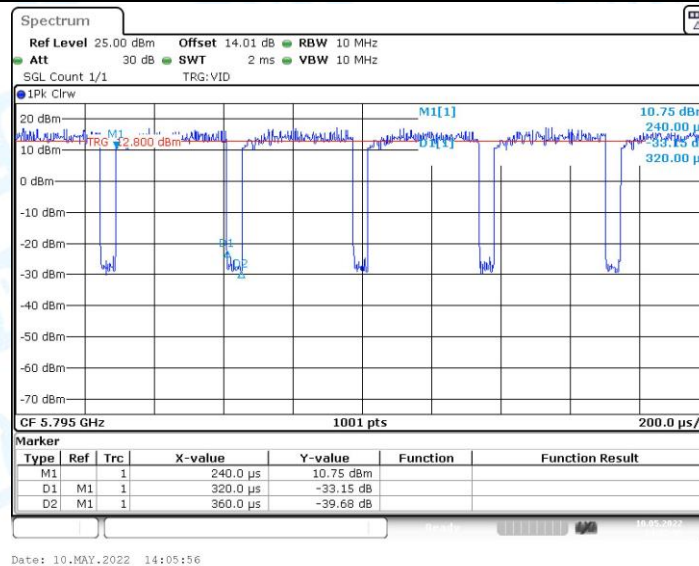
11AX40MIMO\_Ant0\_5755



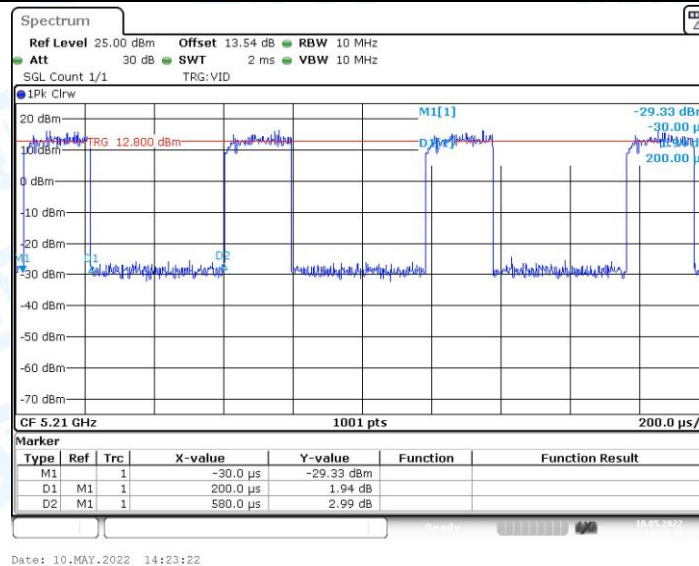
11AX40MIMO\_Ant1\_5755



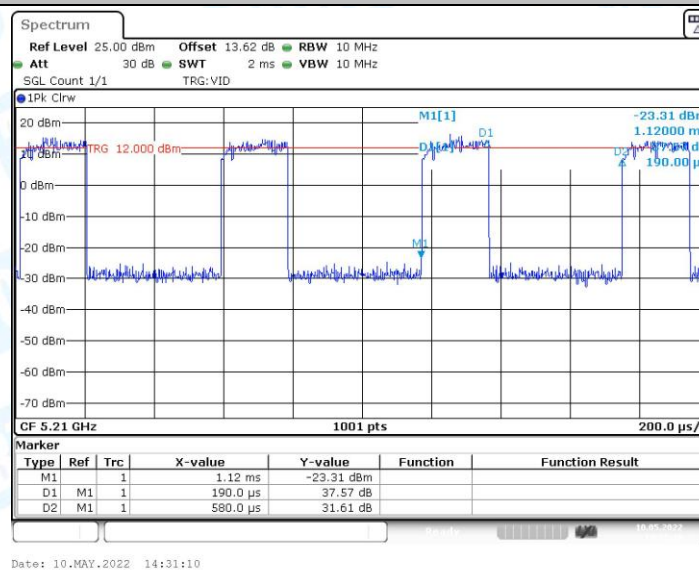
11AX40MIMO\_Ant0\_5795



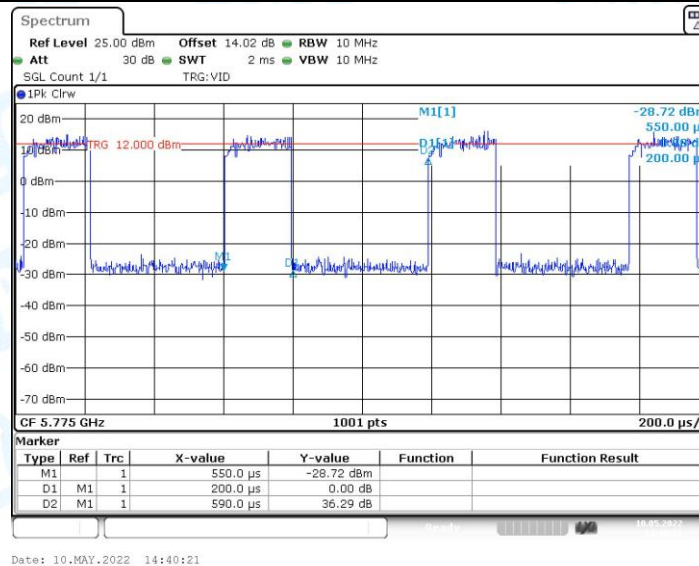
11AX40MIMO\_Ant1\_5795



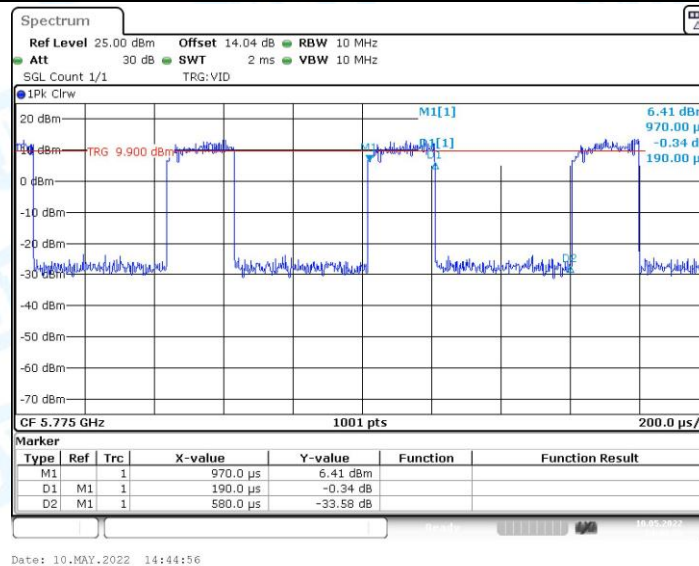
11AX80MIMO\_Ant0\_5210



11AX80MIMO\_Ant1\_5210



### 11AX80MIMO\_Ant0\_5775



### 11AX80MIMO\_Ant1\_5775

-----End of the report-----